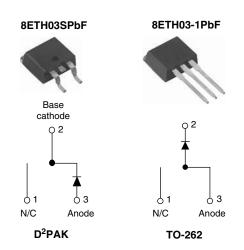




Vishay High Power Products

Hyperfast Rectifier, 8 A FRED PtTM



PRODUCT SUMMARY				
t _{rr}	35 ns			
I _{F(AV)}	8 A			
V_{R}	300 V			

FEATURES

- · Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for Q101 level



DESCRIPTION/APPLICATIONS

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Repetitive peak reverse voltage	V_{RRM}		300	V	
Average rectified forward current	I _{F(AV)}	T _C = 155 °C	8	Δ.	
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	100	A	
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	300	-	-	
Forward voltage V _F	I _F = 8 A	-	1.0	1.25	V	
	I _F = 8 A, T _J = 125 °C	-	0.83	1.00		
Deverage legisers gurrent		$V_R = V_R$ rated	-	0.02	20	
Reverse leakage current I _R	I _R	$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	6.0	200	μΑ
Junction capacitance	C _T	V _R = 300 V	-	31	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8 -		-	nH	

Document Number: 94025 Revision: 21-May-08

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

8ETH03SPbF/8ETH03-1PbF

Vishay High Power Products

Hyperfast Rectifier, 8 A FRED PtTM



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t_{rr}		$I_F = 1 \text{ A}, dI_F/dt = -50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
	t _{rr}	T _J = 25 °C		-	27	-	ns A
		T _J = 125 °C	$I_F = 8 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	=	40	-	
Peak recovery current	1	T _J = 25 °C		-	2.2	-	
	I _{RRM}	T _J = 125 °C		=	5.3	-	
Reverse recovery charge	0	T _J = 25 °C		=	30	-	nC
	Q _{rr}	T _J = 125 °C		-	106	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}	R _{thJC}		1.45	2.5	
Thermal resistance, junction to ambient per leg	R _{thJA}	R _{thJA} Typical socket mount		-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Weight			-	2.0	-	g
			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking daviso		Case style D ² PAK	8ETH03S			
Marking device		Case style TO-262	8ETH03-1			

Document Number: 94025 Revision: 21-May-08



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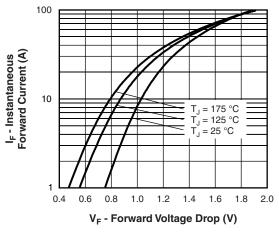


Fig. 1 - Typical Forward Voltage Drop Characteristics

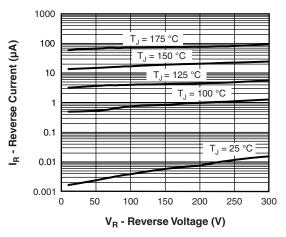


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

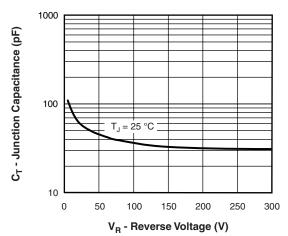


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

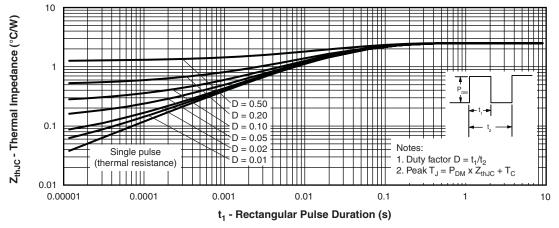


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

8ETH03SPbF/8ETH03-1PbF

Vishay High Power Products

Hyperfast Rectifier, 8 A FRED PtTM



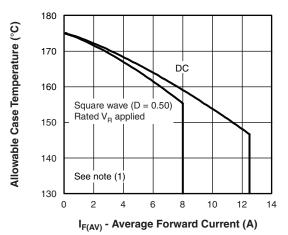


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

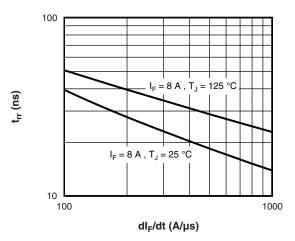


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

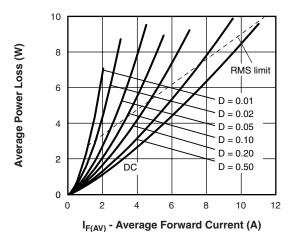


Fig. 6 - Forward Power Loss Characteristics

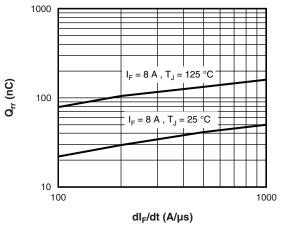


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J \text{ - } (Pd + Pd_{REV}) \text{ x } R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \text{ x } V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \text{ x } I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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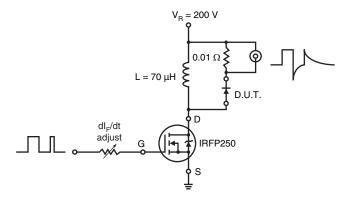
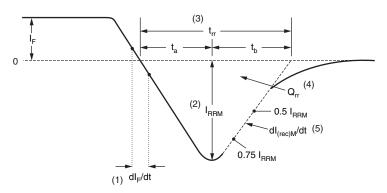


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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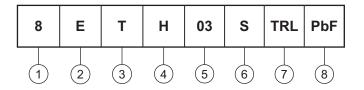
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (8 A)

2 - E = Single diode

3 - $T = TO-220, D^2PAK$

4 - H = Hyperfast rectifier

Voltage rating (03 = 300 V)

6 - • $S = D^2PAK$

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			

Document Number: 94025 Revision: 21-May-08



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