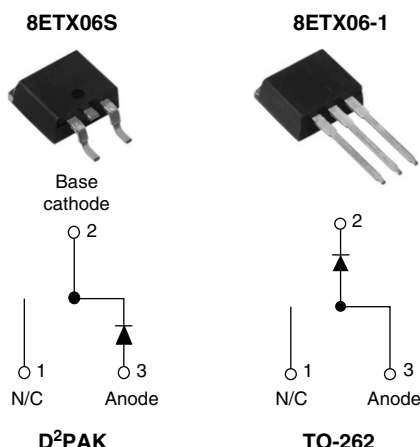


## Hyperfast Rectifier, 8 A FRED P<sub>t</sub><sup>™</sup>



### FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

### DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

### PRODUCT SUMMARY

$t_{rr}$ (typical)	15 ns
$I_{F(AV)}$	8 A
$V_R$	600 V

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 143\text{ °C}$	8	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	110	
Peak repetitive forward current	$I_{FM}$		18	
Operating junction and storage temperatures	$T_J, T_{Stg}$		- 65 to 175	°C

### ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	$V_F$	$I_F = 8\text{ A}$	-	2.3	3.0	
		$I_F = 8\text{ A}, T_J = 150\text{ °C}$	-	1.4	1.7	
Reverse leakage current	$I_R$	$V_R = V_R\text{ rated}$	-	0.3	50	$\mu\text{A}$
		$T_J = 150\text{ °C}, V_R = V_R\text{ rated}$	-	35	500	
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	17	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	8.0	-	nH

**DYNAMIC RECOVERY CHARACTERISTICS** ( $T_C = 25\text{ }^{\circ}\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1\text{ A}$ , $dI_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	15	19	ns
		$I_F = 8\text{ A}$ , $dI_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	16	24	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	17	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	40	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.3	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	4.5	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	20	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	100	-	
Reverse recovery time	$t_{rr}$	$I_F = 8\text{ A}$ $dI_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 390\text{ V}$	-	31	-	ns
Peak recovery current	$I_{RRM}$		-	12	-	A
Reverse recovery charge	$Q_{rr}$		-	195	-	nC

**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J$ , $T_{Stg}$		- 65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case per leg	$R_{thJC}$		-	1.4	2	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient per leg	$R_{thJA}$	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D <sup>2</sup> PAK	8ETX06S			
		Case style TO-262	8ETX06-1			

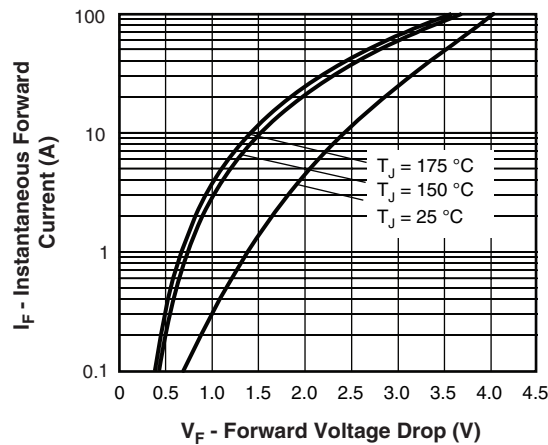


Fig. 1 - Typical Forward Voltage Drop Characteristics

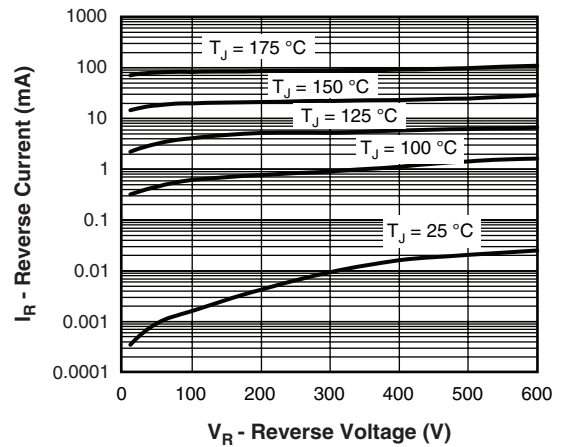


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

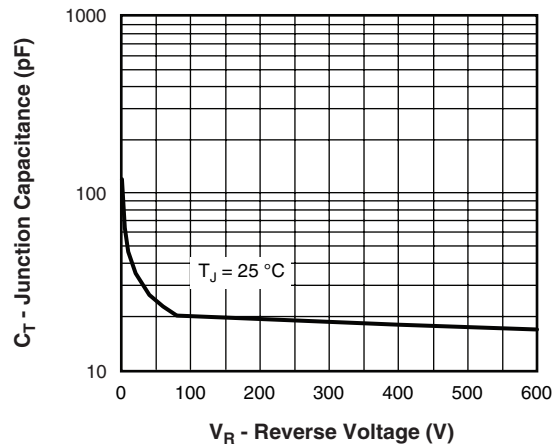
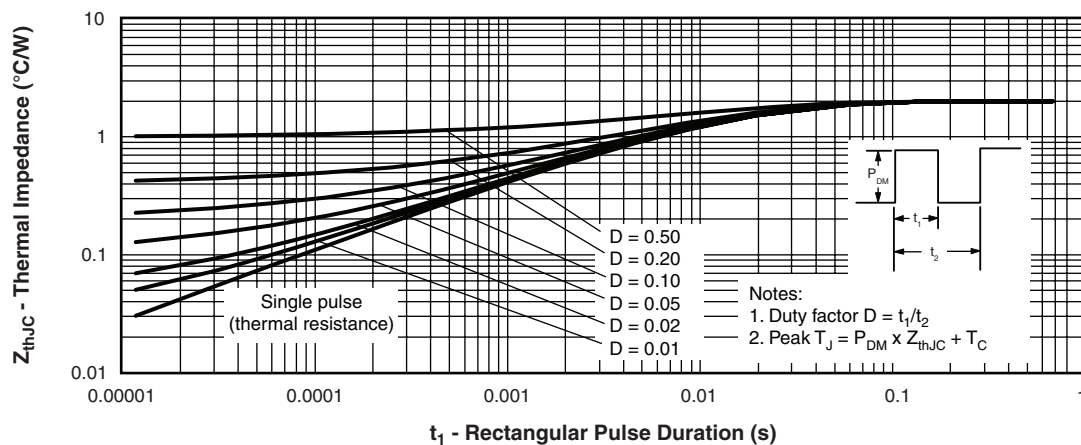


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

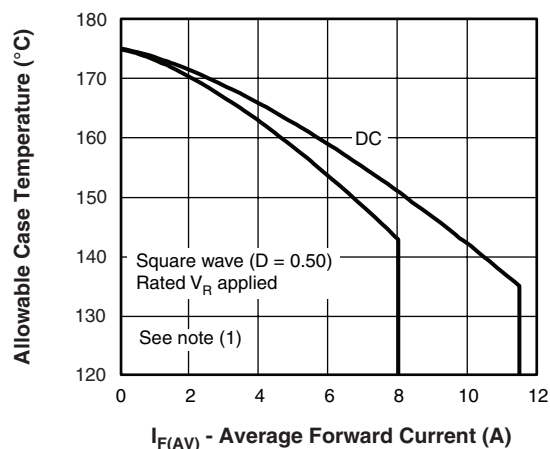


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

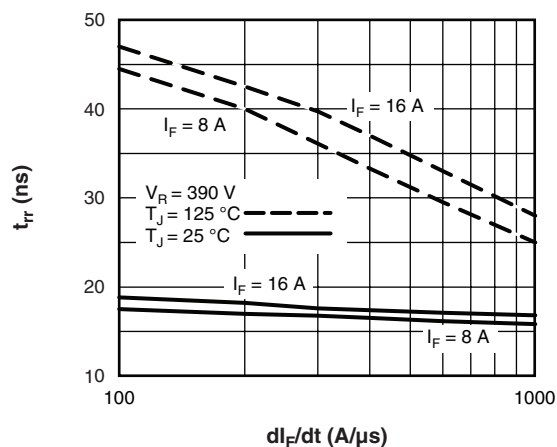


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$

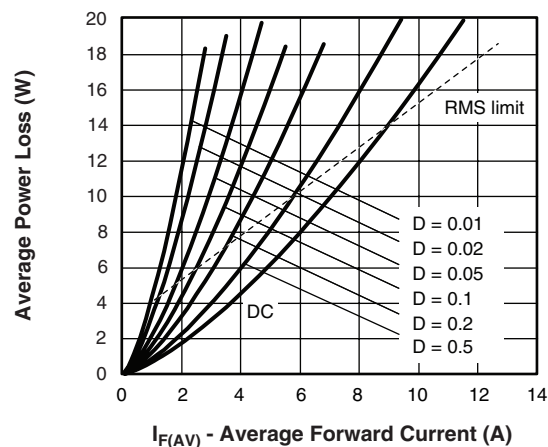


Fig. 6 - Forward Power Loss Characteristics

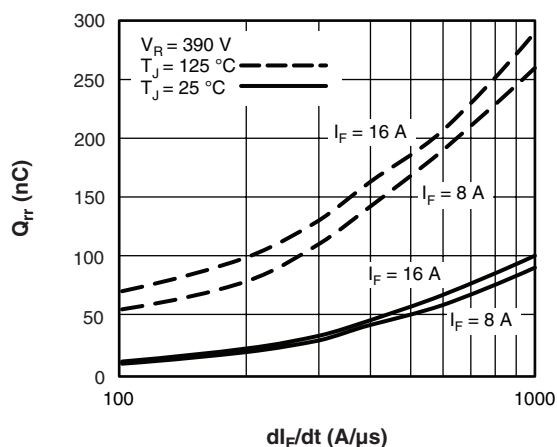


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$

## Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{d_{REV}}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = Rated  $V_R$

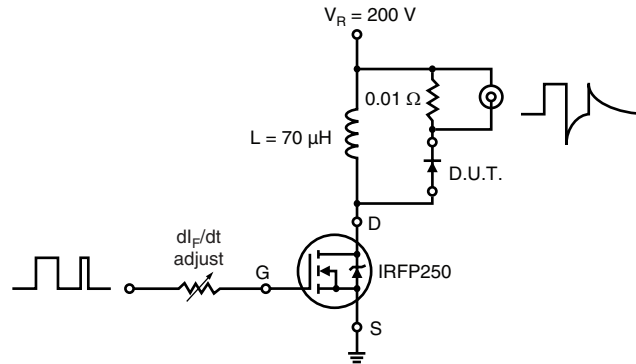


Fig. 9 - Reverse Recovery Parameter Test Circuit

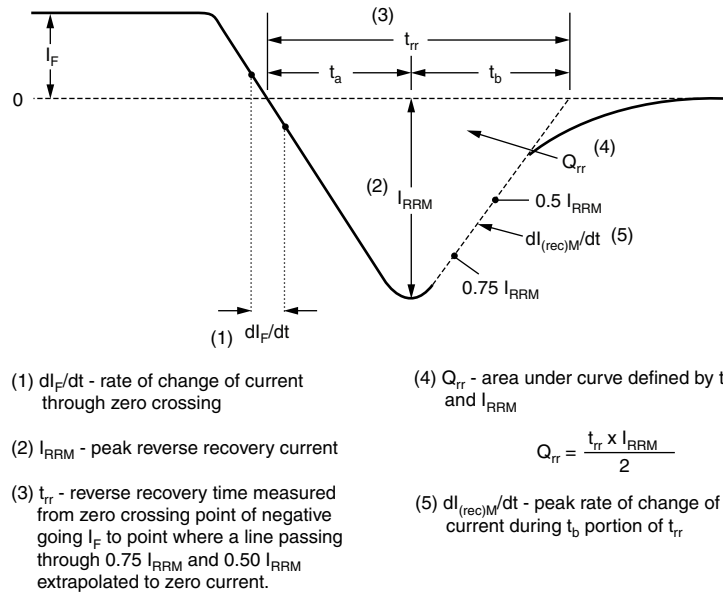


Fig. 10 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	8	E	T	X	06	S	TRL	-
	1	2	3	4	5	6	7	8

- |          |   |  |
|----------|---|--|
| <b>1</b> | - | Current rating (8 A)   |
| <b>2</b> | - | E = Single diode   |
| <b>3</b> | - | T = TO-220, D <sup>2</sup> PAK   |
| <b>4</b> | - | X = Hyperfast rectifier  |
| <b>5</b> | - | Voltage rating (06 = 600 V)  |
| <b>6</b> | - | <ul style="list-style-type: none"> <li>S = D<sup>2</sup>PAK</li> <li>-1 = TO-262</li> </ul>  |
| <b>7</b> | - | <ul style="list-style-type: none"> <li>None = Tube (50 pieces)</li> <li>TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK package)</li> <li>TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK package)</li> </ul> |
| <b>8</b> | - | <ul style="list-style-type: none"> <li>None = Standard production</li> <li>PbF = Lead (Pb)-free</li> </ul>   |

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95014">http://www.vishay.com/doc?95014</a>
Part marking information	<a href="http://www.vishay.com/doc?95008">http://www.vishay.com/doc?95008</a>
Packaging information	<a href="http://www.vishay.com/doc?95032">http://www.vishay.com/doc?95032</a>



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