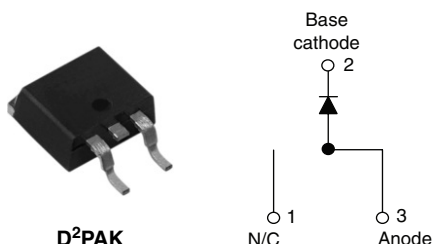


## Schottky Rectifier, 8 A



### FEATURES

- 175 °C  $T_J$  operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level


**RoHS\***  
COMPLIANT

### PRODUCT SUMMARY

$I_{F(AV)}$	8 A
$V_R$	80/100 V

### DESCRIPTION

The 8TQ Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	8	A
$V_{RRM}$	Range	80/100	V
$I_{FSM}$	$t_p = 5 \mu s$ sine	850	A
$V_F$	8 Apk, $T_J = 125^\circ C$	0.58	V
$T_J$	Range	- 55 to 175	°C

### VOLTAGE RATINGS

PARAMETER	SYMBOL	8TQ080GSPbF	8TQ100GSPbF	UNITS
Maximum DC reverse voltage	$V_R$	80	100	V
Maximum working peak reverse voltage	$V_{RWM}$			

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 157\text{ }^{\circ}\text{C}$ , rectangular waveform		8	A
Maximum peak one cycle non-repetitive surge current See fig. 7	$I_{FSM}$	5 $\mu\text{s}$ sine or 3 $\mu\text{s}$ rect. pulse	Following any rated load condition and with rated $V_{RRM}$ applied	850	A
		10 ms sine or 6 ms rect. pulse		230	
Non-repetitive avalanche energy	$E_{AS}$	$T_J = 25\text{ }^{\circ}\text{C}$ , $I_{AS} = 0.5\text{ A}$ , $L = 60\text{ mH}$		7.50	mJ
Repetitive avalanche current	$I_{AR}$	Current decaying linearly to zero in 1 $\mu\text{s}$ Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		0.5	A

\* Pb containing terminations are not RoHS compliant, exemptions may apply

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	8 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.72	V	
		16 A		0.88		
		8 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.58		0.69
		16 A				
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	0.28	mA	
		$T_J = 125\text{ }^{\circ}\text{C}$		7		
Maximum junction capacitance	$C_T$	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$		500	pF	
Typical series inductance	$L_S$	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated $V_R$		10 000	V/ $\mu$ s	

**Note**(1) Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	2	°C/W
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style D <sup>2</sup> PAK	8TQ100GS	

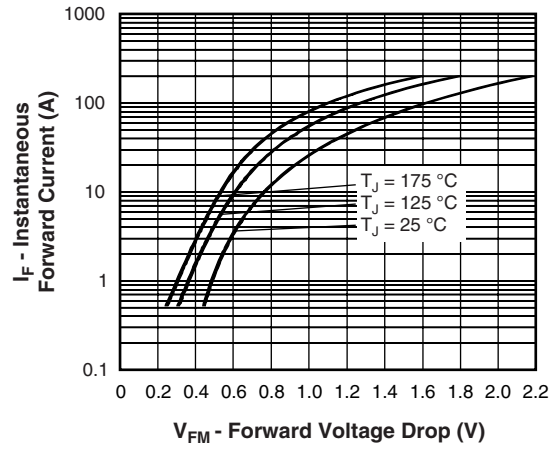


Fig. 1 - Maximum Forward Voltage Drop Characteristics

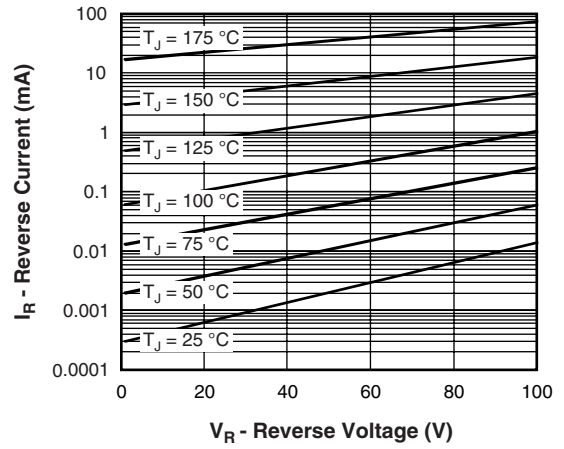


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

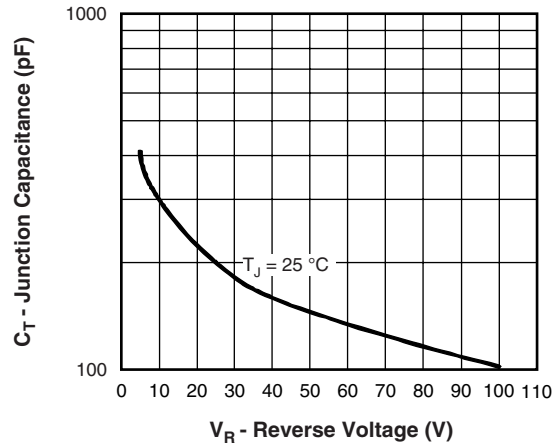
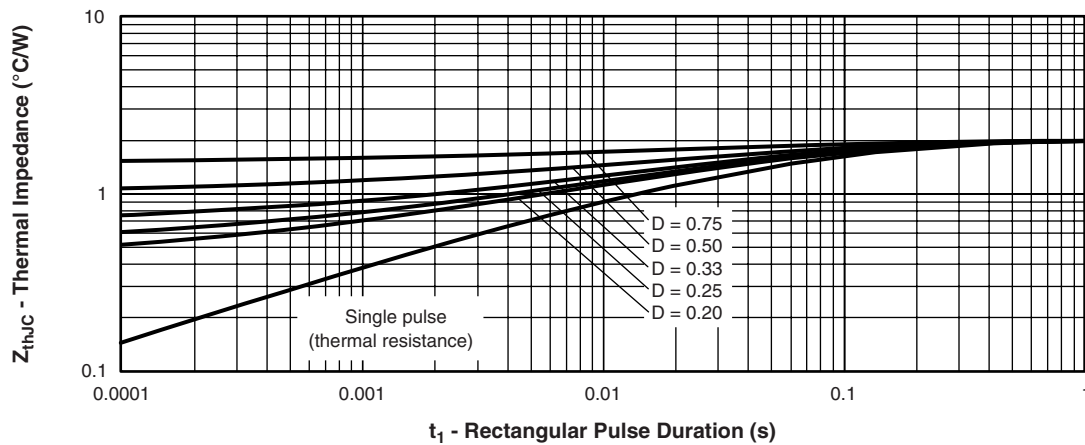


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

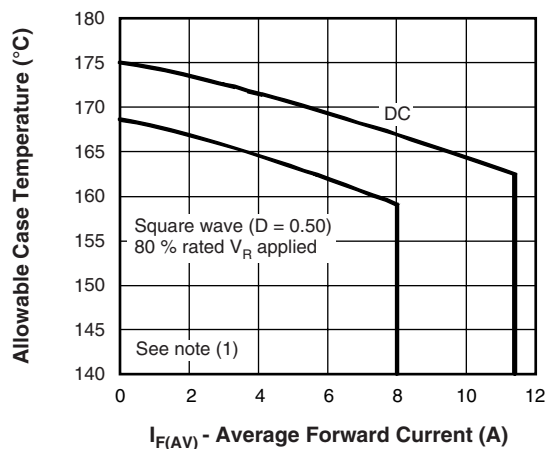


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

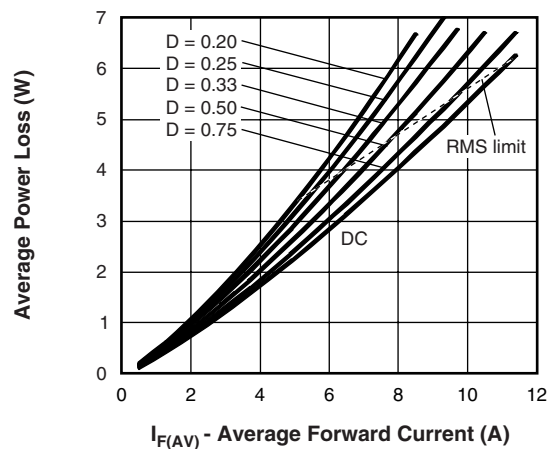


Fig. 6 - Forward Power Loss Characteristics

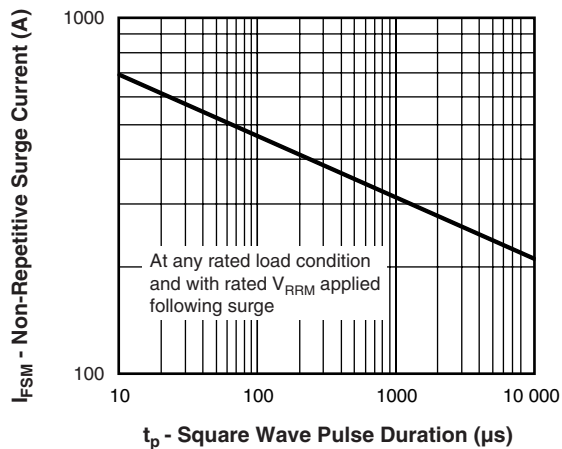


Fig. 7 - Maximum Non-Repetitive Surge Current



Fig. 8 - Unclamped Inductive Test Circuit

### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80\%$  rated  $V_R$



### ORDERING INFORMATION TABLE

Device code	8	T	Q	100	G	S	TRL	PbF
	1	2	3	4	5	6	7	8

- |          |   |  |
|----------|---|--|
| <b>1</b> | - | Current rating (8 = 8 A)   |
| <b>2</b> | - | T = TO-220   |
| <b>3</b> | - | O = Schottky "Q" series  |
| <b>4</b> | - | Voltage ratings  |
| <b>5</b> | - | G = Schottky generation  |
| <b>6</b> | - | S = D <sup>2</sup> PAK   |
| <b>7</b> | - | <ul style="list-style-type: none"><li>• None = Tube (50 pieces)</li><li>• TRL = Tape and reel (left oriented)</li><li>• TRR = Tape and reel (right oriented)</li></ul> |
| <b>8</b> | - | <ul style="list-style-type: none"><li>• None = Standard production</li><li>• PbF = Lead (Pb)-free</li></ul>  |
- 080 = 80 V  
100 = 100 V

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95046">http://www.vishay.com/doc?95046</a>
Part marking information	<a href="http://www.vishay.com/doc?95058">http://www.vishay.com/doc?95058</a>
Packaging information	<a href="http://www.vishay.com/doc?95032">http://www.vishay.com/doc?95032</a>
SPIICE model	<a href="http://www.vishay.com/doc?95291">http://www.vishay.com/doc?95291</a>



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