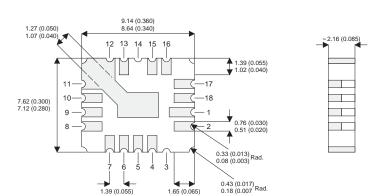


### IP117MAHVE

#### IP117MAE IP117ME



# 0.5 AMP POSITIVE ADJUSTABLE VOLTAGE REGULATOR IN CERAMIC SURFACE MOUNT PACKAGE

#### **FEATURES**

- OUTPUT VOLTAGE RANGE ADJUSTABLE:
   1.25 TO 40V FOR STANDARD VERSION
   1.25 TO 60V FOR -HV VERSION
- 1% OUTPUT VOLTAGE TOLERANCE (-A VERSIONS)
- 0.3% LOAD REGULATION
- 0.01%/V LINE REGULATION
- COMPLETE SERIES OF PROTECTIONS:
  - CURRENT LIMITING
  - THERMAL SHUTDOWN
  - SOA CONTROL

Pins 4,5 — Adjust Pins 6,7,8,9,10,11,12,13 —  $V_{IN}$  Pins 15,16,17,18,1,2 —  $V_{OUT}$ 

#### **DESCRIPTION**

The IP117M Series are three terminal positive adjustable voltage regulators capable of supplying in excess of 0.5A over a 1.25V to 60V output range. These regulators are exceptionally easy to use and require only two external resistors to set the output voltage. In addition to improved line and load regulation, a major feature of the "A" series is the initial output voltage tolerance, which is guaranteed to be less than 1%.

Over full operating conditions, including load, line, and power dissipation, the reference voltage is guaranteed not to vary more than 2%. These devices exhibit current limit, thermal overload protection, and improved power device safe operating area protection, making them essentially indestructible.

#### ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{I-O}}$	Input - Output Differential Voltage	<ul><li>Standard</li></ul>	40V			
		<ul><li>HV Series</li></ul>	60V			
$P_{D}$	Power Dissipation		Internally limited			
$T_J$	Operating Junction Temperature Range		−55 to 150°C			
$T_{STG}$	Storage Temperature		−65 to 150°C			

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## IP117MAHVE IP117MAE IP117MHVE IP117ME

					IP117MAHV IP117MA			IP117MHV , IP117M			
Paran	neter	Test Conditi	ons		Min.	Тур.	Max.	Min.	Тур.	Max.	Units
	Reference Voltage	I <sub>OUT</sub> = 10mA			1.238	1.25	1.262				V
$V_{REF}$		I <sub>OUT</sub> = 10mA to I <sub>MAX</sub>									
		$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$			1.220	1.250	1.270	1.200	1.250	1.300	V
		$P \le P_{MAX}$ $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$									
$\Delta V_{OUT}$	Line Regulation <sup>1</sup>	$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$				0.005	0.010		0.010	0.020	%/V
$\Delta V_{IN}$			$T_J = -55 \text{ to } +150^{\circ}\text{C}$			0.010	0.020		0.020	0.050	1 % / V
	Load Regulation <sup>1</sup>	I <sub>OUT</sub> = 10mA t	I <sub>MAX</sub>	V <sub>OUT</sub> ≤ 5V		5	15		5	15	mV
$\Delta V_{OUT}$				V <sub>OUT</sub> ≥ 5V		0.1	0.3		0.1	0.3	%
$\Delta I_{OUT}$		$I_{OUT} = 10 \text{mA to } I_{MAX} \qquad V_{OUT} \le 5 \text{V}$			15	50		20	50	m∨	
		$T_J = -55 \text{ to } +150^{\circ}\text{C}$ $V_{OUT} \ge$		V <sub>OUT</sub> ≥ 5V		0.3	1		0.3	1	%
	Thermal Regulation	t <sub>p</sub> = 20ms				0.002	0.020		0.030	0.070	%/W
	Ripple Rejection	10)/		$C_{ADJ} = 0$		65			65		dB
		V <sub>OUT</sub> = 10V f = 120Hz	(	C <sub>ADJ</sub> = 10μF	66	80		66	80		dB
			$T_{J} = -5$	55 to +150°C							
I <sub>ADJ</sub>	Adjust Pin Current	$T_J = -55 \text{ to } +150^{\circ}\text{C}$				50	100		50	100	μА
$\Delta I_{ADJ}$	Adjust Pin Current	$I_{OUT} = 10$ mA to $I_{MAX}$ $T_{J} = -55$ to +150°C $V_{IN} - V_{OUT} = 2.5$ V to $V_{MAX}$				0.2	5	0.2		5	μА
									0.2		
	Change										
	Minimum Load Current	$V_{IN} - V_{OUT} = 40V$ $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$				3.5	5		3.5 5	_	
I <sub>MIN</sub>										5	
		$V_{IN} - V_{OUT} = 60V$ (HV SERIES) $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$				3.5	7		0.=	7 mA	mA
									3.5		
I <sub>CL</sub>	Current Limit	$V_{IN} - V_{OUT} \le 1$	5V		0.50	0.00		0.50	0.00		
		$T_{J} = -55 \text{ to } +150^{\circ}\text{C}$			0.50	0.80		0.50	0.80		A
		$V_{IN} - V_{OUT} = 40V$		0.15	0.20		0.15	0.20		A	
		$V_{IN} - V_{OUT} = 6$	N - V <sub>OUT</sub> = 60V <b>(HV SERIES)</b>			0.30			0.30		
$\Delta V_{OUT}$	Temperature	$T_{J} = -55 \text{ to } +150^{\circ}\text{C}$				_				0/	
ΔTEMF	Stability				1	2		1		%	
$\Delta V_{OUT}$	T 0: 133	T 40500									
ΔΤΙΜΕ	Long Term Stability	$T_A = +125^{\circ}C$		t = 1000 Hrs		0.3	1		0.3	1	%
e <sub>n</sub>	RMS Output Noise	f = 10 Hz to 10 kHz			0.001			0.004		%	
'	(% of V <sub>OUT</sub> )							0.001			
$R_{\theta JC}$	Thermal Resistance	10045	_				40			40	0000
	Junction to Case	LCC4 Package					13			13	°C/W

<sup>1)</sup> Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications.

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<sup>2)</sup> Test Conditions unless othewise stated:  $V_{IN} - V_{OUT} = 5V$ ,  $T_J = 25^{\circ}C$ ,  $I_{OUT} = 0.1A$ ,  $P_{MAX} = 10W$ ,  $I_{MAX} = 0.5A$   $V_{MAX} = 40V$  for standard series , 60V for HV series.