Parameter	Rating	Units
Blocking Voltage	60	V _P
Load Current	200	mA
Max R _{ON}	16	Ω
Input Voltage to operate	5-12	V

Features

- 100% Solid State
- Voltage-controlled operation
- · Matches popular reed relay pin-out
- Designed for use in security systems complying with EN50130-4
- Small 4-Pin SIP Package
- Arc-Free With No Snubbing Circuits
- 2500V_{rms} Input/Output Isolation
- No EMI/RFI Generation
- Immune to radiated EM fields
- · Auto Pick & Place, Wave Solderable

Applications

- Security
 - · Passive Infrared Detectors (PIR)
 - Data Signalling
- Sensor Circuitry
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Energy Meters
- Medical Equipment—Patient/Equipment Isolation
- Aerospace
- Industrial Controls

Description

The CPC1217 is a voltage-controlled, single-pole, normally open (1-Form-A), optically coupled solid state relay configuration in a 4-pin Single In-line Package (SIP). Clare's patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing a 2500V_{rms} input-to-output isolation barrier. Control of the isolated output is accomplished by means of a highly effective GaAlAs infrared LED at the input while the internal resistor in series with the LED enables the input voltage-controlled operation.

Because the input is solid state there is no need for snubbers or "catch" diodes to suppress the inductive flyback transient voltage normally associated with EMR coils.

Approvals

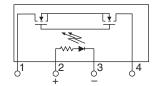
- UL Recognized Component: File # E76270
- EN/IEC 60950 Compliant
- CSA Certified Component: Certificate # 1172007

Ordering Information

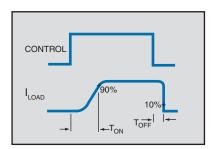
Part #	Description	
CPC1217Y	4-Pin SIP (25/tube)	

Pin Configuration

CPC1217 Pinout



Switching Characteristics of Normally Open (Form A) Devices











Absolute Maximum Ratings

Parameter	Ratings	Units	
Blocking Voltage	60	V _P	
Reverse Input Voltage	5	V	
Input Control Voltage	15	V	
Input Power Dissipation	225	mW	
Total Power Dissipation ¹	800	mW	
Isolation Voltage Input to Output	2500	V _{rms}	
Operational Temperature	-40 to +85	°C	
Storage Temperature	-40 to +125	°C	

excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Absolute Maximum Ratings are stress ratings. Stresses in

Electrical absolute maximum ratings are at 25°C

Electrical Characteristics

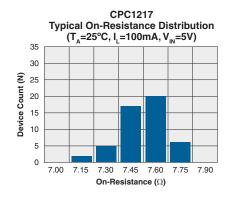
Conditions	Symbol	Min	Тур	Max	Units
·					<u>'</u>
V _{IN} =5V	I_L	-	-	200	— mA
t≤10ms	I _{LPK}	-	-	400	
I _L =200mA	R _{ON}	-	-	16	Ω
$V_L=60V_P$	I _{LEAK}	-	-	1	μΑ
V 5VV 10V	T_{ON}	-	-	5	
ν _{IN} =5ν, ν _L =10ν		-	-	5	– ms
V _{IN} =0V, V _L =50V; f=1MHz		-	25	-	pF
		<u> </u>			
		-	-	3.75	
I _L =200mA	V_{IN}	5	-	12	V
		1	-	-	
V _{IN} =-5V	I _R	-	-	10	μА
-	-	900	1000	1100	Ω
;		1			1
-	-	-	1	-	pF
	$V_{IN}=5V$ $t \le 10 ms$ $I_{L}=200 mA$ $V_{L}=60 V_{P}$ $V_{IN}=5V, V_{L}=10V$ $V_{IN}=0V, V_{L}=50V; f=1 MHz$ $I_{L}=200 mA$ $V_{IN}=-5V$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

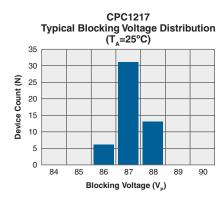
¹ Measurement taken within 1 second of on time.

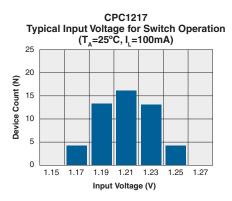
¹ Derate Linearly 6.67 mw / °C

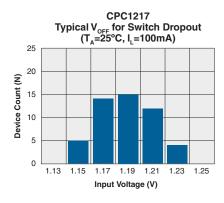


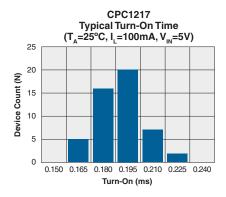
PERFORMANCE DATA*

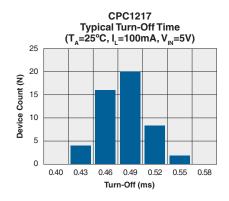


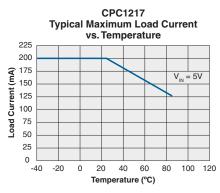


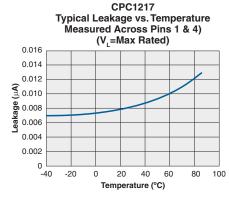


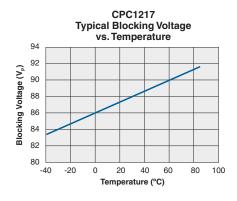


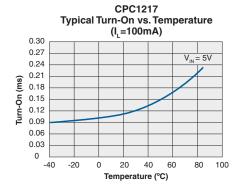


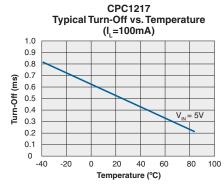


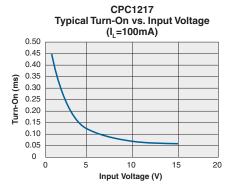








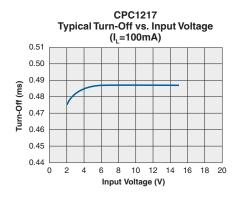


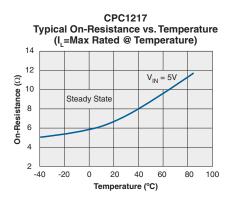


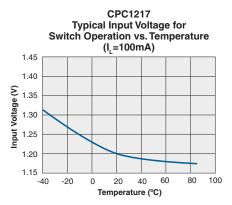
^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

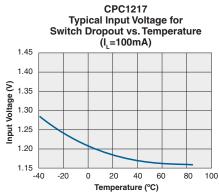


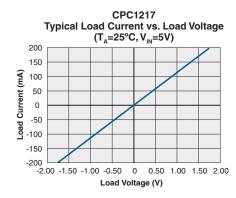
PERFORMANCE DATA*

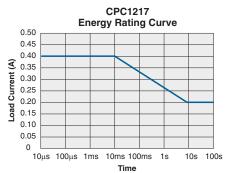












^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

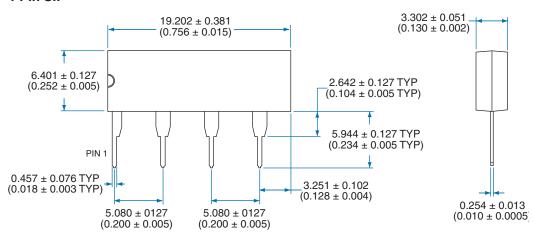






MECHANICAL DIMENSIONS

4-Pin SIP



NOTES:

- 1. Leadframe thickness does not include plating. (1000 microinches maximum)
- 2. Pin location tolerances are non-accumulative.

<u>Dimensions:</u> mm (inches)

For additional information please visit our website at: www.clare.com

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Specification: DS-CPC1217-R03

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