



DES-011

SERIAL DIGITAL TO PARALLEL TTL MODULE

- Small compact design for pcb mounting
- Composite or Component digital inputs
- 8 or 10 bit data
- Reclocked SD outputs available.
- Auto 144,177,270, 360 mb/s Operation
- Automatic Cable equalisation
- + 5V DC power

This interface card accepts multistandard serial digital signals and converts these to a 10 bit parallel data stream with a word rate clock. In addition outputs indicating the presence of TRS and EAV timing reference signals are provided to allow easy decoding of the parallel data stream.

Input Requirements

Input data format *Input data rates*

SMPTE 259M
360 Mb/s serial 4:2:2 widescreen component
270 Mb/s serial 4:2:2 component
144,177 Mb/s serial NTSC/PAL

Input Impedance *Equalisation*

75 ohm terminating
Automatic up to 40 dB at 200 MHz
(Typically 300m of Belden 8281 at 270mb/s)

Output Characteristics

Parallel Output

One parallel word at 14.4, 17.7, 27 or 36Mb/s
TTL/CMOS levels.

SD Outputs *EAV Output*

2 reclocked serial outputs, SMPTE 259M
End of active video flag. For component video, a logic low is output for one cycle of the parallel clock every time an EAV timing reference signal is detected. The pulse is aligned with the fourth word of the timing reference (the XYZ word.) For composite video, this line is always asserted high.

TRS Output

Timing reference flag.
A logic low is output for the duration of the TRS.

Other parameters

Power

+5V $\pm$ 0.25V (300 mA max)

Operating

0°C to 40°C

Temperature

Aqueous Washable

No

Package

DR00174A

ORDERING CODES

DES-011A

DES-011B

Reclocked o/p's

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PACKAGE DETAIL

