



High Voltage 4-Ω Quad SPST CMOS Analog Switch

DESCRIPTION

The DG451 series has four independently selectable high voltage (44 V) SPST switches, each with a typical on resistance of 4 Ω and a typical flatness of 0.2 Ω, ideal parameters for low distortion audio signal switching.

The DG451 (NC) and DG452 (NO) are identical except for the digital logic control input, which is inverted as shown in the Truth Table. The DG453 has two normally closed and two normally open switches.

These are high voltage switches that are fully specified with dual supplies at ± 5 V and ± 15 V and a single supply of 12 V and operating with ultra low power dissipation (18 μW). Fast switching speeds coupled with high signal bandwidth makes these parts suitable for video switching applications. All digital inputs have 0.8 V and 2.4 V logic thresholds ensuring low voltage TTL/CMOS compatibility. Each switch conducts equally well in both directions when on and can handle an input signal range that extends to the supply voltage rails.

The DG451, DG452, and DG453 are pin compatible with the DG411, DG412, and DG413.

FEATURES

- Low on-resistance (4 Ω typical)
- On-resistance flatness (0.2 Ω typical)
- 100 mA continuous current
- 44 V supply maximum rating
- ± 15 V analog signal range
- Fully specified at supply voltages of ± 5 V, 12 V and ± 15 V
- Ultra low power dissipation of (18 μW)
- Fast switching speed:
 - t_{on} 80 ns
 - t_{off} 60 ns
- TTL/CMOS compatible
- ESD protection 2 kV
- Pin compatible with DG411, DG412, and DG413

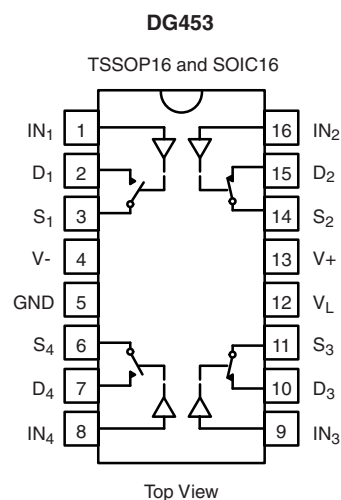
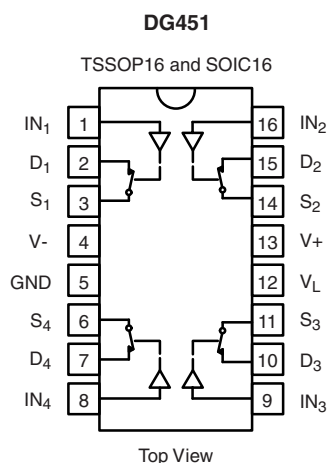


RoHS
COMPLIANT

APPLICATIONS

- Audio and video signal switching
- Precision automatic test equipment
- Precision data acquisition
- Relay replacement
- Communications systems
- Automotive and avionics applications
- Sample and hold systems

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



TRUTH TABLE		
Logic	DG451	DG452
0	ON	OFF
1	OFF	ON

TRUTH TABLE		
Logic	SW ₁ , SW ₄	SW ₂ , SW ₃
0	OFF	ON
1	ON	OFF

DG451/DG452/DG453

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ORDERING INFORMATION

Temp Range	Package	Part Number
DG451/DG452/DG453		
- 40 °C to 125 °C ^a	16-Pin TSSOP	DG451EQ-T1-E3 DG452EQ-T1-E3 DG453EQ-T1-E3
	16-Pin Narrow SOIC	DG451EY-T1-E3 DG452EY-T1-E3 DG453EY-T1-E3

Notes:

a. - 40 to 85 °C datasheet limits apply.

ABSOLUTE MAXIMUM RATINGS $T_A = 25\text{ °C}$, unless otherwise noted

Parameter		Limit	Unit
V+ to V-		44	V
GND to V-		25	
V _L		(GND - 0.3) to (V+) + 0.3	
Digital Inputs ^a , V _S , V _D		(V-) - 2 to (V+) + 2 or 30 mA, whichever occurs first	
Continuous Current (Any Terminal)		100	mA
Peak Current, S or D (Pulsed 1 ms, 10 % Duty Cycle)		300	
Storage Temperature		- 65 to 150	°C
Power Dissipation (Package) ^b	16-Pin TSSOP ^c	450	mW
	16-Pin Narrow SOIC ^d	600	
Thermal Resistance (Package) ^b	16-Pin TSSOP	178	C/W
	16-Pin Narrow SOIC	125	
ESD (HBM)		2	kV

Notes:

a. Signals on S_X, D_X, or I_{NX} exceeding V₊ or V₋ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 5.6 mW/°C above 70 °C.

d. Derate 8.0 mW/°C above 75 °C.

SPECIFICATIONS FOR DUAL SUPPLIES

Parameter	Symbol	Test Conditions Unless Specified V ₊ = 15 V, V ₋ = - 15 V V _L = 5 V, V _{IN} = 2.4 V, 0.8 V ^a	Temp. ^b	Typ. ^c	- 40 to 125 °C		- 40 to 85 °C		Unit
					Min. ^d	Max. ^d	Min. ^d	Max. ^d	
Analog Switch									
Analog Signal Range ^e	V _{ANALOG}		Full		- 15	15	- 15	15	V
On-Resistance	R _{ON}	I _S = - 10 mA, V _D = - 10 V to + 10 V	Room Full	3.8		5.3 8.3		5.3 7.3	Ω
On-Resistance Match	ΔR _{ON}	I _S = - 10 mA, V _D = ± 10 V	Room Full	0.12		0.5 1		0.5 0.5	
On-Resistance Flatness	R _{FLATNESS}	I _S = - 10 mA, V _D = - 5 V, 0 V, + 5 V	Room Full	0.25		0.5 0.5		0.5 0.5	
Switch Off Leakage Current	I _{S(off)}	V _D = ± 10 V, V _S = ∓ 10 V	Room Full	± 0.1	- 0.5 - 20	0.5 20	- 0.5 - 2.5	0.5 2.5	nA
	I _{D(off)}		Room Full	± 0.1	- 0.5 - 20	0.5 20	- 0.5 - 2.5	0.5 2.5	
Channel On Leakage Current	I _{D(on)}	V _S = V _D = ± 10 V	Room Full	± 0.1	- 0.4 - 40	0.4 40	- 1 - 5	1 5	

**SPECIFICATIONS FOR DUAL SUPPLIES**

Parameter	Symbol	Test Conditions Unless Specified V ₊ = 15 V, V ₋ = - 15 V V _L = 5 V, V _{IN} = 2.4 V, 0.8 V ^a	Temp. ^b	Typ. ^c	- 40 to 125 °C		- 40 to 85 °C		Unit
					Min. ^d	Max. ^d	Min. ^d	Max. ^d	
Digital Control									
Input Current, V _{IN} Low	I _{IL}	V _{IN} Under Test = 0.8 V	Full	0.005	- 0.5	0.5	- 0.5	0.5	µA
Input Current, V _{IN} High	I _{IH}	V _{IN} Under Test = 2.4 V	Full	0.005	- 0.5	0.5	- 0.5	0.5	
Input Capacitance ^e	C _{IN}	f = 1 MHz	Room	7					pF
Dynamic Characteristics									
Turn-On Time	t _{ON}	R _L = 300 Ω, C _L = 35 pF V _S = ± 10 V, See Figure 2	Room Full	88		118 160		118 144	ns
Turn-Off Time	t _{OFF}		Room Full	69		97 120		97 112	
Break-Before-Make Time Delay	t _D	DG453 only, V _S = 10 V R _L = 300 Ω, C _L = 35 pF	Room	18					
Charge Injection ^e	Q	V _g = 0 V, R _g = 0 Ω, C _L = 1 nF	Room	22					pC
Off Isolation ^e	OIRR	R _L = 50 Ω, C _L = 5 pF f = 1 MHz	Room	- 60					dB
Channel-to-Channel Crosstalk ^e	X _{TALK}		Room	- 85					
Source Off Capacitance ^e	C _{S(off)}	f = 1 MHz	Room	31					pF
Drain Off Capacitance ^e	C _{D(off)}		Room	34					
Channel On Capacitance ^e	C _{D(on)}		Room	103					
Total Harmonic Distortion ^e	THD	Signal = 5 V _{RMS} , 20 Hz to 20 kHz, R _L = 600 Ω	Room	0.04					%
Power Supplies									
Power Supply Current	I ₊	V ₊ = 16.5 V, V ₋ = - 16.5 V V _L = 5 V, V _{IN} = 0 or 5 V	Room Full	0.001		0.5 5		0.5 5	µA
Negative Supply Current	I ₋		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		
Logic Supply Current	I _L		Room Full	0.001		0.5 5		0.5 5	
Ground Current	I _{GND}		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		

SPECIFICATIONS FOR DUAL SUPPLIES

Parameter	Symbol	Test Conditions Unless Specified V+ = 5 V, V- = - 5 V V _L = 5 V, V _{IN} = 2.4 V, 0.8 V ^a	Temp. ^b	Typ. ^c	- 40 to 125 °C		- 40 to 85 °C		Unit
					Min. ^d	Max. ^d	Min. ^d	Max. ^d	
Analog Switch									
Analog Signal Range ^e	V _{ANALOG}		Full		- 5	5	- 5	5	V
On-Resistance	R _{ON}	V+ = + 5 V, V- = - 5 V I _S = - 10 mA, V _D = - 3.5 V to + 3.5 V	Room Full	3.8		11 15		11 12	Ω
On-Resistance Match	ΔR _{ON}	V+ = + 5 V, V- = - 5 V, I _S = - 10 mA, V _D = ± 3.5 V	Room Full	0.13		0.5 1		0.5 0.5	
Dynamic Characteristics									
Turn-On Time ^e	t _{ON}	R _L = 300 Ω, C _L = 35 pF V _S = 3 V, See Figure 2	Room Full	170		200 296		200 256	ns
Turn-Off Time ^e	t _{OFF}		Room Full	66		96 124		96 113	
Break-Before-Make ^e Time Delay	t _D	DG451 only, V _S = 3 V R _L = 300 Ω, C _L = 35 pF	Room Full	98					
Charge Injection ^e	Q	V _g = 0 V, R _g = 0 Ω, C _L = 1 nF	Full	8					pC



SPECIFICATIONS FOR DUAL SUPPLIES

Parameter	Symbol	Test Conditions Unless Specified V+ = 5 V, V- = - 5 V V _L = 5 V, V _{IN} = 2.4 V, 0.8 V ^a	Temp. ^b	Typ. ^c	- 40 to 125 °C		- 40 to 85 °C		Unit
					Min. ^d	Max. ^d	Min. ^d	Max. ^d	
Power Supplies									
Power Supply Current	I+	V _L = 5 V, V _{IN} = 0 or 5 V	Room Full	0.001		- 0.5 - 5		- 0.5 - 5	μA
Negative Supply Current	I-		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		
Logic Supply Current	I _L		Room Full	0.001		- 0.5 - 5		- 0.5 - 5	
Ground Current	I _{GND}		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		

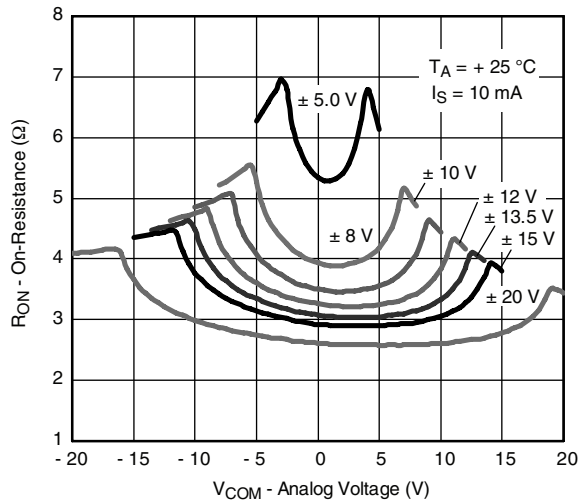
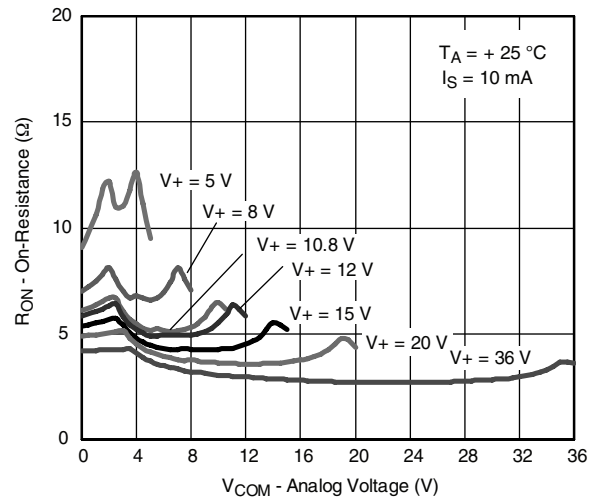
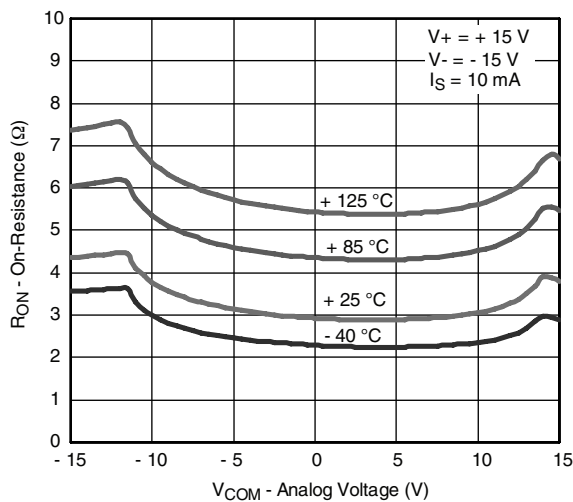
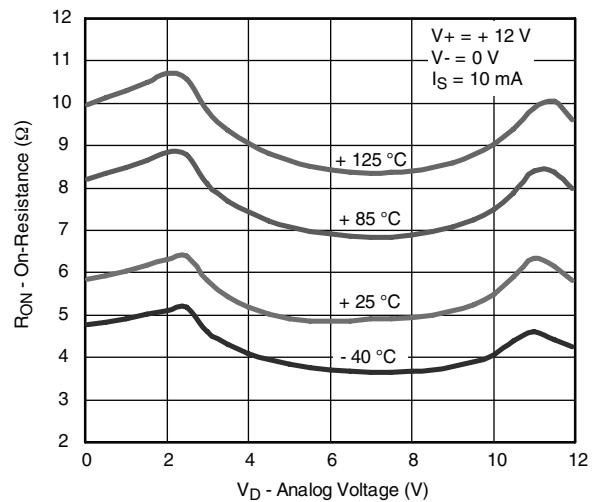
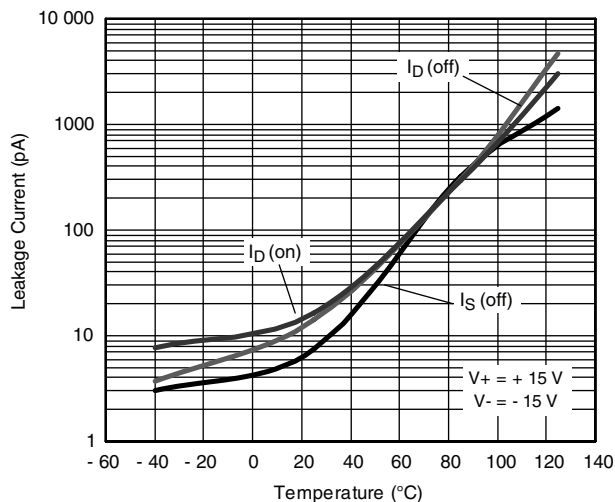
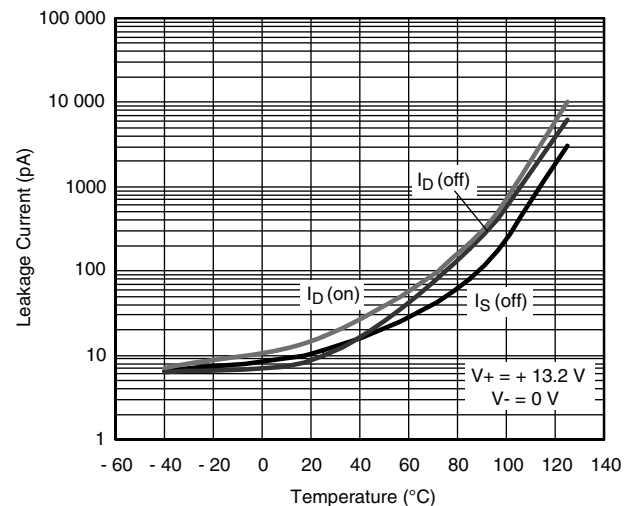
SPECIFICATIONS FOR UNIPOLAR SUPPLIES

Parameter	Symbol	Test Conditions Unless Specified V+ = 12 V, V- = 0 V VL = 5 V, VIN = 2.4 V, 0.8 Va	Temp. b	Typ. c	- 40 to 125 °C		- 40 to 85 °C		Unit
					Min. d	Max. d	Min. d	Max. d	
Analog Switch									
Analog Signal Rangee	VANALOG		Full			12		12	V
On-Resistance	RON	IS = - 10 mA, VD = 0 V to + 10 V	Room Full	5.5		8.1 12.4		8.1 10.4	Ω
On-Resistance Match	ΔRON	IS = - 10 mA, VD = + 10 V	Room Full	0.14		0.5 1		0.5 0.5	
On-Resistance Flatness	RFLATNESS	IS = - 10 mA, VD = 0 V, + 5 V, + 10 V	Room Full	0.94		1.5 1.7		1.5 1.5	
Dynamic Characteristics									
Turn-On Time	tON	RL = 300 Ω, CL = 35 pF VS = 8 V, See Figure 2	Room Full	132		162 238		162 210	ns
Turn-Off Time	tOFF		Room Full	61		91 117		91 105	
Break-Before-Make Time Delay	tD	DG453 only, VS = 8 V RL = 300 Ω, CL = 35 pF	Room	70					
Charge Injectione	Q	Vg = 0 V, Rg = 0 Ω, CL = 1 nF	Room	1					pC
Power Supplies									
Power Supply Current	I+	VL = 5 V, VIN = 0 or 5 V	Room Full	0.001		0.5 5		0.5 5	μA
Negative Supply Current	I-		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		
Logic Supply Current	IL		Room Full	0.001		0.5 5		0.5 5	
Ground Current	IGND		Room Full	- 0.001	- 0.5 - 5		- 0.5 - 5		

Notes:

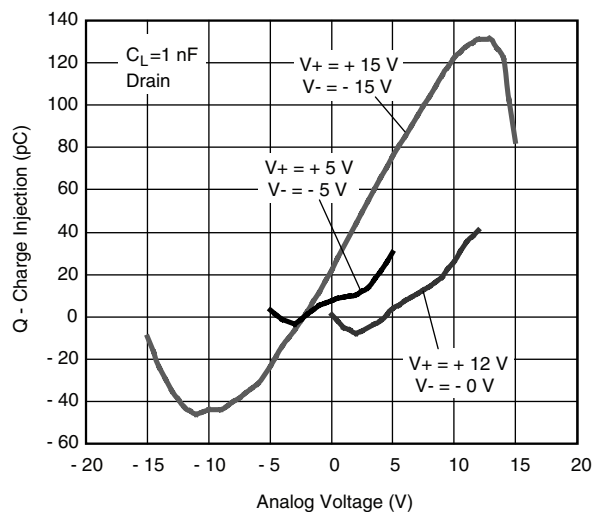
- a. V_{IN} = input voltage to perform proper function.
b. Room = 25 °C, Full = as determined by the operating temperature suffix.
c. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
d. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
e. Guaranteed by design, not subject to production test.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

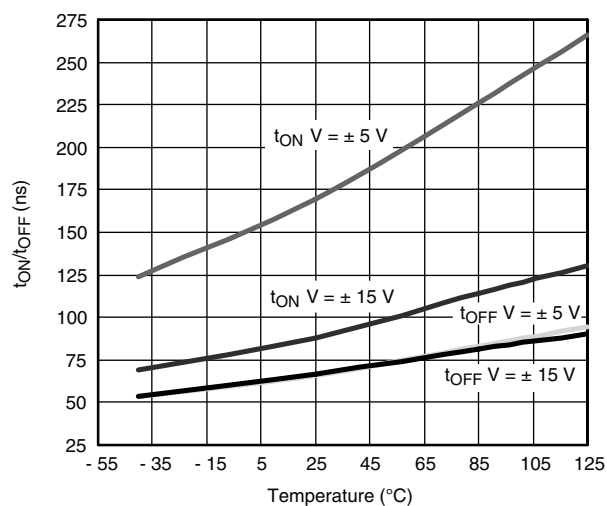
**TYPICAL CHARACTERISTICS** 25 °C, $V_L = 5$ V, unless otherwise noted**On-Resistance vs. V_D and Dual Supply Voltage****On-Resistance vs. V_D and Single Supply Voltage****On-Resistance vs. V_D and Temperature****On-Resistance vs. V_D and Temperature****Leakage Current vs. Temperature****Leakage Current vs. Temperature**

DG451/DG452/DG453

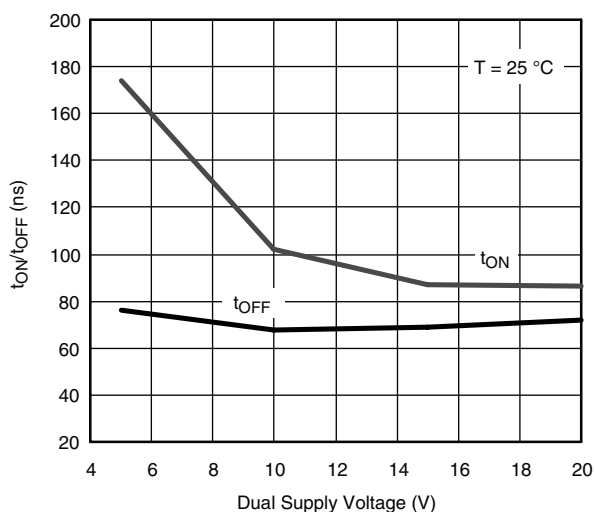
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**TYPICAL CHARACTERISTICS** 25 °C, $V_L = 5$ V, unless otherwise noted

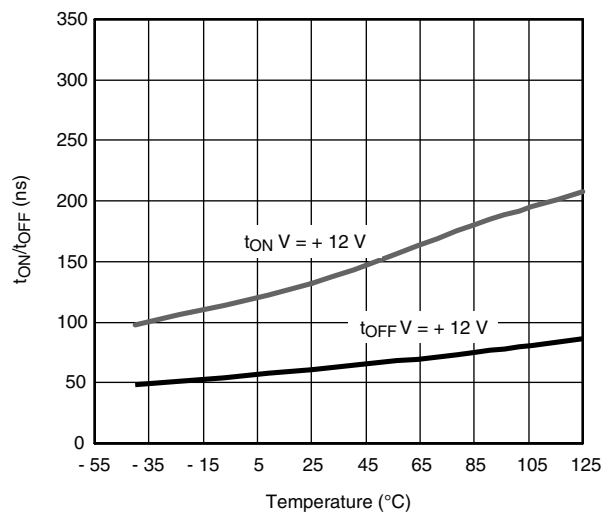
Charge Injection vs. Analog Voltage



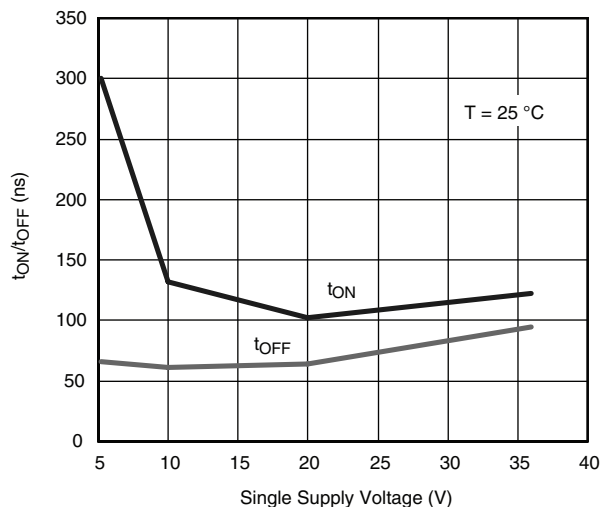
Switching Time vs. Temperature and Dual Supply Voltage



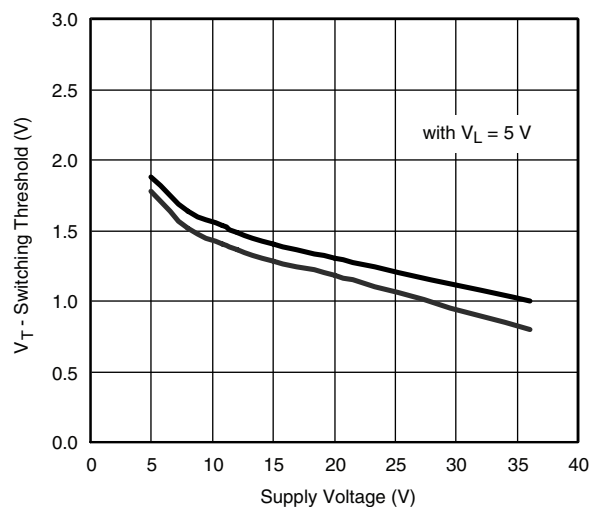
Switching Time vs. Dual Supply Voltage



Switching Time vs. Temperature and Single Supply Voltage

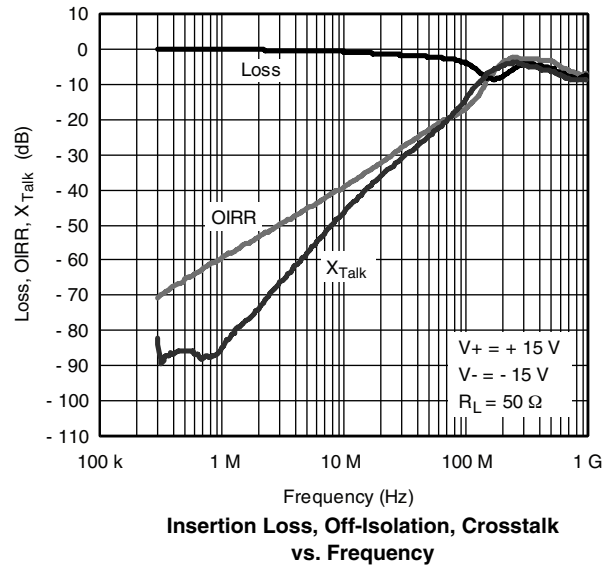
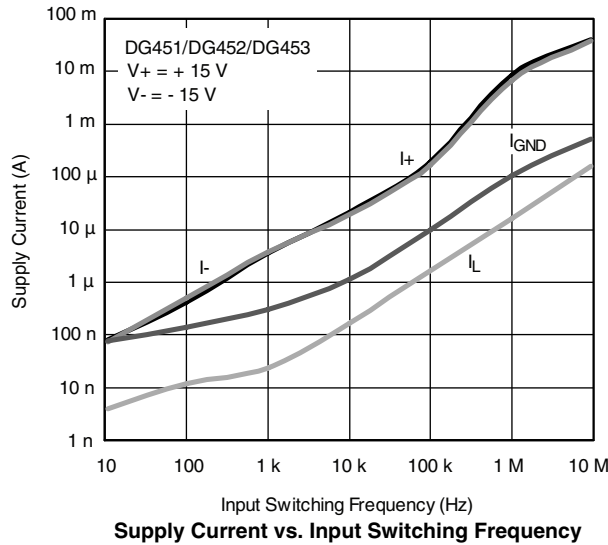


Switching Time vs. Single Supply Voltage

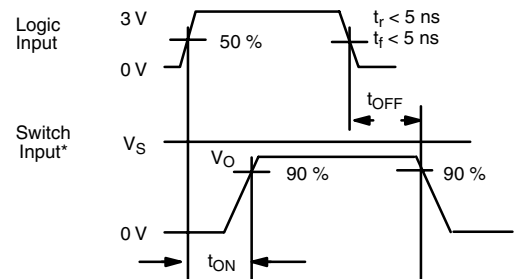
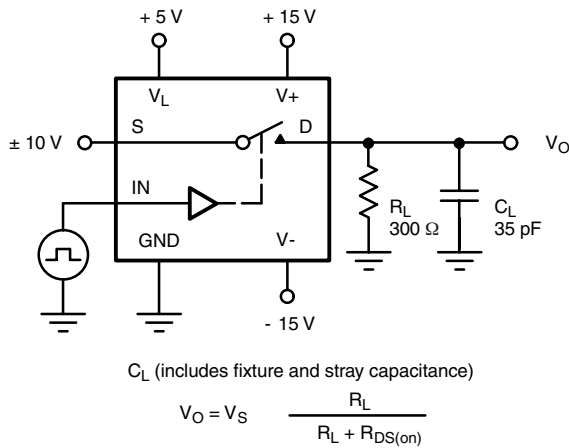


Switching Threshold vs. Supply Voltage

TYPICAL CHARACTERISTICS 25 °C, $V_L = 5$ V, unless otherwise noted



TEST CIRCUITS



Note: Logic input waveform is inverted for switches that have the opposite logic sense control

Figure 1. Switching Time

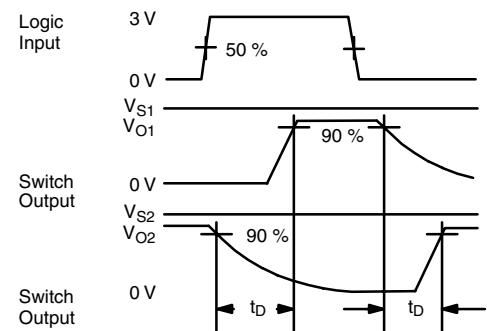
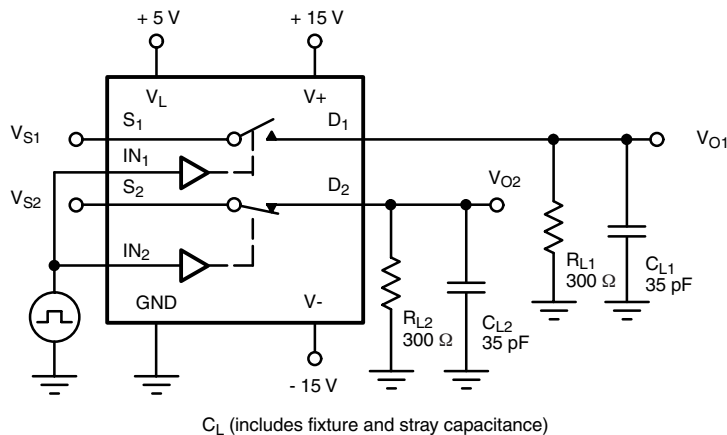


Figure 2. Break-Before-Make (DG453)

DG451/DG452/DG453

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TEST CIRCUITS

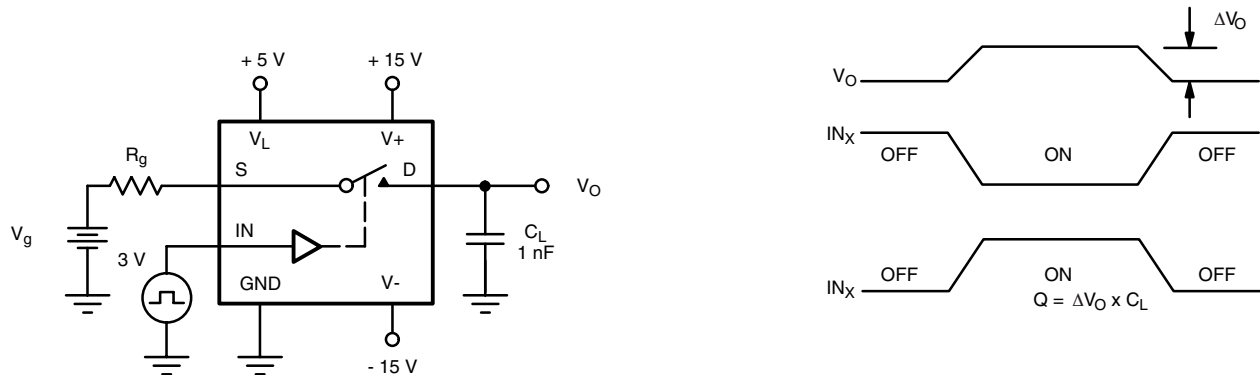


Figure 3. Charge Injection

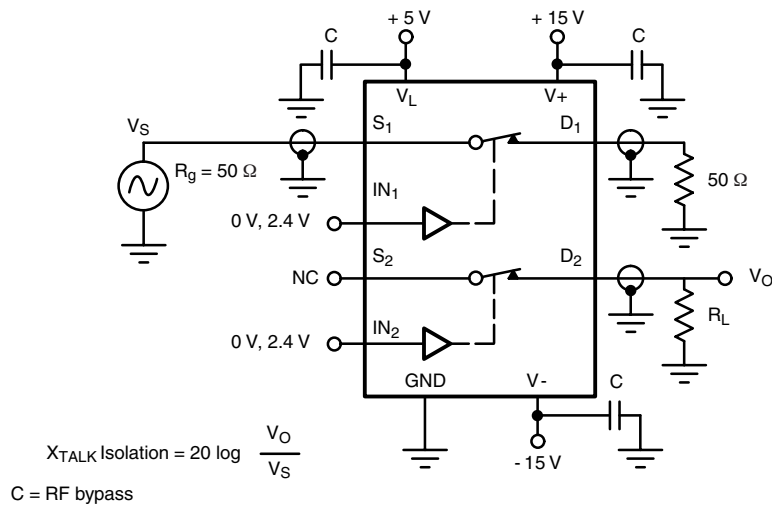


Figure 4. Crosstalk

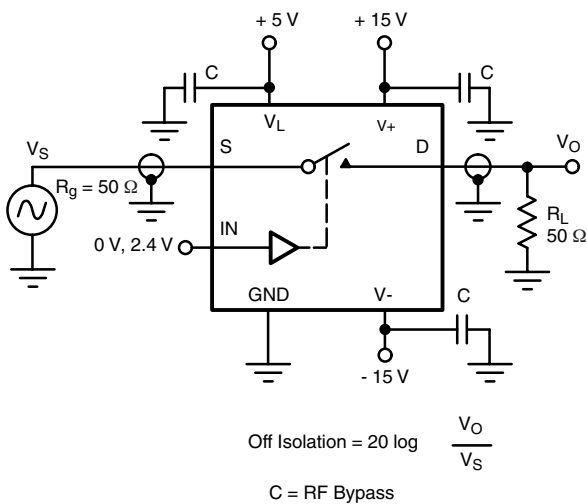


Figure 5. Off-Isolation

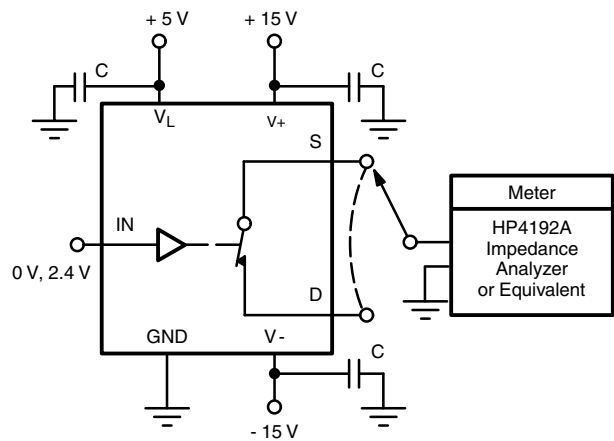


Figure 6. Source/Drain Capacitances

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