

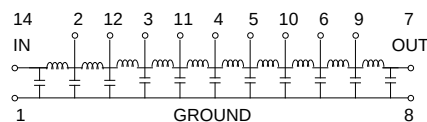
14 Pin DIP Passive Delay Lines

Zo OHMS ±10%	DELAY nS ±5% or ± 2nS†	TAP DELAYS nS ±10% or ± 0.5nS†	RISE TIME nS Max.	ATTEN DB Max.	PCA PART NUBER	Zo OHMS ±10%	DELAY nS ±5% or ± 2nS†	TAP DELAYS nS ± 10% or ± 0.5nS†	RISE TIME nS Max.	ATTEN DB Max.	PCA PART NUMBER
50	10	1	3	1	EP6400-1	200	100	10	20	1	EP6400-8
50	25	2.5	5	1	EP6400-2	200	200	20	40	1	EP6400-9
50	50	5 ±1	10	1	EP6400-3	250	250	25	50	1	EP6400-16
100	20	2	4	1	EP6400-4	300	60	6 ±1	12	1	EP6400-10
100	50	5 ±1	10	1	EP6400-5	300	150	15	30	1	EP6400-11
100	100	10	20	1	EP6400-6	300	300	30	60	1	EP6400-12
100	250	25	50	1	EP6400-18	500	100	10	20	1	EP6400-13
200	20	2	4	1	EP6400-17	500	250	25	50	2	EP6400-14
200	40	4	8	1	EP6400-7	500	500	50	100	2	EP6400-15

†Which ever is greater.

DC Electrical Characteristics	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay	1K	100	PPM/°C
Insulation Resistance @ 100 Vdc		100	Meg Ohms
Dielectric Strength			Vdc

Schematic



Recommended Operating Conditions	Min	Max	Unit
P _W *	Pulse Width % of Total Delay	200	%
D*	Duty Cycle	40	%
T _A	Operating Free Air Temperature	0	70 °C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25°C		
V _{IN}	Pulse Input Voltage	3 Volts
PW	Pulse Width % of Total Delay	300 %
T _{RI}	Input Rise Time (10 - 90%)	2.0 nS
PRR	Pulse Repetition Rate @ T _d ≤ 150 nS	1.0 MHz
	Pulse Repetition Rate @ T _d > 150 nS	300 KHz

Package Dimensions

