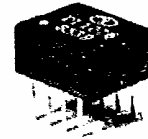


SERIES DL1060 8 PIN DIGITAL DELAY MODULES

- 8 Pin DIL Package • Low Profile Encapsulation Construction • TTL and DTL Compatible
- 5 Equally Spaced Taps

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C



Input Test Conditions

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay Nanosecond	Tap to Tap Delay NSEC (1)	Rise Time NSEC Max (2)
DL1061	25 $\pm$ 3	5 $\pm$ 0.6	3
DL1062	30 $\pm$ 3	6 $\pm$ 1	3
DL1063	35 $\pm$ 3	7 $\pm$ 1	3
DL1064	40 $\pm$ 3	8 $\pm$ 2	3
DL1065	45 $\pm$ 3	9 $\pm$ 2	3
DL1066	50 $\pm$ 3	10 $\pm$ 2	3
DL1067	75 $\pm$ 4	15 $\pm$ 3	3
DL1068	100 $\pm$ 5	20 $\pm$ 3	3
DL1069	125 $\pm$ 6.5	25 $\pm$ 3	4
DL1070	150 $\pm$ 7.5	30 $\pm$ 3	4
DL1071	175 $\pm$ 8	35 $\pm$ 4	4
DL1072	200 $\pm$ 10	40 $\pm$ 4	4
DL1073	225 $\pm$ 11.25	45 $\pm$ 4	4
DL1074	250 $\pm$ 12.5	50 $\pm$ 5	4
DL1075	300 $\pm$ 15	60 $\pm$ 6	5
DL1076	350 $\pm$ 17.5	70 $\pm$ 7	5
DL1077	400 $\pm$ 20	80 $\pm$ 8	5
DL1078	450 $\pm$ 22.5	90 $\pm$ 9	5
DL1079	500 $\pm$ 25	100 $\pm$ 10	5

Note: (1) Measured at 1.5v level leading edge.
(2) Measured from 0.75v to 2.4v.

