

Single P-channel MOSFET

ELM34407AA-N

■ General description

ELM34407AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -30V$
- $I_d = -8A$
- $R_{ds(on)} < 32m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 55m\Omega$ ($V_{gs} = -4.5V$)

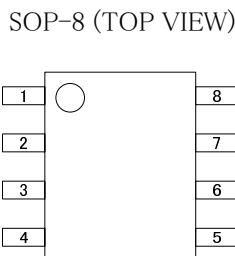
■ Maximum absolute ratings

Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-30	V	
Gate-source voltage	V_{gs}	± 25	V	
Continuous drain current	I_d	-8	A	
		-7		
Pulsed drain current	I_{dm}	-30	A	3
Power dissipation	P_d	2.5	W	
		1.3		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

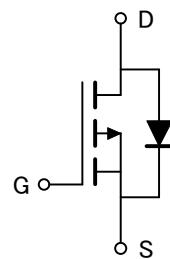
Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		25	°C/W	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		50	°C/W	

■ Pin configuration



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	$I_d=-250\ \mu A, V_{gs}=0V$	-30			V	
Zero gate voltage drain current	Idss	$V_{ds}=-24V, V_{gs}=0V$ $V_{ds}=-20V, V_{gs}=0V, T_j=125^\circ C$			-1 -10	μA	
Gate-body leakage current	Igss	$V_{ds}=0V, V_{gs}=\pm 25V$			± 100	nA	
Gate threshold voltage	Vgs(th)	$V_{ds}=V_{gs}, I_d=-250\ \mu A$	-0.8	-1.5	-2.5	V	
On state drain current	Id(on)	$V_{gs}=-10V, V_{ds}=-5V$	-30			A	1
Static drain-source on-resistance	Rds(on)	$V_{gs}=-10V, I_d=-8A$ $V_{gs}=-4.5V, I_d=-6A$		26 44	32 55	$m\Omega$ $m\Omega$	1
Forward transconductance	Gfs	$V_{ds}=-10V, I_d=-6A$		7		S	1
Diode forward voltage	Vsd	$I_s=-1A, V_{gs}=0V$			-1	V	1
Max. body-diode continuous current	Is				-3	A	
Pulsed body-diode current	Ism				-6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	$V_{gs}=0V, V_{ds}=-15V, f=1MHz$			920		pF
Output capacitance	Coss				190		pF
Reverse transfer capacitance	Crss				120		pF
SWITCHING PARAMETERS							
Total gate charge	Qg	$V_{gs}=-10V, V_{ds}=-15V$ $I_d=-6A$			18.5		nC
Gate-source charge	Qgs				2.7		nC
Gate-drain charge	Qgd				4.5		nC
Turn-on delay time	td(on)	$V_{gs}=-10V, V_{ds}=-10V$ $I_d \approx -1A, R_{gen}=3\ \Omega$			7.7		ns
Turn-on rise time	tr				5.7		ns
Turn-off delay time	td(off)				20.0		ns
Turn-off fall time	tf				9.5		ns
Body diode reverse recovery charge	Qrr				7.9		nC

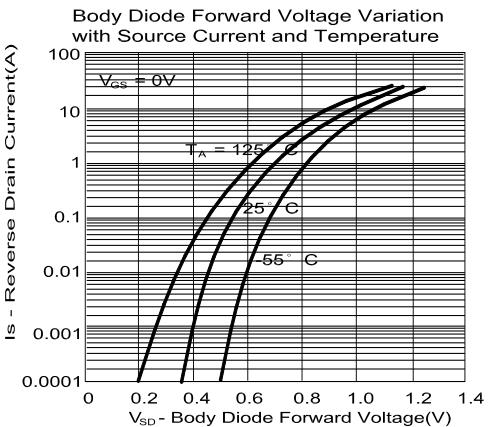
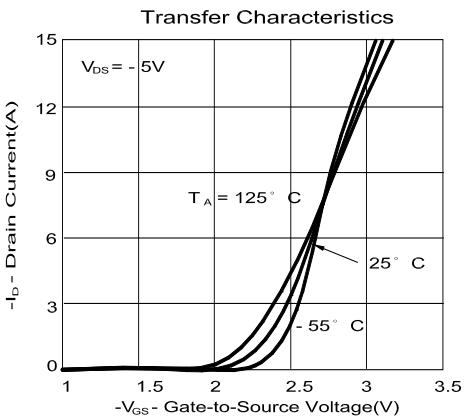
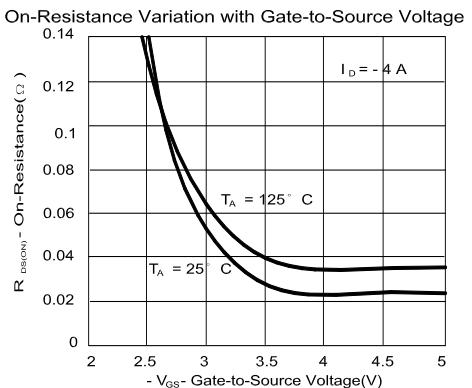
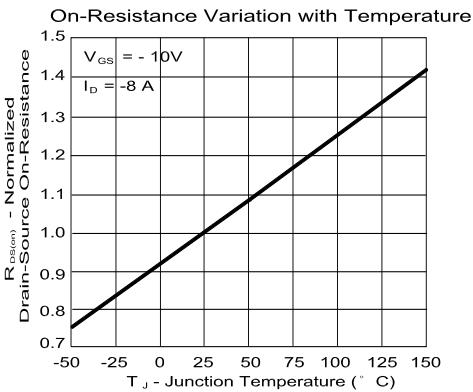
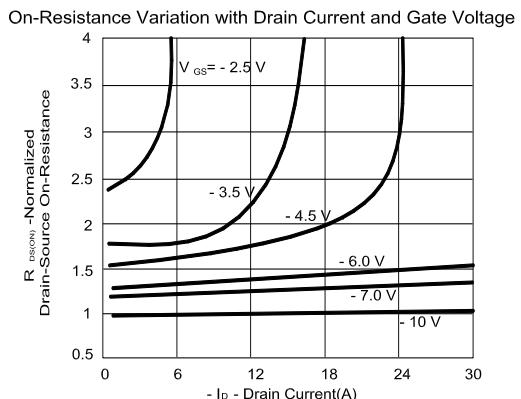
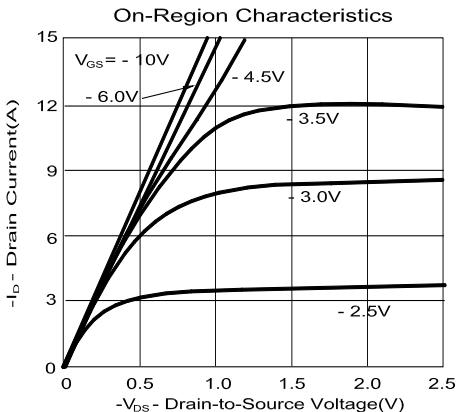
NOTE :

1. Pulsed width $\leq 300\ \mu sec$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

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■ Typical electrical and thermal characteristics



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