

Single P-channel MOSFET

ELM34411AA-N

General description

ELM34411AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

Features

- $V_{ds} = -30V$
- $I_d = -12A$
- $R_{ds(on)} < 14m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 22m\Omega$ ($V_{gs} = -4.5V$)

Maximum absolute ratings

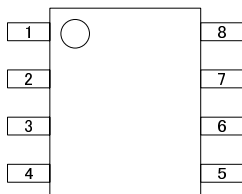
Parameter		Symbol	Limit	Unit	Note
Drain-source voltage		V_{ds}	-30	V	
Gate-source voltage		V_{gs}	± 25	V	
Continuous drain current	$T_a = 25^\circ C$	I_d	-12	A	
	$T_a = 70^\circ C$		-9		
Pulsed drain current		I_{dm}	-50	A	3
Power dissipation	$T_a = 25^\circ C$	P_d	2.5	W	
	$T_a = 70^\circ C$		1.3		
Junction and storage temperature range		T_j, T_{stg}	-55 to 150	$^\circ C$	

Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		25	$^\circ C/W$	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		50	$^\circ C/W$	

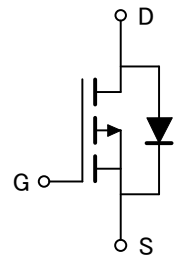
Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

Circuit



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Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=-250 μA, Vgs=0V	-30			V	
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V			-1	μ A	
		Vds=-20V, Vgs=0V, Tj=125°C			-10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±25V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250 μ A	-1.0	-1.5	-3.0	V	
On state drain current	Id(on)	Vgs=-10V, Vds=-5V	-50			A	1
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-12A		12	14	m Ω	1
		Vgs=-4.5V, Id=-9A		18	22	m Ω	
Forward transconductance	Gfs	Vds=-10V, Id=-12A		28		S	1
Diode forward voltage	Vsd	Is=If, Vgs=0V			-1.2	V	1
Max. body-diode continuous current	Is				-2.1	A	
Pulsed body-diode current	Ism				-4	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz		3000		pF	
Output capacitance	Coss			870		pF	
Reverse transfer capacitance	Crss			360		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=-10V, Vds=-15V Id=-12A		30	42	nC	2
Gate-source charge	Qgs			9		nC	2
Gate-drain charge	Qgd			11		nC	2
Turn-on delay time	td(on)	Vgs=-10V, Vds=-15V Id ≈ -1A, Rgen=6 Ω		12		ns	2
Turn-on rise time	tr			16		ns	2
Turn-off delay time	td(off)			50		ns	2
Turn-off fall time	tf			100		ns	2

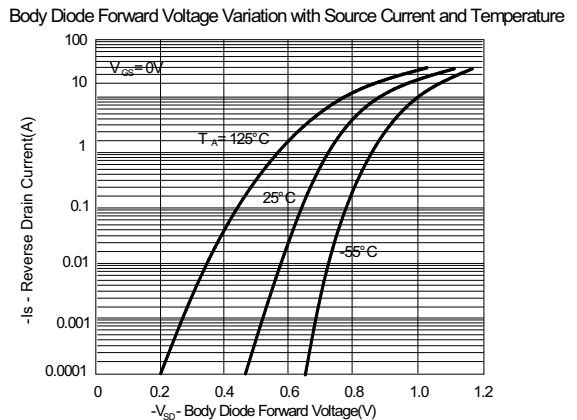
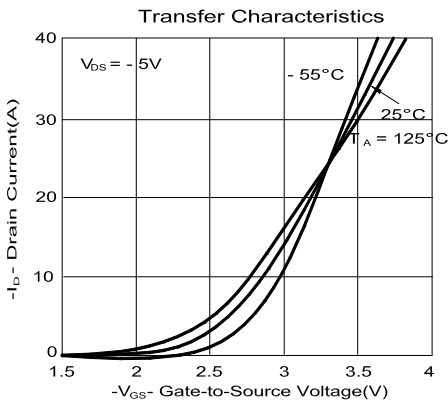
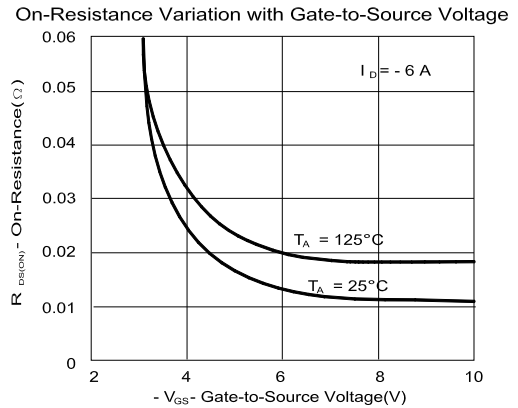
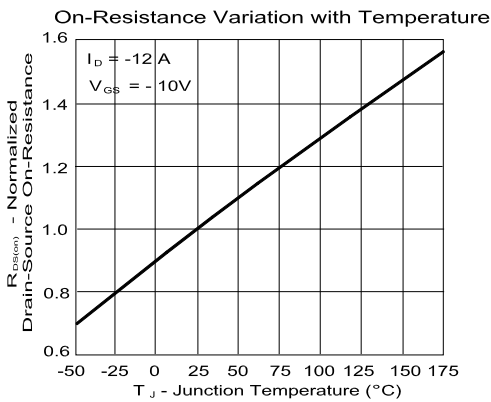
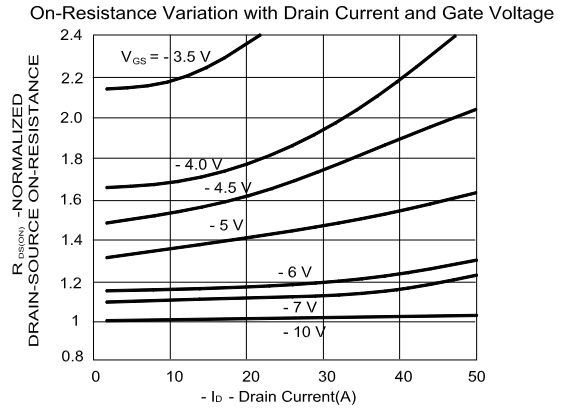
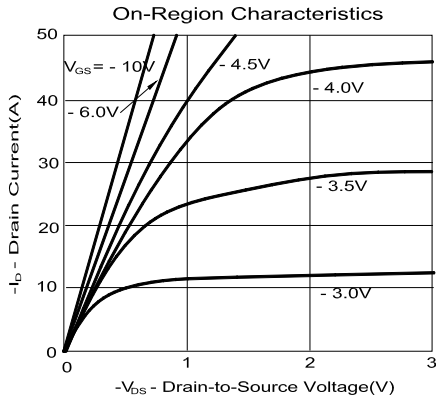
NOTE :

1. Pulsed width $\leq$ 300 μ sec and Duty cycle $\leq$ 2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq$ 1%.

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Typical electrical and thermal characteristics



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