

FSP3124

FEATURES

- Output voltage: 3.3V, 5.0V, 12V, and adjustable output version
- Adjustable version output voltage range: 1.23V to 18V $\pm$ 4%
- 150KHz $\pm$ 15% fixed switching frequency
- Voltage mode non-synchronous PWM control
- Thermal-shutdown and current-limit protection
- ON/OFF shutdown control input
- Operating voltage can be up to 22V
- Output load current: 3A
- Low power standby mode
- Built-in switching transistor on chip
- TO252-5L packages

APPLICATIONS

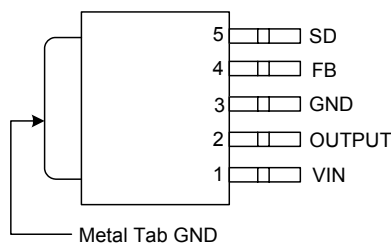
- Simple High-efficiency step-down regulator
- On-card switching regulators
- Positive to negative converter

GENERAL DESCRIPTION

The FSP3124 series are monolithic IC that design for a step-down DC/DC converter, and own the ability of driving a 3A load without additional transistor component. Due to reducing the number of external component, the board space can be saved easily. The external shutdown function can be controlled by logic level and then come into standby mode. The internal compensation makes feedback control have good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If current limit function occurred and V_{FB} is down to 0.5V below, the switching frequency will be reduced. The FSP3124 series operates at a switching frequency of 150 KHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Other features include a guaranteed $\pm$ 4% tolerance on output voltage under specified input voltage and output load conditions, and $\pm$ 15% on the oscillator frequency. The output version included fixed 3.3V, 5V, 12V, and an adjustable type. The package is available in a standard 5-lead TO-252 package.

PIN CONFIGURATION

(Top View)



Symbol	Name	Descriptions
1	Vin	Operating Voltage Input
2	Output	Switching Output
3	Gnd	Ground
4	FB	Output Voltage Feedback Control
5	SD	ON/OFF Shutdown

■ ABSOLUTE MAXIMUM RATINGS

Symbol	Paramrter	Rating	Unit
V_{CC}	Supply Voltage	+24	V
V_{SD}	ON/OFF Pin input Voltage	-0.3 to +18	V
V_{FB}	Feedback Pin Voltage	-0.3 to +18	V
V_{OUT}	Output Voltage to Ground	-1	V
P_D	Power Dissipation	Internally Limited	W
T_{ST}	Storage Temperature	-65 to +150	°C
T_{OP}	Operating Temperature	-40 to +125	°C
V_{OP}	Operating Voltage	+4.5 to +22	V

■ ELECTRICAL CHARACTERISTICS (ALL OUTPUT VOLTAGE VERSIONS)

Unless otherwise specified, $V_{IN}=12V$ for 3.3V,5V, adjustable version and $V_{IN}=18V$ for the 12V version. $I_{LOAD}=0.5A$

Symbol	Paramrter		Conditions		Min.	Typ.	Max.	Unit
I _B	Feedback bias current		V _{FB} =1.3V(Adjustable version only)			-10	-50 -100	nA
F _{OSC}	Oscillator frequency				127 110	150	173 173	KHz
F _{CSP}	Short Circuit Oscillator Frequency		When Current Limit Occur and V _{FB} < 0.5V, T _A =25°C		10	30	50	kHz
V _{SAT}	Saturation voltage		I _{OUT} =3A no outside circuit V _{FB} =0V force driver on				1.4 1.5	V
DC	Max.Duty Cycle(ON)		V _{FB} =0V force driver on				100	%
	Min.Duty Cycle(OFF)		V _{FB} =12V force driver off		0			
I _{CL}	Current limit		Peak current no outside circuit V _{FB} =0V force driver on	3.6		6.9	A	
				3.4		7.5		
I _L	Output=0	Output leakage current	no outside circuit V _{FB} =12V force driver off			-50	μA	
	Output=-1		V _{IN} =22V			-30	mA	
I _Q	Quiescent Current		V _{FB} =12V force driver off			5	10	mA
I _{STBY}	Standby Quiesient Current		ON/OFF Pin=5V V _{IN} =22V			70	200 250	μA
V _{IL}	ON/OFF pin logic input threshold voltage		Low(regultaor ON)			1.3	0.6	V
V _{IH}			High(regultaor OFF)				2.0	
I _H	ON/OFF pin logic input current		V _{LOGIC} =2.5V(OFF)				-15	μA
I _L	ON/OFF pin input current		V _{LOGIC} =0.5V(ON)				-5	
θ _{JC}	Thermal Resistance		TO252-5L	Junction to case		10		°C/W
θ _{JA}	Thermal Resistance with Copper Area of Approximately 3 in ²		TO252-5L	Junction to ambient		50		°C/W

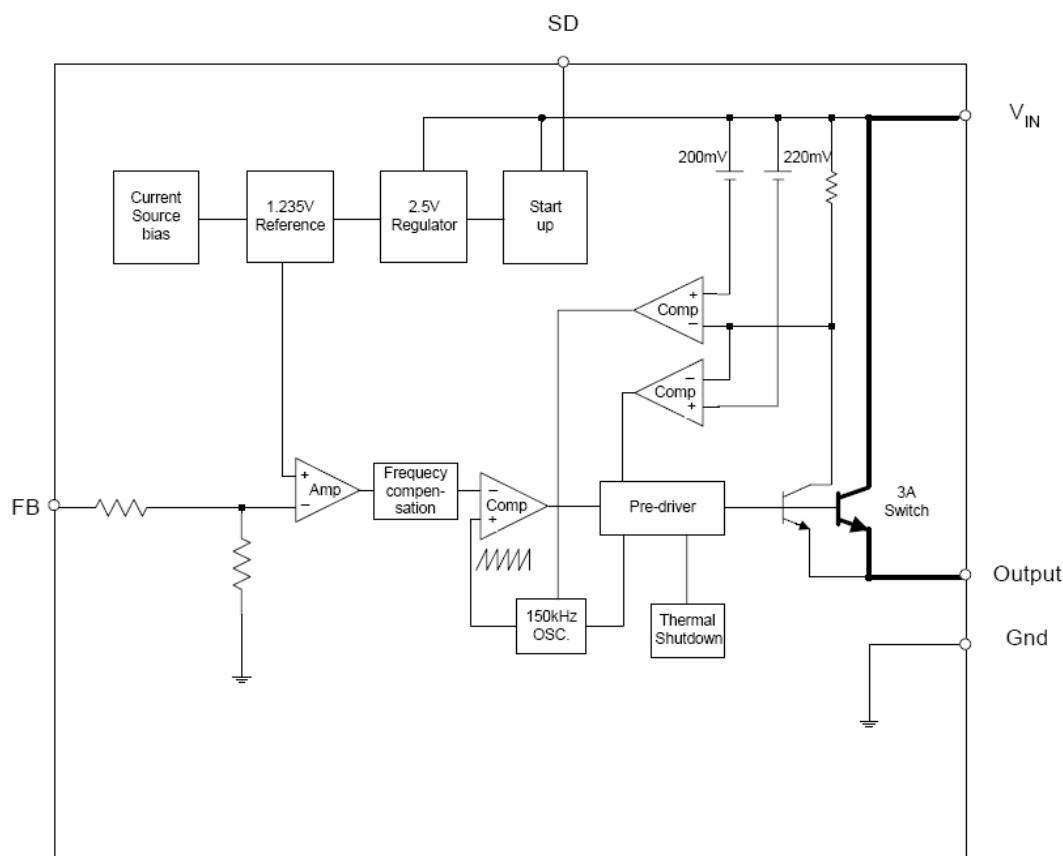
Specifications with **boldface type** are for full operating temperature range, the other type are for $T_J=25^\circ C$.

■ ELECTRICAL CHARACTERISTICS (CONTINUED)

	Symbol	Parameter	Conditions	Typ.	Limit	Unit
FSP3124-ADJ	V _{FB}	Output Feedback	5V≤V _{IN} ≤22V 0.2A≤I _{LOAD} ≤3A V _{OUT} programmed for 3V	1.23	1.193/ 1.18 1.267/ 1.28	V V _{MIN} V _{MAX}
	η	Efficiency	V _{IN} =12V, I _{LOAD} =3A	75		%
FSP3124-3.3V	V _{OUT}	Output voltage	5.5V≤V _{IN} ≤22V 0.2A≤I _{LOAD} ≤3A	3.3	3.168/ 3.135 3.432/ 3.465	V V _{MIN} V _{MAX}
	η	Efficiency	V _{IN} =12V, I _{LOAD} =3A	75		%
FSP3124-5.0V	V _{OUT}	Output voltage	8V≤V _{IN} ≤22V 0.2A≤I _{LOAD} ≤3A	5	4.8/ 4.75 5.2/ 5.25	V V _{MIN} V _{MAX}
	η	Efficiency	V _{IN} =12V, I _{LOAD} =3A	80		%
FSP3124-12V	V _{OUT}	Output voltage	15V≤V _{IN} ≤22V 0.2A≤I _{LOAD} ≤3A	12	11.52/ 11.4 12.48/ 12.6	V V _{MIN} V _{MAX}
	η	Efficiency	V _{IN} =16V, I _{LOAD} =3A	90		%

Specifications with **boldface type** are for full operating temperature range, the other type are for T_J=25°C.

■ BLOCK DIAGRAM



■ FUNCTION DESCRIPTION

Pin Function

V_{IN}
This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be present at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

Ground

Circuit ground.

Output

Internal switch. The voltage at this pin switches between $(V_{IN} - V_{SAT})$ and approximately $-0.5V$, with a duty cycle of approximately V_{OUT} / V_{IN} . To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be kept a minimum.

Feedback

Senses the regulated output voltage to complete the feedback loop.

ON/OFF

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 150uA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of 18V) shuts the regulator down. If this shutdown feature is not needed, the ON/OFF pin can be wired to the ground pin.

Thermal Considerations

The TO-252 surface mount package tab is designed to be soldered to the copper on a printed circuit board. The copper and the board are the heat sink for this package and the other heat producing components, such as the catch diode and inductor. The PC board copper area that the package is soldered to should be at least 0.4 in², and ideally should have 2 or more square inches of 2 oz. Additional copper area improves the thermal characteristics, but with copper areas greater than approximately 6 in², only small improvements in heat dissipation are realized. If further thermal improvements are needed, double sided, multilayer PC board with large copper areas and/or airflow are recommended.

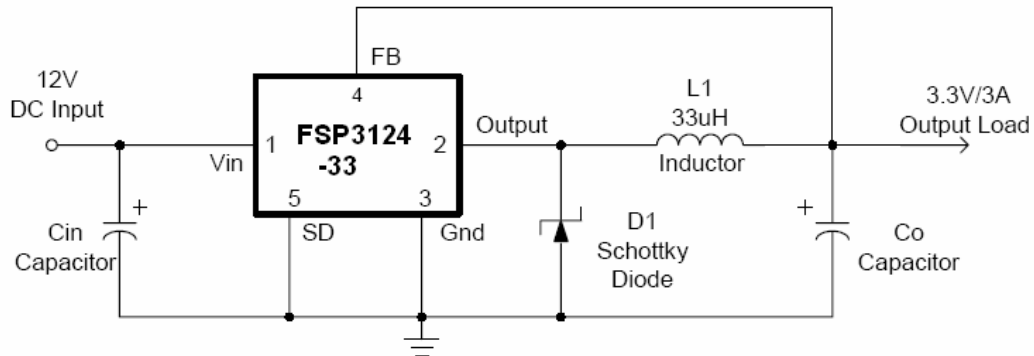
The FSP3124 (TO-252 package) junction temperature rise above ambient temperature with a 3A load for various input and output voltages. This data was taken with the circuit operating as a buck switching regulator with all components mounted on a PC board to simulate the junction temperature under actual operating conditions. This curve can be used for a quick check for the approximate junction temperature for various conditions, but be aware that there are many factors that can affect the junction temperature. When load currents higher than 2A are used, double sided or multilayer PC boards with large copper areas and/or airflow might be needed, especially for high ambient temperatures and high output voltages.

For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper should be used in the board layout. (Once exception to this is the output (switch) pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

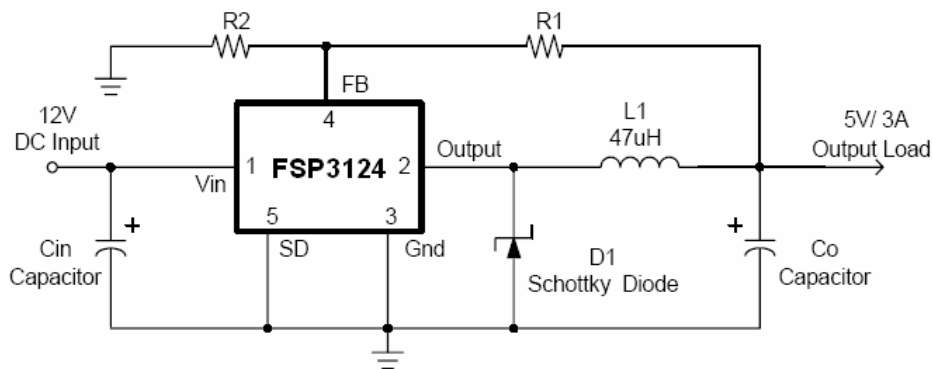
Package thermal resistance and junction temperature rise numbers are all approximate, and there are many factors that will affect these numbers. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multilayer board and the amount of solder on the board. The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

■ TYPICAL APPLICATION CIRCUITS

(1) Fixed Output Circuit

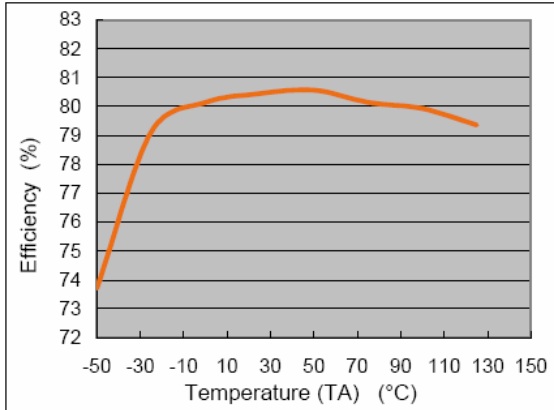


(2) Adjustable Output Circuit

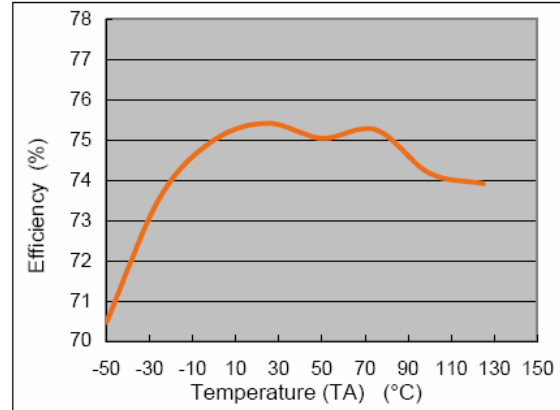


TYPICAL PERFORMANCE CHARACTERISTICS

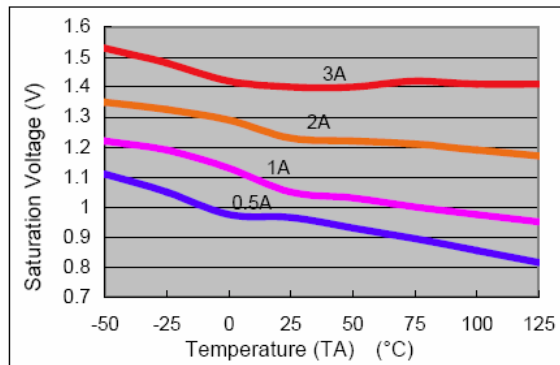
FSP3124 Efficiency v.s. Temperature
($V_{in}=12V, V_{out}=5V, I_o=3A$)



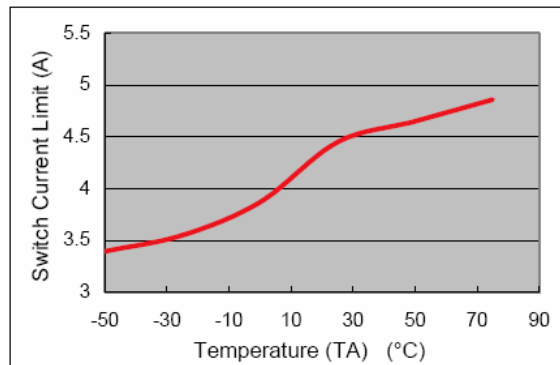
FSP3124 Efficiency v.s. Temperature
($V_{in}=12V, V_{out}=3.3V, I_o=3A$)



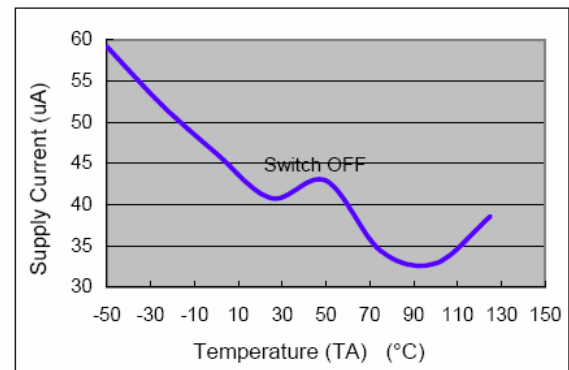
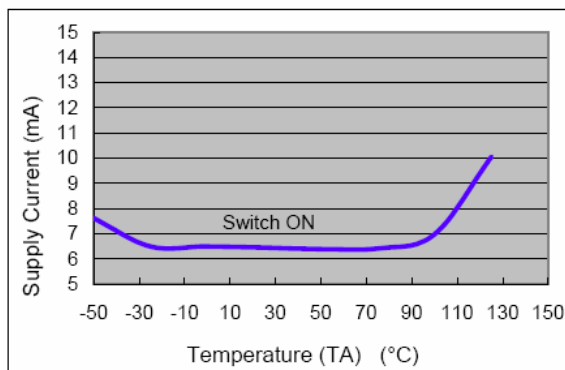
FSP3124 Saturation Voltage v.s. Temperature
($V_{cc}=12V, V_{fb}=0V, V_{SD}=0$)



FSP3124 Switch Current Limit v.s. Temperature
($V_{cc}=12V, V_{fb}=0V$)

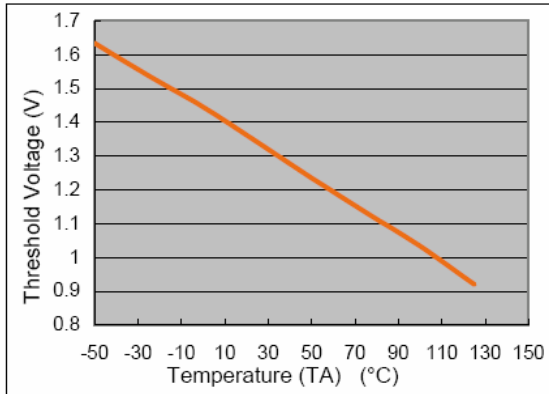


FSP3124 Supply Current v.s. Temperature
($V_{cc}=12V$, No Load, $V_{on/off}=0V$ (Switch ON), $V_{on/off}=5V$ (Switch OFF))

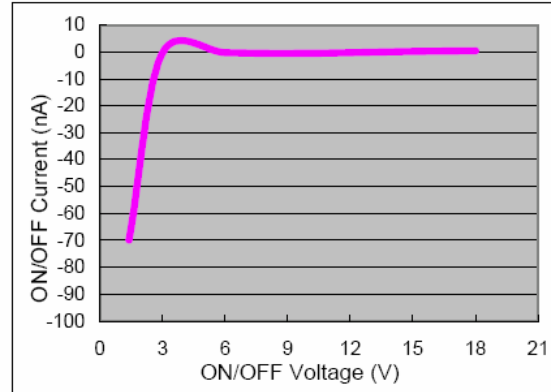


■ TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

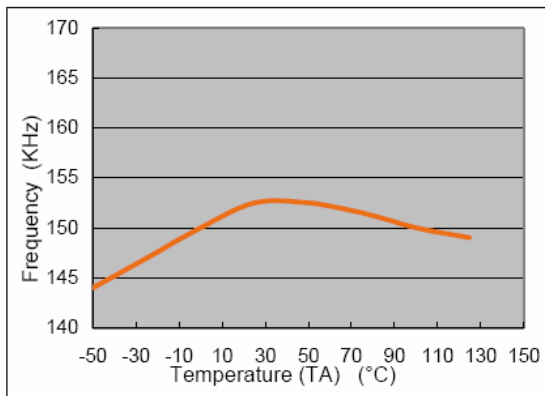
FSP3124 Threshold Voltage v.s. Temperature
(V_{cc}=12V, I_o=100mA)



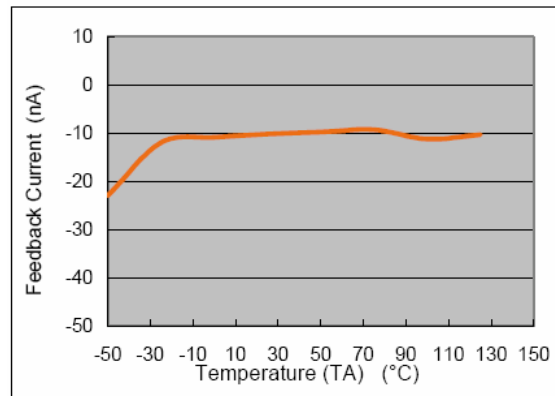
FSP3124 ON/OFF Current v.s. ON/OFF Voltage
(V_{in}=12V)



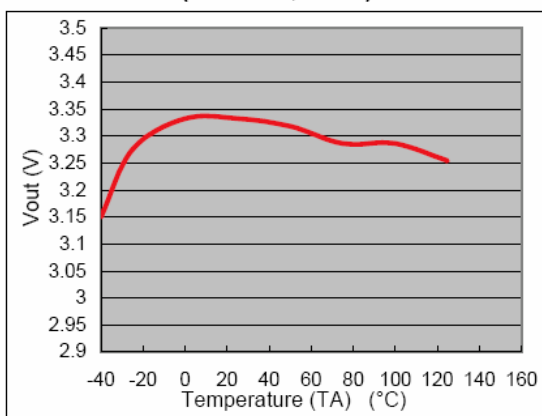
FSP3124 Frequency v.s. Temperature
(V_{cc}=12V, I_o=500mA, V_{out}=5V)



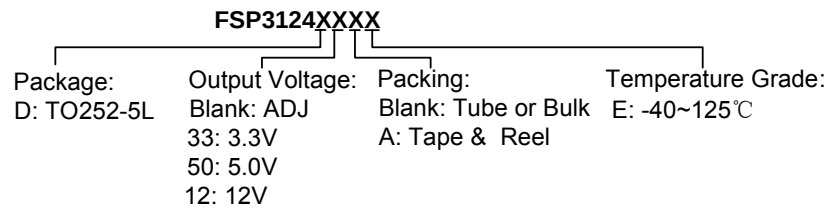
FSP3124 Feedback Current v.s. Temperature
(V_{cc}=12V, V_{out}=5V, V_{fb}=1.3V)



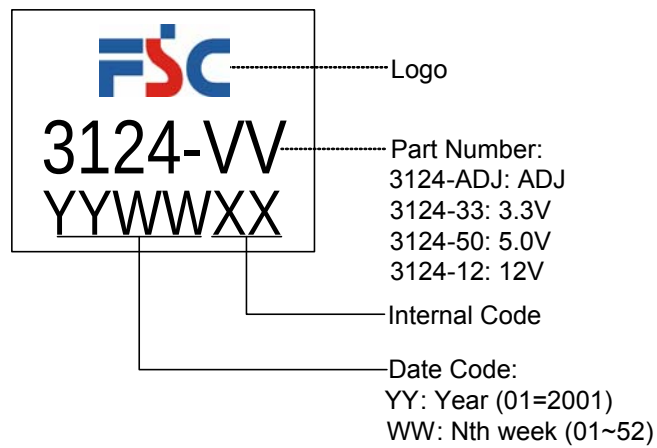
FSP3124 Output Voltage v.s. Temperature
(V_{in}=12V, I_o=3A)



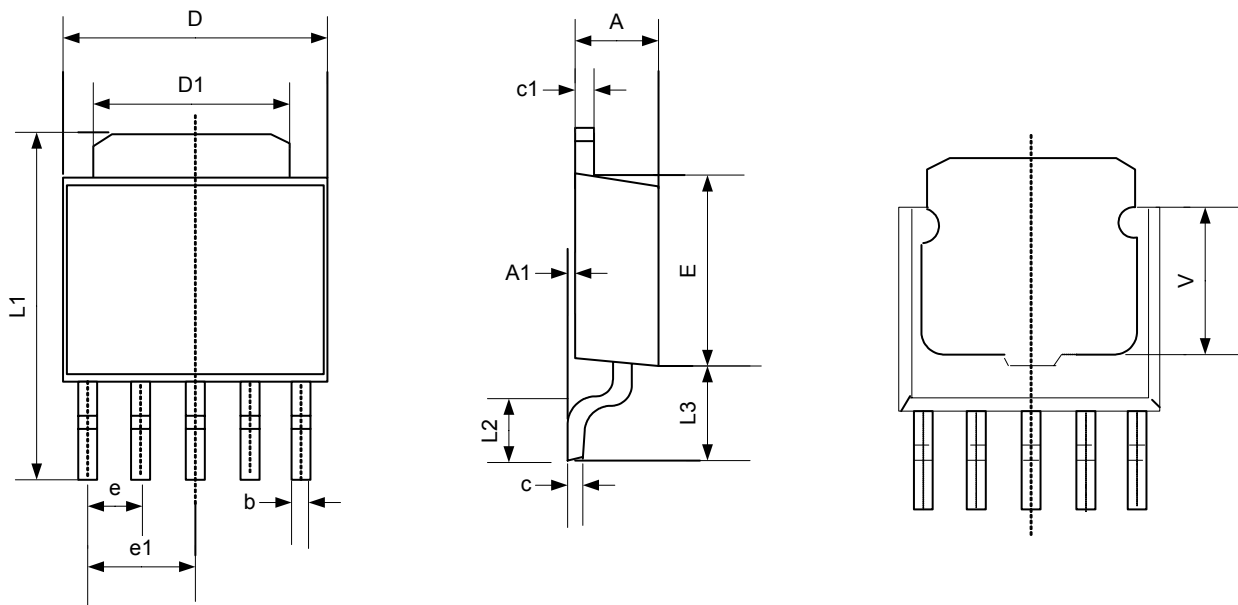
ORDERING INFORMATION



MARKING INFORMATION



■ PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.150	0.000	0.006
b	0.400	0.630	0.016	0.025
c	0.450	0.580	0.018	0.023
c1	0.450	0.580	0.018	0.023
D	6.350	6.850	0.250	0.270
D1	5.200	5.500	0.205	0.217
E	5.400	6.200	0.213	0.244
e	1.270TYP.		0.050TYP.	
e1	2.540TYP.		1.000TYP.	
L1	9.000	11.300	0.360	0.452
L2	0.900	1.630	0.035	0.064
L3	2.200	2.800	0.087	0.110
V	3.800REF		0.150REF	