

HAT1054R

Silicon P Channel Power MOS FET
High Speed Power Switching

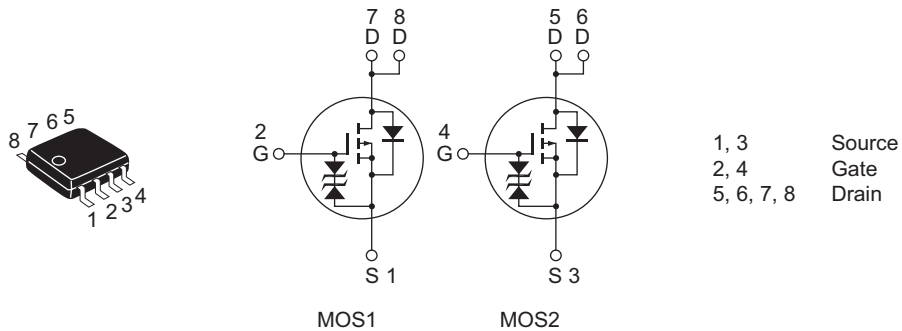
REJ03G1154-0300
(Previous: ADE-208-1224A)
Rev.3.00
Sep 07, 2005

Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

Outline

RENESAS Package code: PRSP0008DD-D
(Package name: SOP-8 <FP-8DAV>)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DS}	-20	V
Gate to source voltage	V_{GS}	±12	V
Drain current	I_D	-6	A
Drain peak current	$I_{D(pulse)}$ ^{Note 1}	-48	A
Body-drain diode reverse drain current	I_{DR}	-6	A
Channel dissipation	P_{ch} ^{Note 2}	2	W
Channel dissipation	P_{ch} ^{Note 3}	3	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$ 2. 1 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), $PW \leq 10 s$ 3. 2 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), $PW \leq 10 s$

Electrical Characteristics

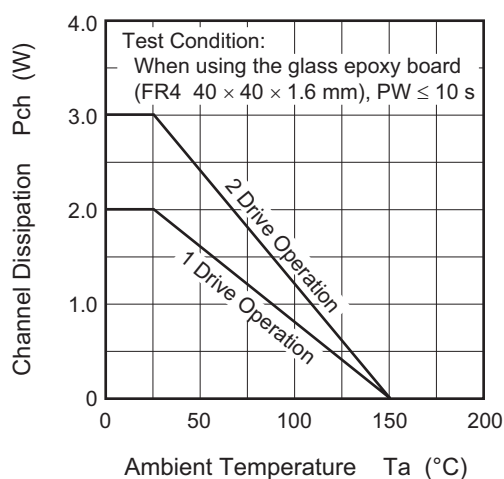
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR) DSS}$	-20	—	—	V	$I_D = -10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR) GSS}$	±12	—	—	V	$I_G = \pm 100 \mu A$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	±10	μA	$V_{GS} = \pm 10 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	-1	μA	$V_{DS} = -20 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.4	—	-1.4	V	$V_{DS} = -10 \text{ V}$, $I_D = -1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	24	30	mΩ	$I_D = -3 \text{ A}$, $V_{GS} = -4.5 \text{ V}$ ^{Note 4}
	$R_{DS(on)}$	—	35	50	mΩ	$I_D = -3 \text{ A}$, $V_{GS} = -2.5 \text{ V}$ ^{Note 4}
Forward transfer admittance	$ y_{fs} $	6	10	—	S	$I_D = -3 \text{ A}$, $V_{DS} = -10 \text{ V}$ ^{Note 4}
Input capacitance	C_{iss}	—	1550	—	pF	$V_{DS} = -10 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	400	—	pF	
Reverse transfer capacitance	C_{rss}	—	300	—	pF	
Total gate charge	Q_g	—	18	—	nC	$V_{DD} = -10 \text{ V}$ $V_{GS} = -4.5 \text{ V}$ $I_D = -6 \text{ A}$
Gate to source charge	Q_{gs}	—	3	—	nC	
Gate to drain charge	Q_{gd}	—	6.5	—	nC	
Turn-on delay time	$t_{d(on)}$	—	25	—	ns	$V_{GS} = -4.5 \text{ V}$, $I_D = -3 \text{ A}$, $V_{DD} \cong -10 \text{ V}$ $R_L = 3.3 \Omega$ $R_g = 4.7 \Omega$
Rise time	t_r	—	50	—	ns	
Turn-off delay time	$t_{d(off)}$	—	85	—	ns	
Fall time	t_f	—	40	—	ns	
Body-drain diode forward voltage	V_{DF}	—	-0.85	-1.10	V	$I_F = -6 \text{ A}$, $V_{GS} = 0$ ^{Note 4}
Body-drain diode reverse recovery time	t_{rr}	—	60	—	ns	$I_F = -6 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 20 \text{ A}/\mu s$

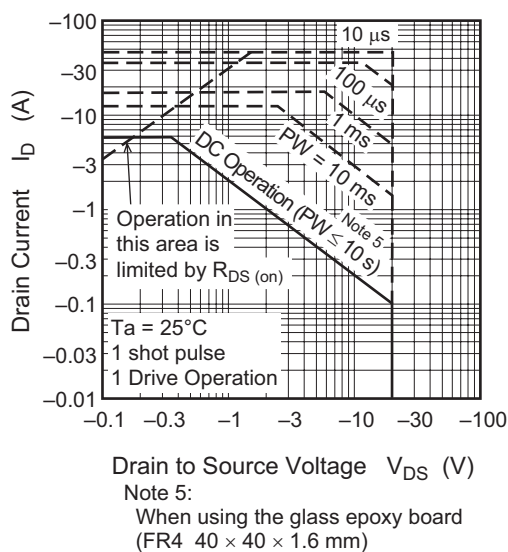
Note: 4. Pulse test

Main Characteristics

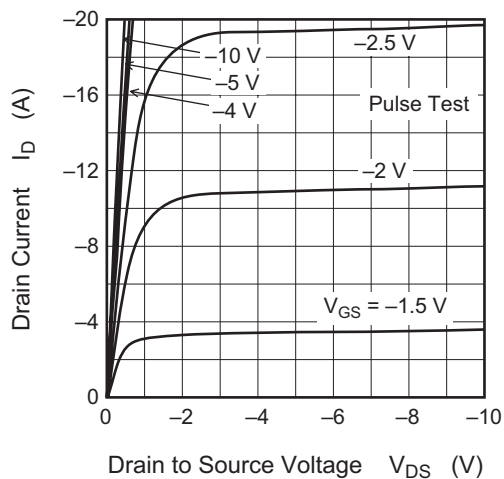
Power vs. Temperature Derating



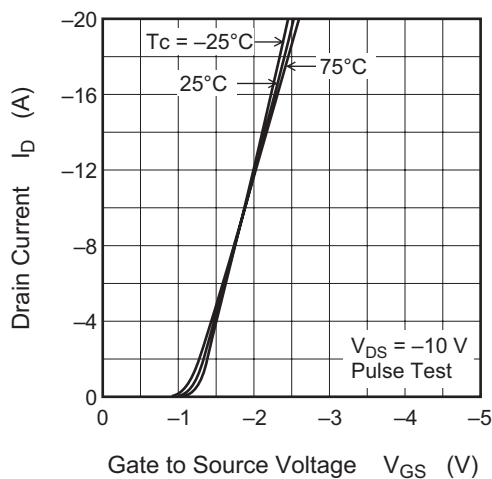
Maximum Safe Operation Area



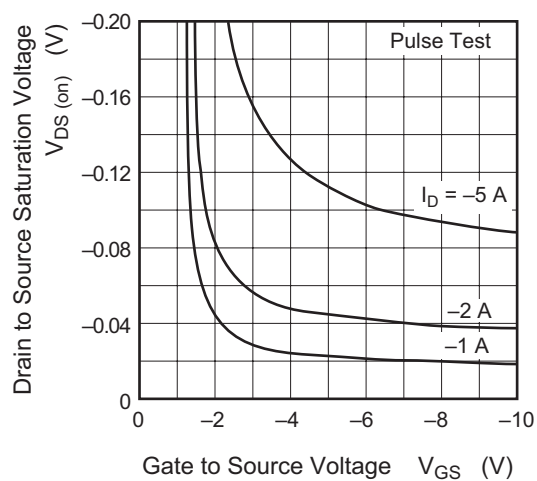
Typical Output Characteristics



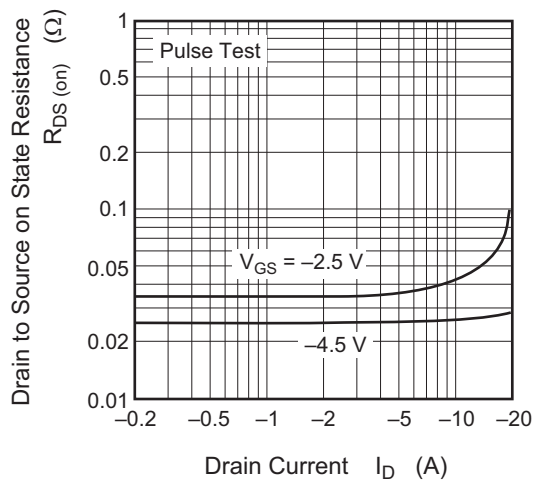
Typical Transfer Characteristics

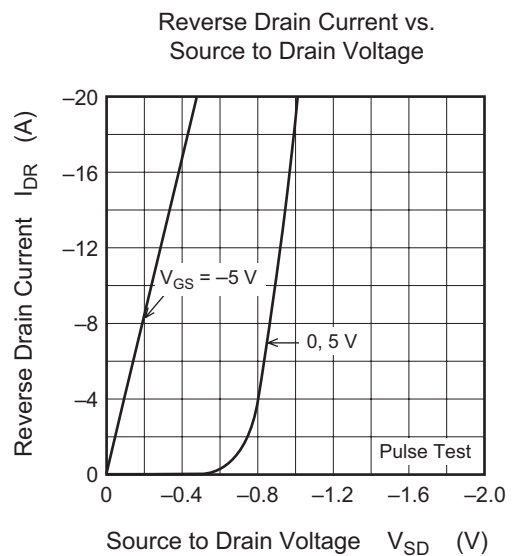
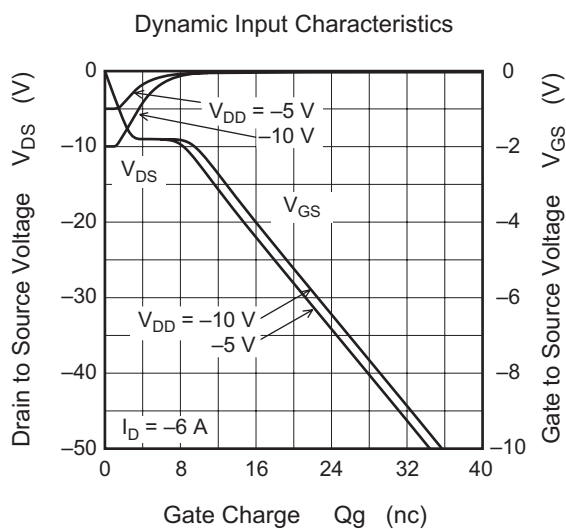
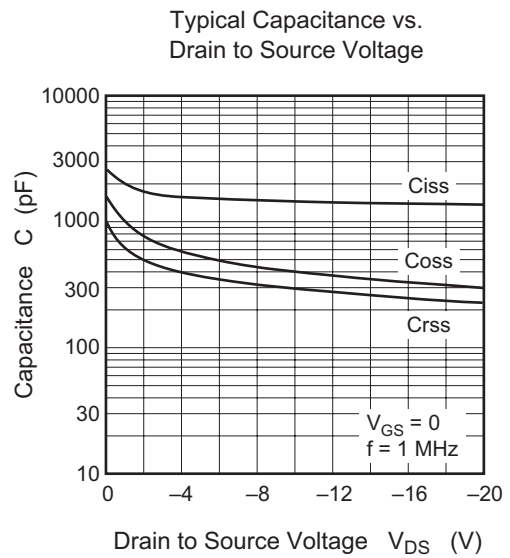
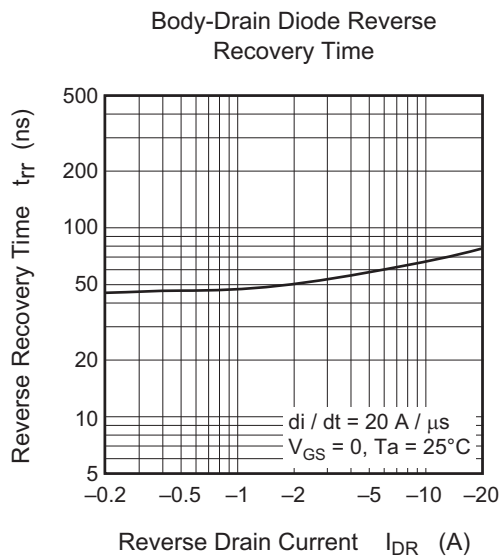
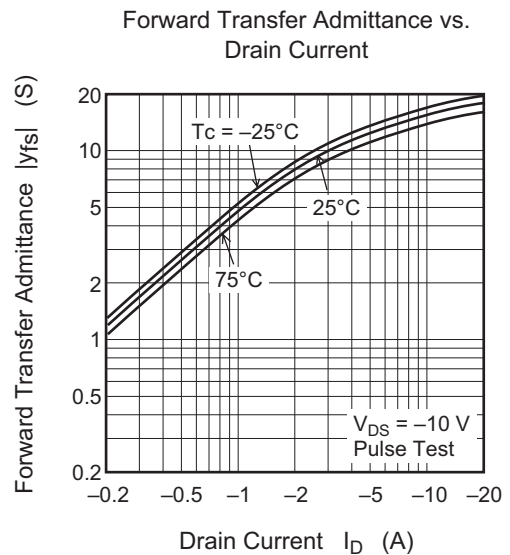
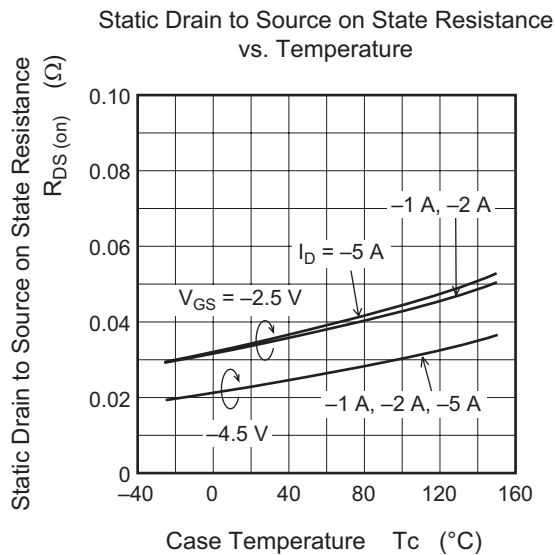


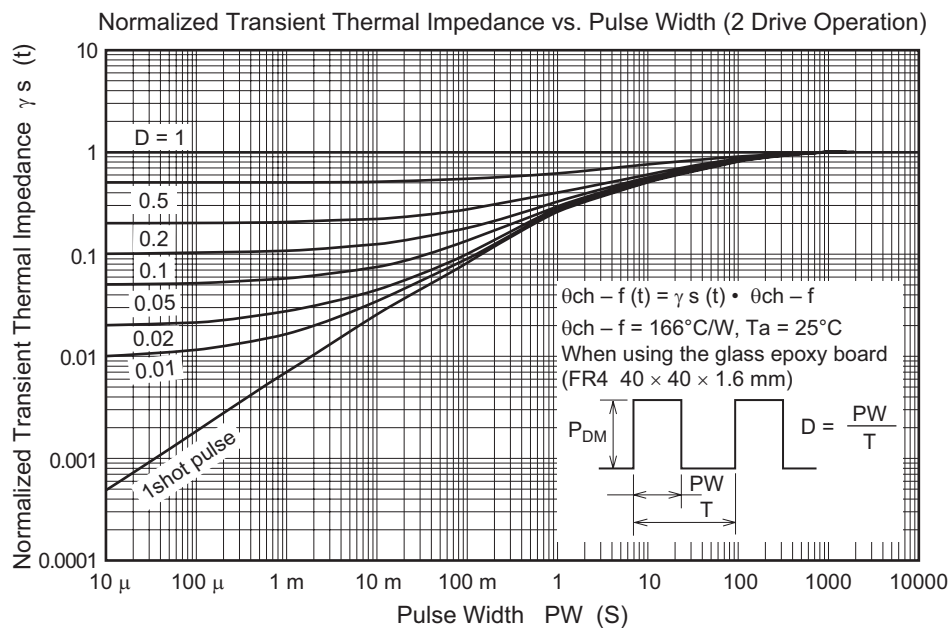
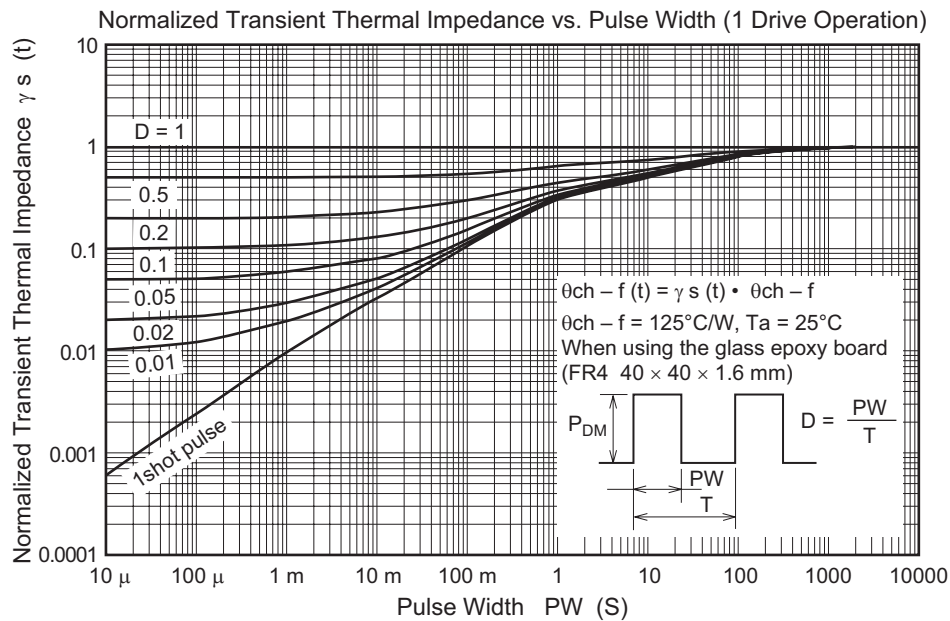
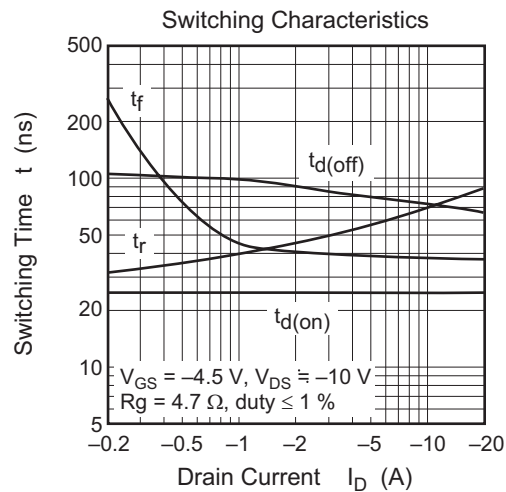
Drain to Source Saturation Voltage vs. Gate to Source Voltage



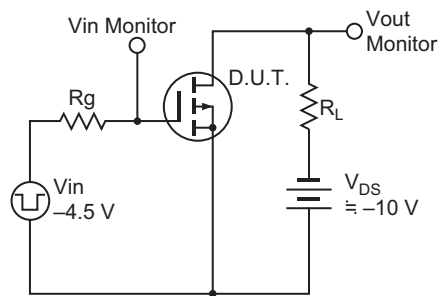
Static Drain to Source on State Resistance vs. Drain Current



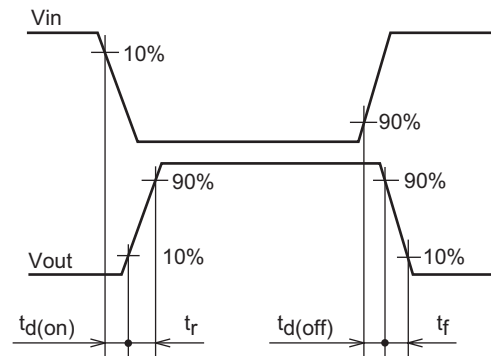




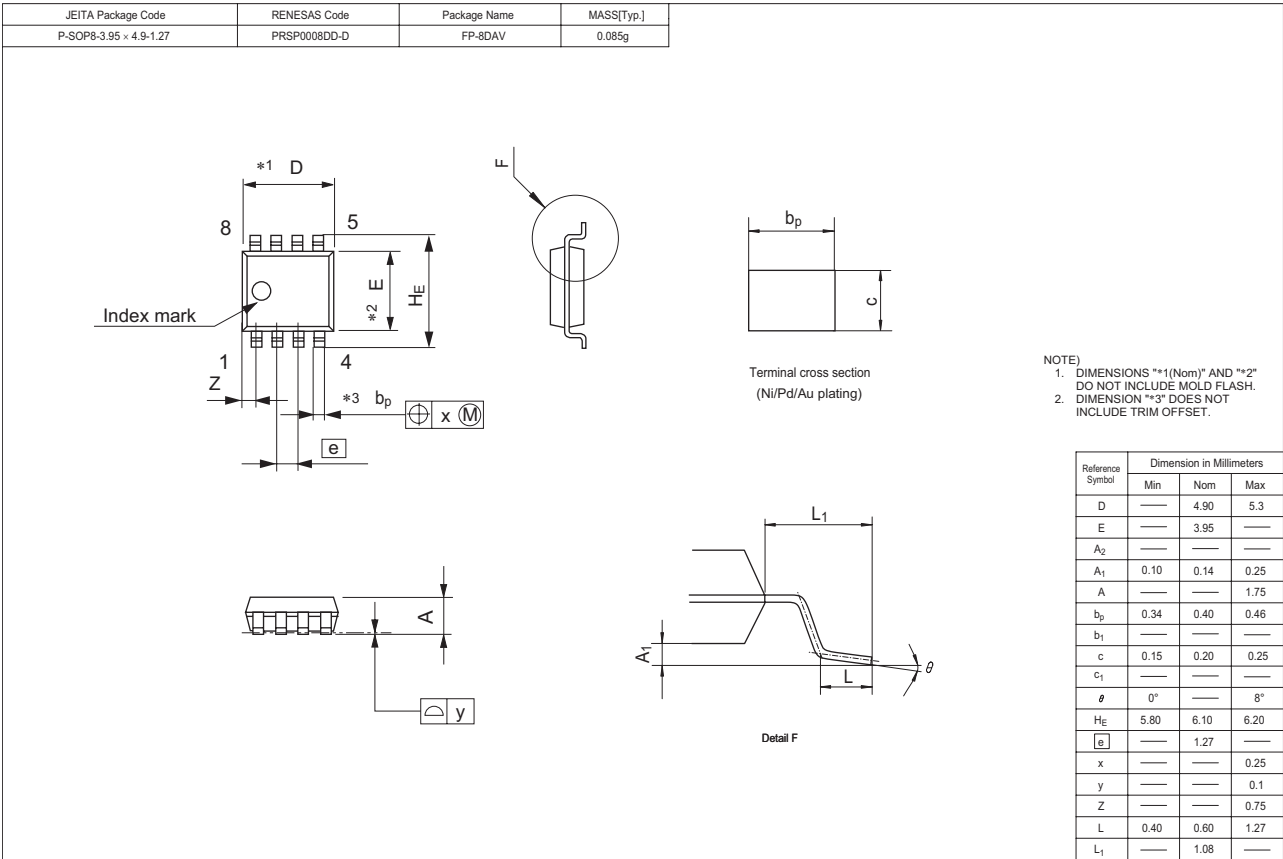
Switching Time Test Circuit



Switching Time Waveform



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT1054R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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