

HAT2168H

Silicon N Channel Power MOS FET Power Switching

REJ03G0046-0700 Rev.7.00 Sep 20, 2005

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 6 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

Outline

RENESAS Package code: PTZZ0005DA-A)
(Package name: LFPAK)

5
D
4
G
1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	30	A
Drain peak current	I _{D(pulse)} Note1	120	A
Body-drain diode reverse drain current	I _{DR}	30	A
Avalanche current	I _{AP} Note 2	15	A
Avalanche energy	E _{AR} Note 2	22	mJ
Channel dissipation	Pch Note3	15	W
Channel to Case Thermal Resistance	θch-C	8.33	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to + 150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

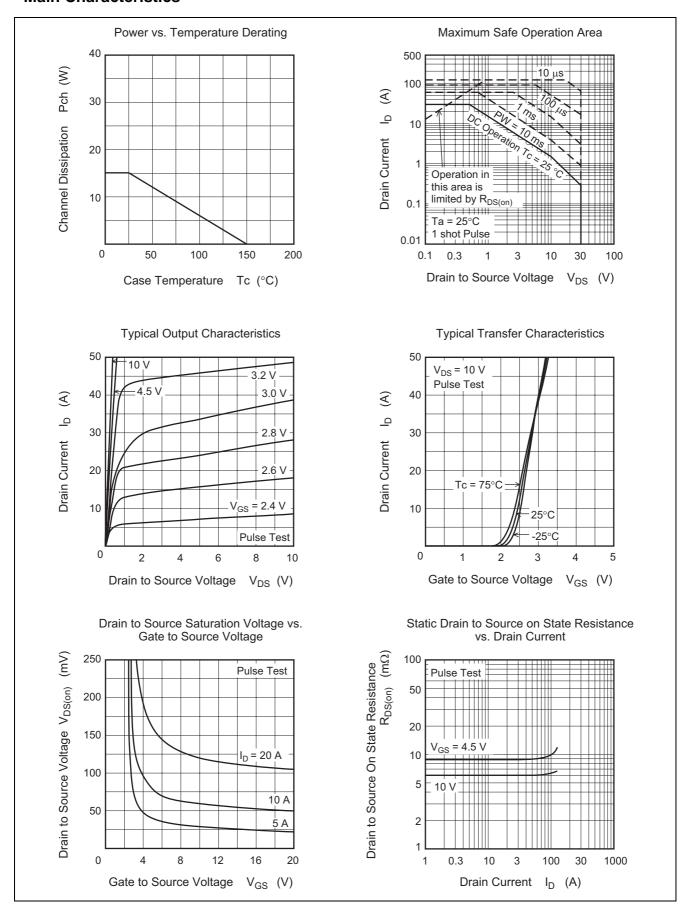
Electrical Characteristics

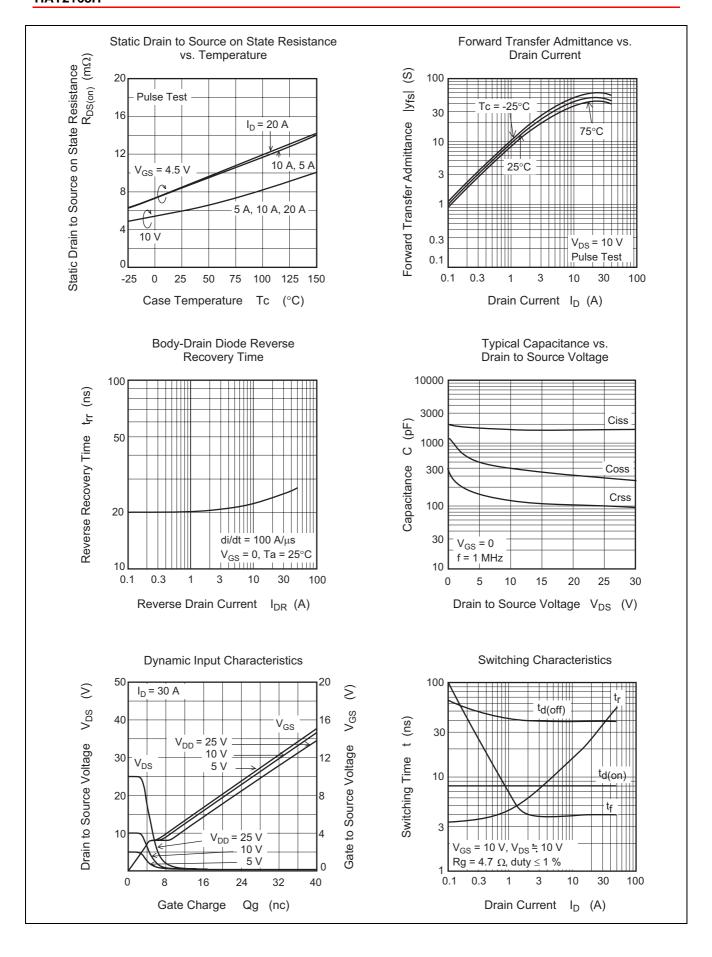
 $(Ta = 25^{\circ}C)$

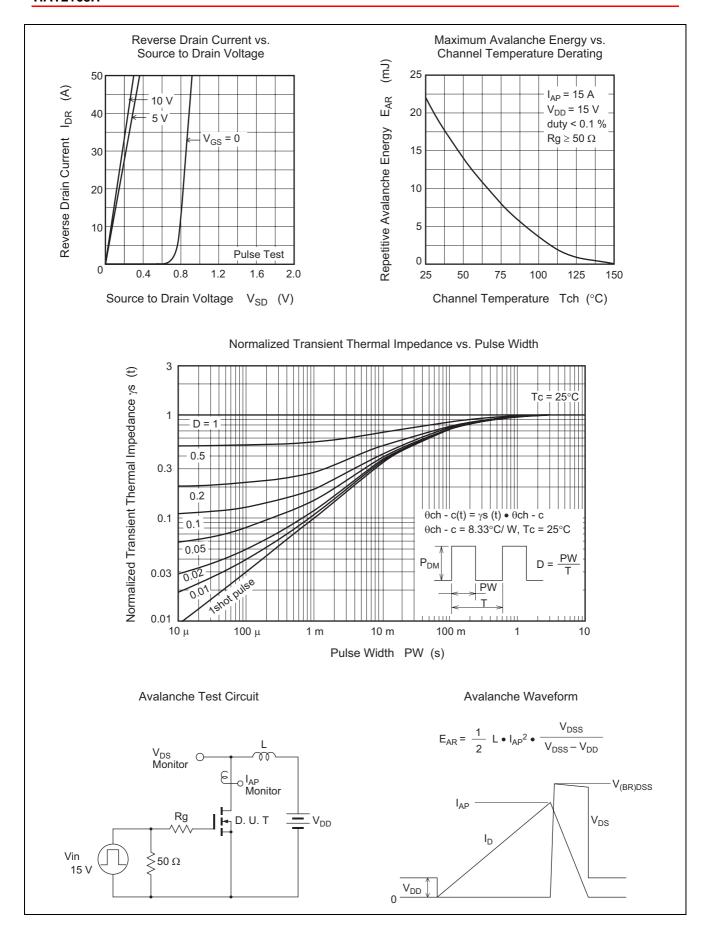
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20			٧	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_		±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_		1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0		2.5	>	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	6.0	7.9	$m\Omega$	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	8.8	13.5	mΩ	$I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	30	50	_	S	$I_D = 15 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	1730		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	400	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	130	_	pF	
Gate Resistance	Rg	_	0.55	_	Ω	
Total gate charge	Qg	_	11	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	5	_	nC	$I_D = 30 \text{ A}$
Gate to drain charge	Qgd	_	2.4	_	nC	
Turn-on delay time	t _{d(on)}	_	8	_	ns	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A},$
Rise time	t _r	_	20	_	ns	$V_{DD} \cong 10 \text{ V}, R_L = 0.67 \Omega,$
Turn-off delay time	t _{d(off)}	_	40	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	4	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.85	1.10	V	$IF = 30 A$, $V_{GS} = 0$ Note4
Body-drain diode reverse recovery	t _{rr}	_	25	_	ns	IF = 30 A, V _{GS} = 0
time						$di_F/dt = 100 \text{ A/} \mu\text{s}$

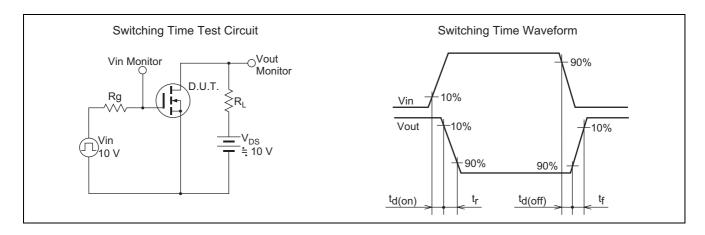
Notes: 4. Pulse test

Main Characteristics

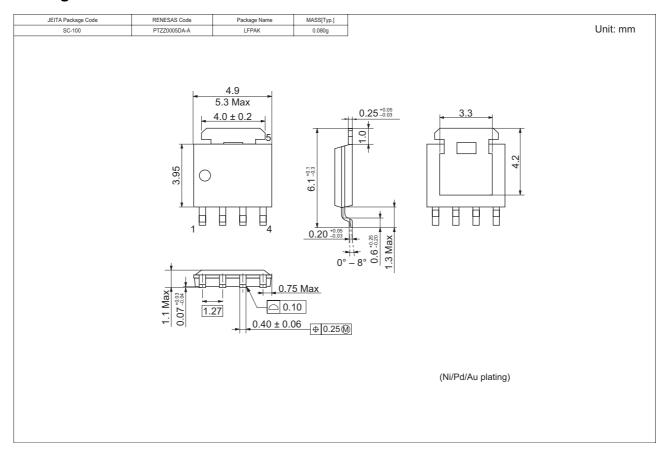








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2168H-EL-E	2500 pcs	Taping

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