# NSR0320MW2T1

# **Schottky Barrier Diodes**

These Schottky barrier diodes are designed for high current, handling capability, and low forward voltage performance.

## Features

- Low Forward Voltage 0.24 Volts (Typ) @  $I_F = 10 \text{ mAdc}$
- High Current Capability
- ESD Rating Human Body Model: CLASS 3B – Machine Model: C
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

## **MAXIMUM RATINGS** (T<sub>J</sub> = $125^{\circ}C$ unless otherwise noted)

| Symbol           | Value   | Unit  |
|------------------|---|---|
| V <sub>R</sub>   | 20  | Vdc   |
| V <sub>RM</sub>  | 23  | V   |
| P <sub>F</sub>   | 200<br>2.0  | mW<br>mW/°C   |
| ١ <sub>F</sub>   | 1   | A   |
| ١ <sub>F</sub>   | 5   | A   |
| TJ               | -55 to +125   | °C  |
| T <sub>stg</sub> | -55 to +150   | °C  |
|                  | V <sub>R</sub><br>V <sub>RM</sub><br>P <sub>F</sub><br>I <sub>F</sub><br>I <sub>F</sub><br>T <sub>J</sub> | $\begin{array}{c c} V_{R} & 20 \\ \hline V_{RM} & 23 \\ \hline P_{F} & 200 \\ 2.0 \\ \hline I_{F} & 1 \\ \hline I_{F} & 5 \\ \hline T_{J} & -55 \text{ to } +125 \end{array}$ |

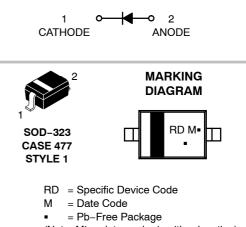
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



## **ON Semiconductor®**

http://onsemi.com

# HIGH CURRENT SCHOTTKY BARRIER DIODE



(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

| Device        | Package                | Shipping†          |
|---------------|------------------------|--------------------|
| NSR0320MW2T10 | G SOD-323<br>(Pb-Free) | 3000/Tape & Reel   |
| NSR0320MW2T30 | G SOD-323<br>(Pb-Free) | 10,000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

## NSR0320MW2T1

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

| Characteristic  | Symbol         | Min | Тур  | Max  | Unit |
|---|----------------|-----|------|------|------|
| Total Capacitance (V <sub>R</sub> = 5.0 V, f = 1.0 MHz) | CT             | -   | 25   | 29   | pF   |
| Reverse Leakage (V <sub>R</sub> = 15 V)                 | I <sub>R</sub> | -   | 10   | 50   | μΑ   |
| Reverse Leakage (V <sub>R</sub> = 2.0 V @ 85°C)         | I <sub>R</sub> | -   | 200  | 300  | μΑ   |
| Reverse Leakage (V <sub>R</sub> = 15.0 V @ 85°C)        | I <sub>R</sub> | -   | 450  | 1000 | μΑ   |
| Forward Voltage (I <sub>F</sub> = 10 mA)                | V <sub>F</sub> | -   | 0.24 | 0.27 | V    |
| Forward Voltage (I <sub>F</sub> = 100 mA)               | V <sub>F</sub> | -   | 0.30 | 0.35 | V    |
| Forward Voltage (I <sub>F</sub> = 900 mA)               | V <sub>F</sub> | -   | 0.45 | 0.50 | V    |

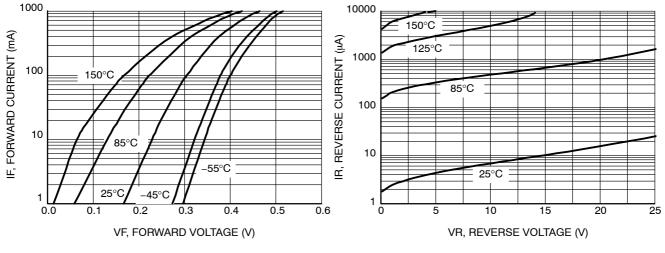




Figure 2. Leakage Current

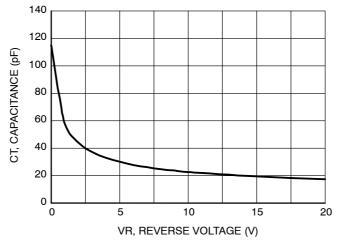
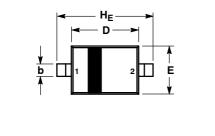
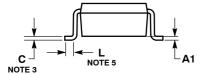


Figure 3. Total Capacitance

#### PACKAGE DIMENSIONS

SOD-323 CASE 477-02 ISSUF H





NOTES 1.

DIMENSIONING AND TOLERANCING PER ANSI

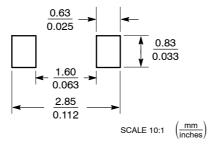
- Y14.5M. 1982. CONTROLLING DIMENSION: MILLIMETERS.
- 2. 3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
- FLASH, PROTRUSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. DIMENSION L IS MEASURED FROM END OF 4.
- 5 RADIUS.

|     | MILLIMETERS |      |       | INCHES    |       |       |
|-----|-------------|------|-------|-----------|-------|-------|
| DIM | MIN         | NOM  | MAX   | MIN       | NOM   | MAX   |
| Α   | 0.80        | 0.90 | 1.00  | 0.031     | 0.035 | 0.040 |
| A1  | 0.00        | 0.05 | 0.10  | 0.000     | 0.002 | 0.004 |
| A3  | 0.15 REF    |      |       | 0.006 REF |       |       |
| b   | 0.25        | 0.32 | 0.4   | 0.010     | 0.012 | 0.016 |
| С   | 0.089       | 0.12 | 0.177 | 0.003     | 0.005 | 0.007 |
| D   | 1.60        | 1.70 | 1.80  | 0.062     | 0.066 | 0.070 |
| Е   | 1.15        | 1.25 | 1.35  | 0.045     | 0.049 | 0.053 |
| L   | 0.08        |      |       | 0.003     |       |       |
| HE  | 2.30        | 2.50 | 2.70  | 0.090     | 0.098 | 0.105 |

STYLE 1: PIN 1. CATHODE 2. ANODE

## **SOLDERING FOOTPRINT\***

A3



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILC does not convey any license under its patent rights or the rights of others. SCILC products are not designed, intended, or authorized for use a components in systems intended for surgical implant into the body, or other applications. Buyer purchase or use SCILLC products for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

#### Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850

#### ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative