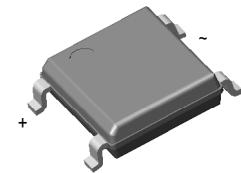




LB2S-LB10S

SINGLE-PHASE SURFACE MOUNT BRIDGE RECTIFIER



VOLTAGE RANGE: 200 - 1000V

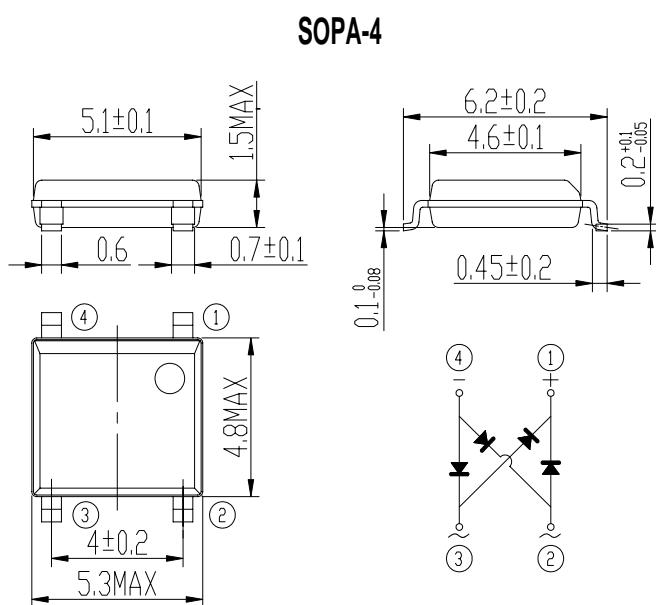
CURRENT: 1.0 A

Features

- Low Profile: Typical height of 1.4mm
- Ideal for automated placement
- High surge current capability

Mechanical Data

- Case:SOPA-4
- Epoxy meets UL-94V-0 Flammability rating
- Terminals:Matte tin plated leads, solderable per J-STD-002B and JESD22-B102D
- Polarity:As marked on body



Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

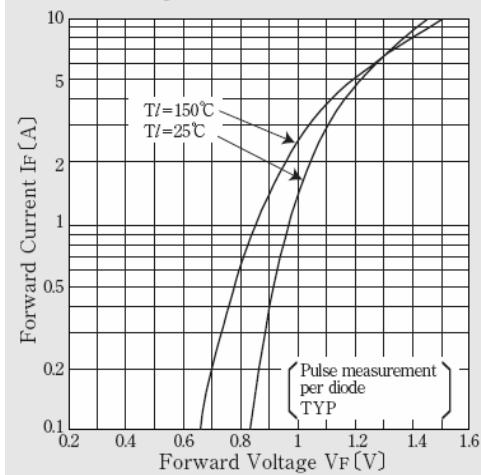
Characteristic	Symbol	LB2S	LB4S	LB6S	LB8S	LB10S	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	140	280	420	560	800	V
Maximum DC blocking voltage	V _{DC}	200	400	600	800	1000	V
Maximum Average forward output rectified current on glass-epoxy P.C.B on aluminum substrate	I _{F(AV)}			1.0			A
				0.8			
Peak forward surge current 8.3 ms single sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}			30			A
Rating for fusig (t<8.3ms)	I _{2t}			3			A ² sec
Maximum instantaneous forward voltage drop per diode at 0.4A	V _F			0.95			V
Maximum DC reverse current at T _A =25 rated DC blocking voltage per leg T _A =125	I _R			5 500			µA
Typical thermal resistance per leg (Note 1)	R _{θJA} R _{θJL}			80 25			/W
Operating junction temperature range	T _J			-55 to +150			
Storage temperature range	T _{STG}			-55 to +150			

Notes1. Device mounted P.C.B with 0.47x0.47"(12mmx12mm) Copper Pads.

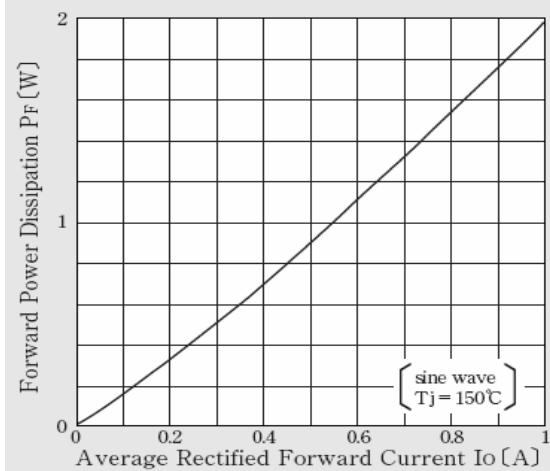
2. JEDEC registered values

(TA=25 unless otherwise noted)

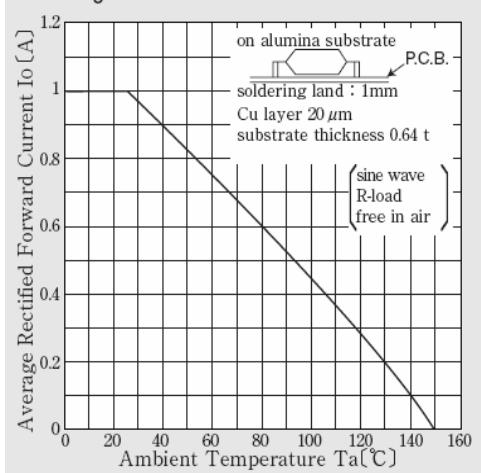
Forward Voltage



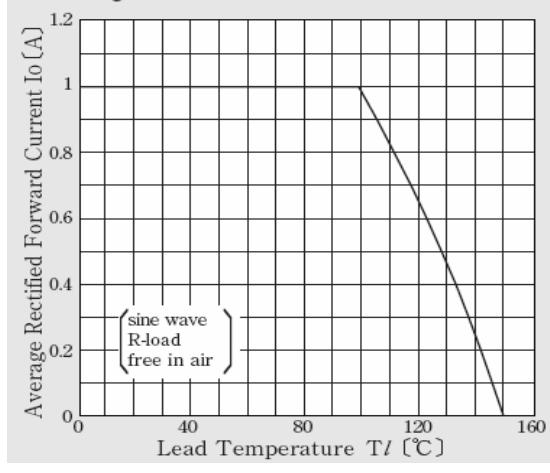
Forward Power Dissipation



Derating Curve Ta-Io



Derating Curve Tj-Io



Peak Surge Forward Current Capability

