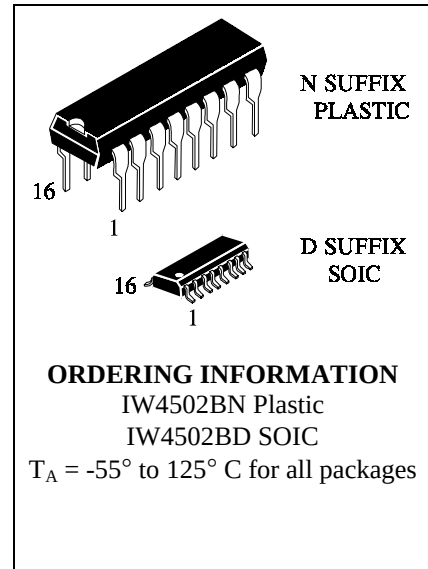


**IW4502B**

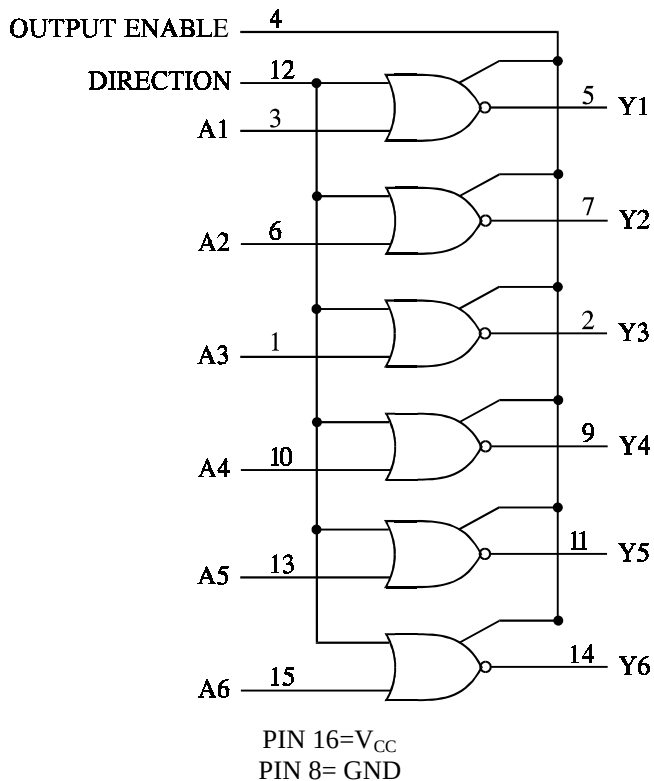
## Strobed Hex Inverter/Buffer High-Voltage Silicon-Gate CMOS

The IW4502B consists of six inverter/buffers with 3-state outputs. A logic "1" on the OUTPUT ENABLE input produces a high impedance state in all six outputs. This feature permits common busing of the outputs, thus simplifying system design. A logic "1" on the DIRECTION input switches all six outputs to logic "0" if the OUTPUT ENABLE input is a logic "0".

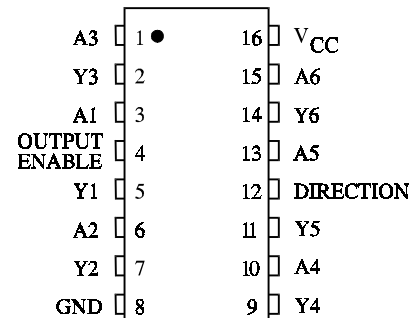
- Operating Voltage Range: 3.0 to 18 V
- Maximum input current of 1  $\mu$ A at 18 V over full package-temperature range; 100 nA at 18 V and 25°C
- Noise margin (over full package temperature range):
  - 1.0 V min @ 5.0 V supply
  - 2.0 V min @ 10.0 V supply
  - 2.5 V min @ 15.0 V supply



### LOGIC DIAGRAM



### PIN ASSIGNMENT



### FUNCTION TABLE

| Inputs        |           |   | Output |
|---------------|-----------|---|--------|
| Output Enable | Direction | A | Y      |
| L             | L         | L | H      |
| L             | L         | H | L      |
| L             | H         | X | L      |
| H             | X         | X | Z      |

Z = high impedance  
 X = don't care

**MAXIMUM RATINGS\***

| Symbol    | Parameter                                                                        | Value                  | Unit |
|-----------|----------------------------------------------------------------------------------|------------------------|------|
| $V_{CC}$  | DC Supply Voltage (Referenced to GND)                                            | -0.5 to +20            | V    |
| $V_{IN}$  | DC Input Voltage (Referenced to GND)                                             | -0.5 to $V_{CC} + 0.5$ | V    |
| $V_{OUT}$ | DC Output Voltage (Referenced to GND)                                            | -0.5 to $V_{CC} + 0.5$ | V    |
| $I_{IN}$  | DC Input Current, per Pin                                                        | $\pm 10$               | mA   |
| $P_D$     | Power Dissipation in Still Air, Plastic DIP+<br>SOIC Package+                    | 750<br>500             | mW   |
| $P_D$     | Power Dissipation per Output Transistor                                          | 100                    | mW   |
| Tstg      | Storage Temperature                                                              | -65 to +150            | °C   |
| $T_L$     | Lead Temperature, 1 mm from Case for 10 Seconds<br>(Plastic DIP or SOIC Package) | 260                    | °C   |

\*Maximum Ratings are those values beyond which damage to the device may occur.  
Functional operation should be restricted to the Recommended Operating Conditions.

+Derating - Plastic DIP: - 10 mW/°C from 65° to 125°C  
SOIC Package: - 7 mW/°C from 65° to 125°C

**RECOMMENDED OPERATING CONDITIONS**

| Symbol            | Parameter                                            | Min | Max      | Unit |
|-------------------|------------------------------------------------------|-----|----------|------|
| $V_{CC}$          | DC Supply Voltage (Referenced to GND)                | 3.0 | 18       | V    |
| $V_{IN}, V_{OUT}$ | DC Input Voltage, Output Voltage (Referenced to GND) | 0   | $V_{CC}$ | V    |
| $T_A$             | Operating Temperature, All Package Types             | -55 | +125     | °C   |

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation,  $V_{IN}$  and  $V_{OUT}$  should be constrained to the range  $GND \leq (V_{IN} \text{ or } V_{OUT}) \leq V_{CC}$ .

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or  $V_{CC}$ ).  
Unused outputs must be left open.

**DC ELECTRICAL CHARACTERISTICS**(Voltages Referenced to GND)

| Symbol          | Parameter                                      | Test Conditions                                                                                                                                   | V <sub>CC</sub><br>V   | Guaranteed Limit            |                               |                                | Unit |
|-----------------|------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------|------------------------|-----------------------------|-------------------------------|--------------------------------|------|
|                 |                                                |                                                                                                                                                   |                        | ≥-55°C                      | 25°C                          | ≤125°C                         |      |
| V <sub>IH</sub> | Minimum High-Level Input Voltage               | V <sub>OUT</sub> =0.5 V<br>V <sub>OUT</sub> =1 V<br>V <sub>OUT</sub> =1.5                                                                         | 5.0<br>10<br>15        | 3.5<br>7<br>11              | 3.5<br>7<br>11                | 3.5<br>7<br>11                 | V    |
| V <sub>IL</sub> | Maximum Low-Level Input Voltage                | V <sub>OUT</sub> = V <sub>CC</sub> - 0.5V<br>V <sub>OUT</sub> = V <sub>CC</sub> - 1.0 V<br>V <sub>OUT</sub> = V <sub>CC</sub> - 1.5V              | 5.0<br>10<br>15        | 1.5<br>3<br>4               | 1.5<br>3<br>4                 | 1.5<br>3<br>4                  | V    |
| V <sub>OH</sub> | Minimum High-Level Output Voltage              | V <sub>IN</sub> =GND                                                                                                                              | 5.0<br>10<br>15        | 4.95<br>9.95<br>14.95       | 4.95<br>9.95<br>14.95         | 4.95<br>9.95<br>14.95          | V    |
| V <sub>OL</sub> | Maximum Low-Level Output Voltage               | V <sub>IN</sub> = V <sub>CC</sub>                                                                                                                 | 5.0<br>10<br>15        | 0.05<br>0.05<br>0.05        | 0.05<br>0.05<br>0.05          | 0.05<br>0.05<br>0.05           | V    |
| I <sub>IN</sub> | Maximum Input Leakage Current                  | V <sub>IN</sub> = GND or V <sub>CC</sub>                                                                                                          | 18                     | ±0.1                        | ±0.1                          | ±1.0                           | μA   |
| I <sub>CC</sub> | Maximum Quiescent Supply Current (per Package) | V <sub>IN</sub> = GND or V <sub>CC</sub>                                                                                                          | 5.0<br>10<br>15<br>20  | 1<br>2<br>4<br>20           | 1<br>2<br>4<br>20             | 30<br>60<br>120<br>600         | μA   |
| I <sub>OL</sub> | Minimum Output Low (Sink) Current              | V <sub>IN</sub> = GND or V <sub>CC</sub><br>U <sub>OL</sub> =0.4 V<br>U <sub>OL</sub> =0.5 V<br>U <sub>OL</sub> =1.5 V                            | 5.0<br>10<br>15        | 3.84<br>9.6<br>25.2         | 3.06<br>7.8<br>20.4           | 2.16<br>5.4<br>14.4            | mA   |
| I <sub>OH</sub> | Minimum Output High (Source) Current           | V <sub>IN</sub> = GND or V <sub>CC</sub><br>U <sub>OH</sub> =2.5 V<br>U <sub>OH</sub> =4.6 V<br>U <sub>OH</sub> =9.5 V<br>U <sub>OH</sub> =13.5 V | 5.0<br>5.0<br>10<br>15 | -2<br>-0.64<br>-1.6<br>-4.2 | -1.6<br>-0.51<br>-1.3<br>-3.4 | -1.15<br>-0.36<br>-0.9<br>-2.4 | mA   |
| I <sub>OZ</sub> | Maximum Tree-State Leakage Current             | Output in High-Impedance State<br>V <sub>IN</sub> = GND or V <sub>CC</sub><br>V <sub>OUT</sub> = GND or V <sub>CC</sub>                           | 18                     | ±0.4                        | ±0.4                          | ±12                            | μA   |

**AC ELECTRICAL CHARACTERISTICS**( $C_L=50\text{pF}$ ,  $R_L=200\text{k}\Omega$  unless otherwise specified, Input  $t_r=t_f=20\text{ ns}$ )

| Symbol    | Parameter                                                                                   | $V_{CC}$<br>V | Guaranteed Limit         |                    |                          | Unit |
|-----------|---------------------------------------------------------------------------------------------|---------------|--------------------------|--------------------|--------------------------|------|
|           |                                                                                             |               | $\geq -55^\circ\text{C}$ | $25^\circ\text{C}$ | $\leq 125^\circ\text{C}$ |      |
| $t_{PHL}$ | Maximum Propagation Delay, Input A or Direction to Output Y (Figure 1)                      | 5.0           | 270                      | 270                | 540                      | ns   |
|           |                                                                                             | 10            | 120                      | 120                | 240                      |      |
|           |                                                                                             | 15            | 80                       | 80                 | 160                      |      |
| $t_{PLH}$ | Maximum Propagation Delay, Input A or Direction to Output Y (Figure 1)                      | 5.0           | 380                      | 380                | 760                      | ns   |
|           |                                                                                             | 10            | 180                      | 180                | 360                      |      |
|           |                                                                                             | 15            | 130                      | 130                | 260                      |      |
| $t_{PHZ}$ | Maximum Propagation Delay, Output Enable to Output Y (Figure 2)<br>$R_L = 1\text{ k}\Omega$ | 5.0           | 120                      | 120                | 240                      | ns   |
|           |                                                                                             | 10            | 80                       | 80                 | 160                      |      |
|           |                                                                                             | 15            | 60                       | 60                 | 120                      |      |
| $t_{PZH}$ | Maximum Propagation Delay, Output Enable to Output Y (Figure 2)<br>$R_L = 1\text{ k}\Omega$ | 5.0           | 220                      | 220                | 440                      | ns   |
|           |                                                                                             | 10            | 100                      | 100                | 200                      |      |
|           |                                                                                             | 15            | 80                       | 80                 | 160                      |      |
| $t_{PLZ}$ | Maximum Propagation Delay, Output Enable to Output Y (Figure 2)<br>$R_L = 1\text{ k}\Omega$ | 5.0           | 250                      | 250                | 500                      | ns   |
|           |                                                                                             | 10            | 130                      | 130                | 260                      |      |
|           |                                                                                             | 15            | 110                      | 110                | 220                      |      |
| $t_{PZL}$ | Maximum Propagation Delay, Output Enable to Output Y (Figure 2)<br>$R_L = 1\text{ k}\Omega$ | 5.0           | 250                      | 250                | 500                      | ns   |
|           |                                                                                             | 10            | 110                      | 110                | 220                      |      |
|           |                                                                                             | 15            | 80                       | 80                 | 160                      |      |
| $t_{TLH}$ | Maximum Output Transition Time, Any Output (Figure 1)                                       | 5.0           | 200                      | 200                | 400                      | ns   |
|           |                                                                                             | 10            | 100                      | 100                | 200                      |      |
|           |                                                                                             | 15            | 80                       | 80                 | 160                      |      |
| $t_{THL}$ | Maximum Output Transition Time, Any Output (Figure 1)                                       | 5.0           | 120                      | 120                | 240                      | ns   |
|           |                                                                                             | 10            | 60                       | 60                 | 120                      |      |
|           |                                                                                             | 15            | 40                       | 40                 | 80                       |      |
| $C_{IN}$  | Maximum Input Capacitance                                                                   | -             |                          | 7.5                |                          | pF   |
| $C_{OUT}$ | Maximum Tree-State Output Capacitance (Output in High-Impedance State)                      | -             |                          | 15                 |                          | pF   |

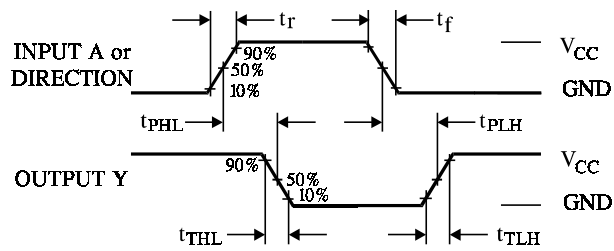


Figure 1. Switching Waveforms

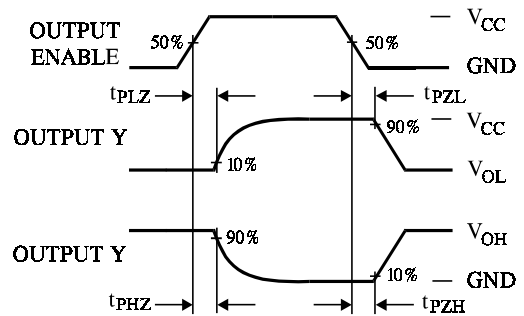


Figure 2. Switching Waveforms

### EXPANDED LOGIC DIAGRAM (1/6 of the Device)

