TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

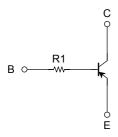
RN2112FT,RN2113FT

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications.

Unit: mm

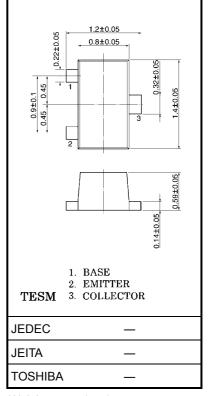
- High-density mount is possible because of devices housed in very thin TESM packages.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Wide range of resistor values are available to use in various circuit designs.
- Complementary to RN1112FT, RN1113FT

Equivalent Circuit and Bias Resistor Values



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	IC	-100	mA
Collector power dissipation	PC	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

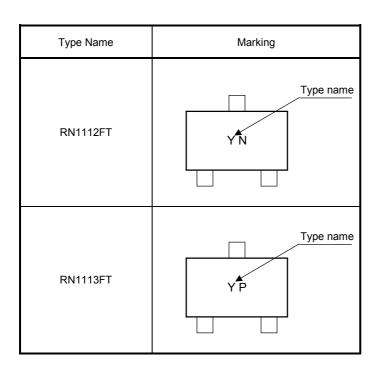


Weight: g (typ.)



Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$			-100	nA
DC current gain		h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	120	_	400	
Collector-emitter saturation voltage		V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Transition frequency		f _T	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance		C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	3	6	pF
Input resistor	RN2112FT	- R1	_	15.4	22	28.6	kΩ
	RN2113FT			32.9	47	61.1	



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