#### 2-PHASE DRUM MOTOR DRIVER

The KA8328D is a monolithic integrated circuit, and it is suitable for drum motor driver of VCR system.

# 20-SOP-300

#### **FEATURES**

- 2-phase, full-wave, linear BLDC motor driver with 2 hall sensors
- Built-in voltage or current control circuit.
- Built-in regulated power supply for hall devices.
- · Built-in thermal shutdown (TSD) circuit.
- · Built-in hall amp.
- · High output current

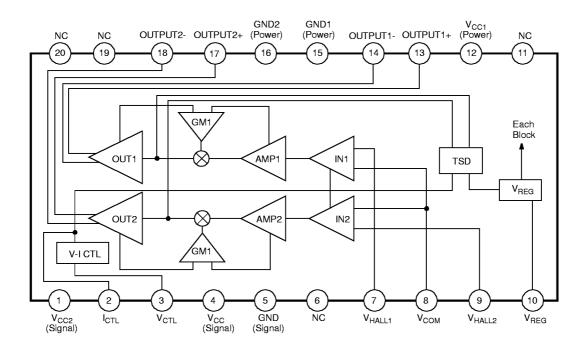
#### **ORDERING INFORMATION**

Device	Package	Operating Temperature
KA8328D	20-SOP-300	−25°C ~ +75°C

#### **TARGET APPLICATIONS**

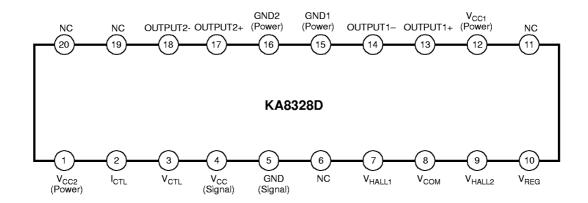
· VCR drum motors

#### **BLOCK DIAGRAM**





# **PIN CONFIGURATIONS**



## **PIN DESCRIPTION**

Pin No.	Symbol	I/O	Description	Pin No.	Symbol	I/O	Description
1	V <sub>CC2</sub>		Supply voltage (Power)	11	NC	_	No connection
2	I <sub>CTL</sub>	I	Current control input	12	V <sub>CC1</sub>	_	Supply voltage (Power)
3	V <sub>CTL</sub>	I	Voltage control input	13	OUTPUT +	0	Φ A+ output
4	V <sub>CC</sub>	-	Supply voltage (Signal)	14	OUTPUT –	0	Φ A– output
5	GND	-	Ground (Signal)	15	GND1	_	Ground (Power)
6	NC	-	No connection	16	GND2	_	Ground (Power)
7	V <sub>HALL1</sub>	I	Hall signal input	17	OUTPUT +	0	Φ B+ output
8	V <sub>COM</sub>	I	Common hall signal input	18	OUTPUT –	0	Φ B– output
9	V <sub>HALL2</sub>	I	Hall signal input	19	NC	_	No connection
10	V <sub>REG</sub>	0	Regulated voltage output	20	NC	-	No connection



# **EQUIVALENT CIRCUITS**

Description	Pin No.	Internal circuit
Driver input (Current control)	2	2 8 TSD 7 9
Driver input (Voltage control)	3	3 W VREF
Hall signal input	7,8,9	8 TSD 7 9
Regulator output	10	Band-gap- Referenced Biasing circuit
Driver output	13,14 17,18	13 (1) Feedback Circuit



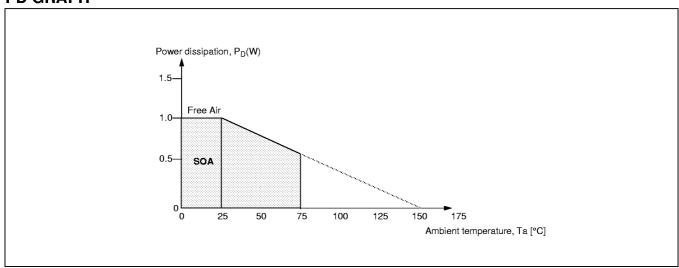
# ABSOLUTE MAXIMUM RATING (Ta=25°C)

Characteristics	Symbol	Value	Unit	Remark
Supply voltage	V <sub>CCmax</sub>	20	V	-
Maxium Output current	I <sub>Omax</sub>	1.2 <sup>note1</sup>	Α	-
V <sub>REG</sub> output current	I <sub>REGmax</sub>	40	mA	-
I <sub>CTL</sub> input current	I <sub>CTLmax</sub>	1	mA	
V <sub>COM</sub> input voltage	V <sub>COMmax</sub>	V <sub>REG</sub> -1	V	
Power dissipation	P <sub>d</sub>	1 <sup>note2</sup>	W	No heat sink
Operating temperature	T <sub>OPR</sub>	−25 ~ +75	°C	Ambient temperature
Storage temperature	T <sub>STG</sub>	-40 ~ +125	°C	Ambient temperature

#### NOTES:

- 1. Duty 1/100, pulse width  $500\mu s$
- 2. 1) When mounted on glass epoxy PCB  $(76.2 \times 114 \times 1.57 \text{mm})$ 
  - 2) Power dissipation reduces 120mW / °C for using above Ta=25°C
  - 3) Do not exceed Pd and SOA.

#### **PD GRAPH**



## **OPERATING CONDITION**

Characteristics	Symbol	Value	Unit	
Operating supply voltage	V <sub>CC</sub>	8 ~ 16	٧	

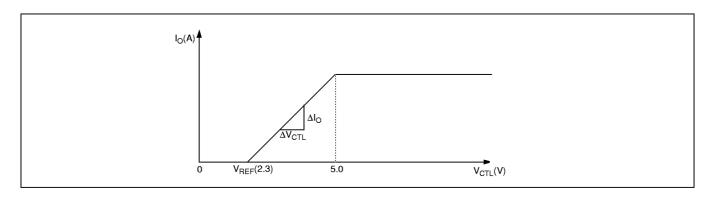


## **ELECTRICAL CHARACTERISTICS**

(Ta=25°C, V<sub>CC</sub>=14V, unless otherwise specified)

Characteristic	Symbol	Toot conditions	Spec.			Unit	
Characteristic	Syllibol	Test conditions	Min.	Тур.	max.	Ullit	
Quiescent current	ΙQ	I <sub>Q</sub> V <sub>CC</sub> =14V		6	10	mA	
Regulated voltage (2)	V <sub>REG2</sub>	V <sub>CC</sub> =14V	4.6	5.0	5.4	V	
Regulated voltage (5)	V <sub>REG5</sub>	V <sub>CC</sub> =14V, I <sub>REG</sub> =20mA	4.6	5.0	5.4	V	
Regulated voltage (8)	V <sub>REG8</sub>	V <sub>CC</sub> =14V, I <sub>REG</sub> =40mA	4.6	5.0	5.4	V	
I <sub>CTL</sub> input voltage	V <sub>ICTL</sub>	V <sub>CC</sub> =14V, I <sub>REG</sub> =10mA	1.2	1.3	1.4	٧	
Current gain G <sub>A</sub>	G <sub>A2</sub>	V <sub>HALL1</sub> =2.5V, V <sub>COM</sub> =2.2V I <sub>CTL</sub> =100μA	4100	4900	5700	_	
Current gain G <sub>B</sub> G <sub>B2</sub>		V <sub>HALL1</sub> =2.5V, V <sub>COM</sub> =2.2V I <sub>CTL</sub> =100μA	4100	4900	5700	-	
Current gain ratio	R	G <sub>A2</sub> / G <sub>B2</sub>	0.8	1	1.2	-	
Output transistor saturation voltage (Upper)	V <sub>S-UA3</sub>	/ <sub>S-UA3</sub> I <sub>O</sub> =800mA		1.3	2.0	٧	
Output transistor saturation voltage (Lower)	V <sub>S-DA3</sub>	I <sub>O</sub> =800mA		2.0	3.0	٧	
Motor drive A	I <sub>O-A1</sub>	$I_{O-A1}$ $V_{HALL1}=2.5V, V_{COM}=2.2V$ $I_{CTL}=100\mu A$		850	980	mA	
Motor drive B I <sub>O-B1</sub>		V <sub>HALL1</sub> =2.5V, V <sub>COM</sub> =2.2V I <sub>CTL</sub> =100μA	720	850	980	mA	
VOLTAGE CONTROL			1		l		
V <sub>CTL</sub> offset voltage V <sub>OFFL</sub>		V <sub>CTL</sub> =0~V <sub>REG</sub>	-200	0	200	mW	
V <sub>CTL</sub> input current	I <sub>VCTL</sub>	V <sub>CTL</sub> =2.5V	_	1.0	6.0	μА	
Voltage control gain G <sub>M</sub>		$\begin{array}{c} \Delta I_{O} \ / \ \Delta \ V_{CTL} \\ V_{CTL} = V_{REF}(2.3) + 0.5 V \\ V_{REF}(2.3) + 1.0 V \\ V_{HALL1} = 2.5 V, \ V_{COM} = 2.2 V \end{array}$	0.38	0.51	0.70	A / V	

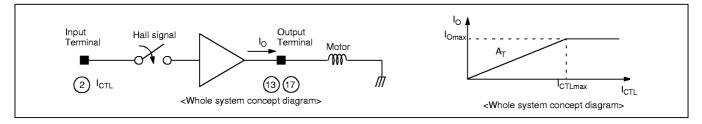
NOTE: Graph. of voltage control gain



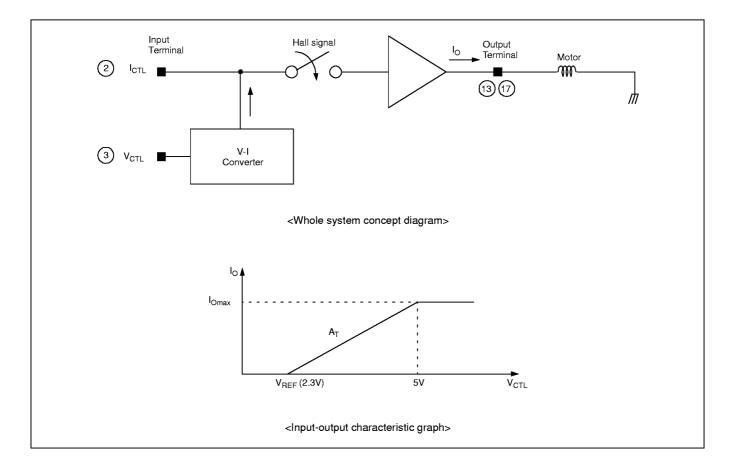


#### **APPLICATION INFORMATION**

1. The whole system concept of the KA8328D can be briefly diagrammatized as follows. Namely it may be though to be an amplifier whose on-off action is operated in accordance with the output signals of the hall sensors which detects the rotor position. The servo current (I<sub>CTL</sub>) inputted in the input terminal controls the output current (I<sub>O</sub>) of the output terminal and the motor speed. At is the gain of the whole system, and determines the characteristic of the output to the input as in the following diagram.

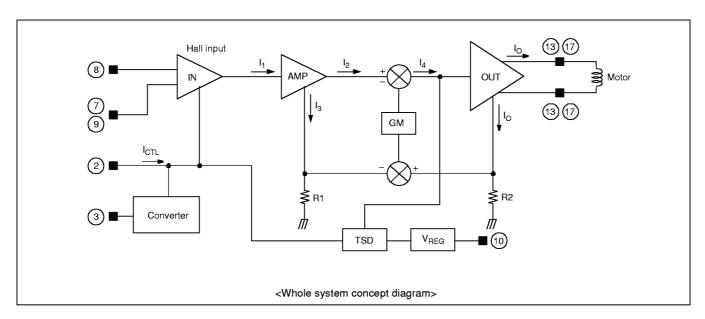


2. Also the output current ( $I_O$ ) can be controlled through inputting the servo voltage ( $V_{CTL}$ ) instead of the servo current ( $I_{CTL}$ ) which applied to the input terminal. In this case, by using the V-I converter as in the following diagram, the servo voltage ( $V_{CTL}$ ) is converted in turn into a current ( $I_{VO}$ ) to control the output current ( $I_O$ ).





3. The whole system concept explained above can be expressed in a more detailed block diagram as follows.



AMP, GM (Feedback), and OUT (Driver) are blocks which determine, the gain  $(A_T)$ . The hall input functions as an ON-OFF switch which is drived by the hall signal. Amp is an amplifier which amplifies the input current 4 times. GM is a feedback circuit which returns the feedback of the output current  $(I_O)$ , and OUT (Driver) is the power switch part which drives the motor.

V<sub>REG</sub> is a constant voltage source which supply stabilized voltage to each block.

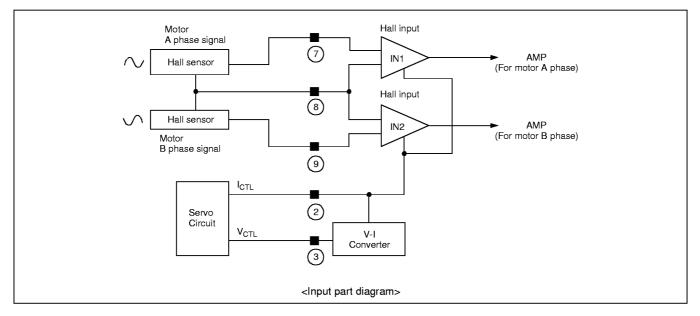
TSD is the overheat protective circuit which protect IC from the overheat, that is, in the event of overheat it protect IC through grounding the input part and OUT (Driver) part.

As for the input-output characteristic, if the input current  $I_{CTL}=100\mu A$  is applied, then  $I_1=100\mu A \rightarrow I_2$  -  $I_3=400\mu A$  (=100 $\mu A \times 4$ )  $\rightarrow I_{C}=490\mu A$ , so the current gain become 4900 times.

The output current is detected through the output current detecting resistance (R2), and transferred to the feedback circuit (GM), and then again returned to OUT (Driver) as feedback, so the output current is kept constant at a desired value.

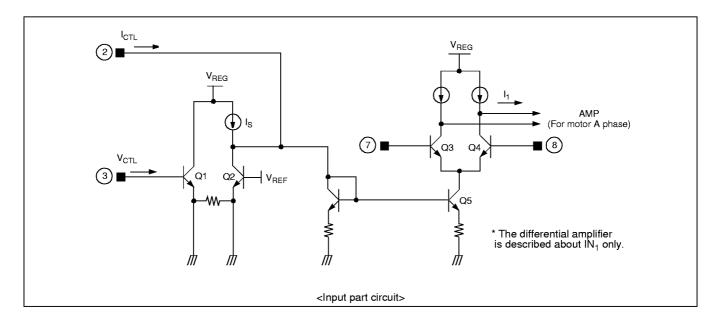
As a result the motor speed is also kept constant.

4. With having the output signal of the hall sensor which detects the rotor position and the servo current (or voltage) which controls the output current as in the following diagram, the differential amplifiers in the hall input parts are operated so that the current may flow at a proper phase of the motor.



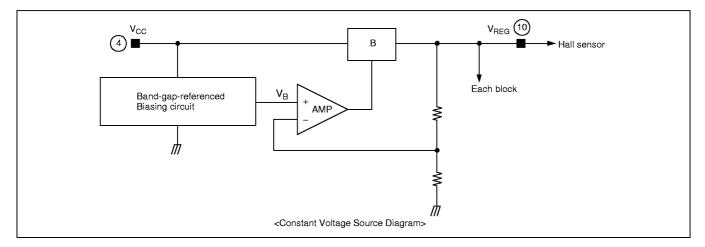
The followings is the simplified circuit diagram of the input part circuit. Firstly when the input control current (I<sub>CTL</sub>) is inputted, TR Q5 is operated, and TR Q3 or TR Q4 of the differential amplifier is operated in accordance with the output signal of the Hall sensor, and so the current (I<sub>1</sub>) is outputted.

Next when the input control voltage ( $V_{CTL}$ ) is inputted, if the input voltage is higher than the standard voltage ( $V_{REF}$  2.3V), TR Q1 is operated, and so TR Q5 is operated by the current source ( $I_S$ ).



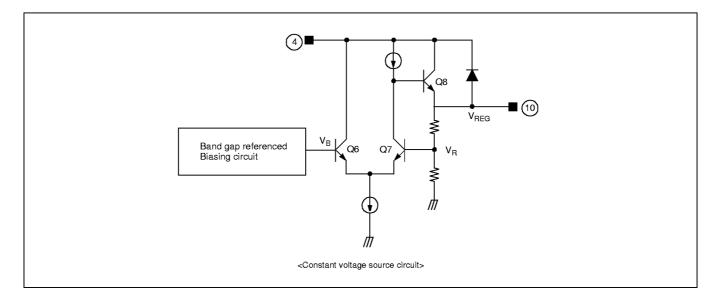


5.The constant voltage source (V<sub>REG</sub>) has the function of keeping the output voltage always constant, with having the V<sub>CC</sub> voltage as its input, as in the following diagram. V<sub>REG</sub> always keeps constant voltage even though the V<sub>CC</sub> or internal temperature changes because the band-gap referenced biasing circuit is used as the reference voltage source of the constant voltage source. V<sub>REG</sub> becomes the supply voltage source of each internal block and the external hall sensors.



The following is the simplified circuit of the constant voltage source ( $V_{REG}$ ). The output voltage ( $V_B$ ) of the band-gap reference biasing circuit is the reference voltage, and always maintains constant voltage.

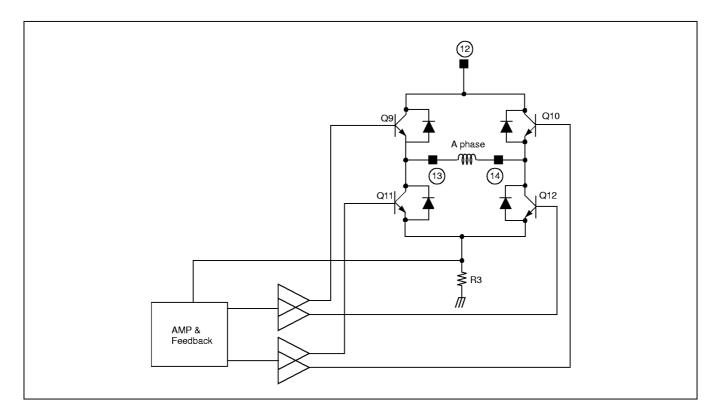
The other input voltage  $(V_R)$  changes according to the state of the output voltage. So the output voltage always maintains constant voltage with the voltage difference of the input voltage  $V_B$  and  $V_R$ .



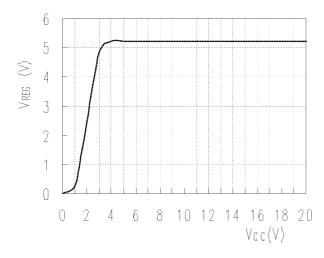
6.OUT (Driver) has the function of driving the motor through amplifying the driving current to a proper level.

Freewheeling diode is designed at the C-E terminal of each TR, and protect the TR by making the motor current freewheeling when the TR is turned off.

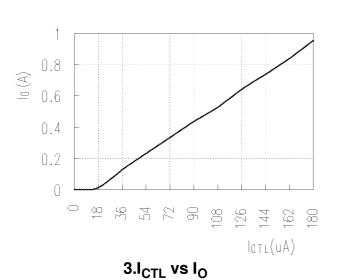
The motor current is kept constant through being detected by the resistance (R3) and returned as feedback again via the Feedback circuit. Accordingly the motor rotative velocity is also kept constant.



# **CHARACTERISTIC GRAPHS**

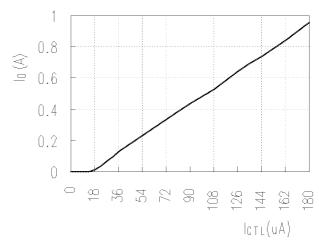


1.V<sub>CC</sub> vs V<sub>REG</sub>



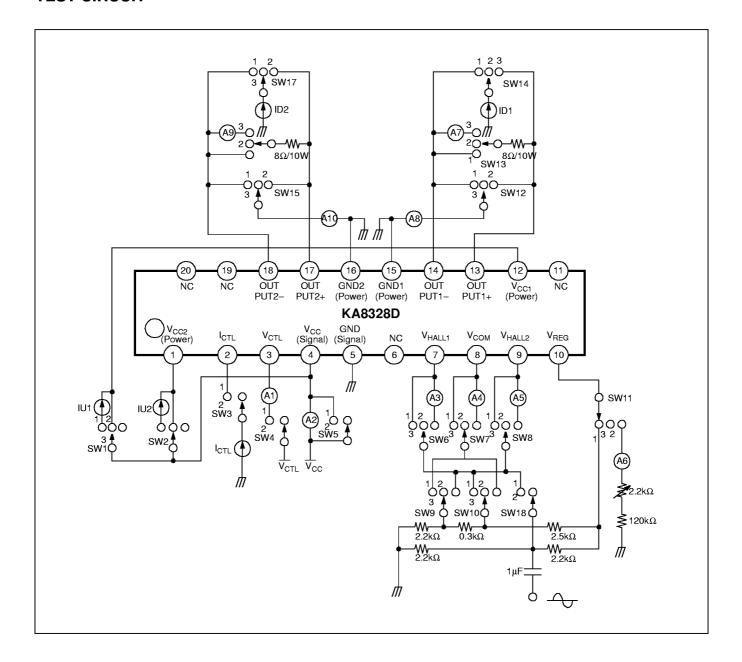
Vcc (V)

2.V<sub>CC</sub> vs I<sub>CC</sub>

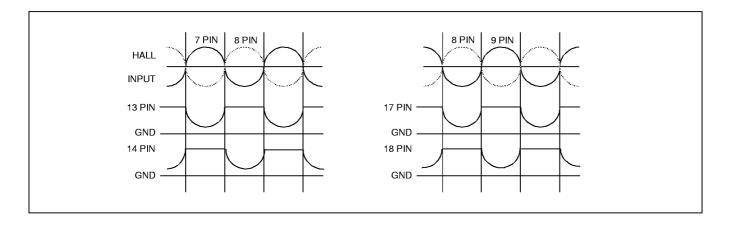


1.I<sub>CTL</sub> vs V<sub>CTL</sub>

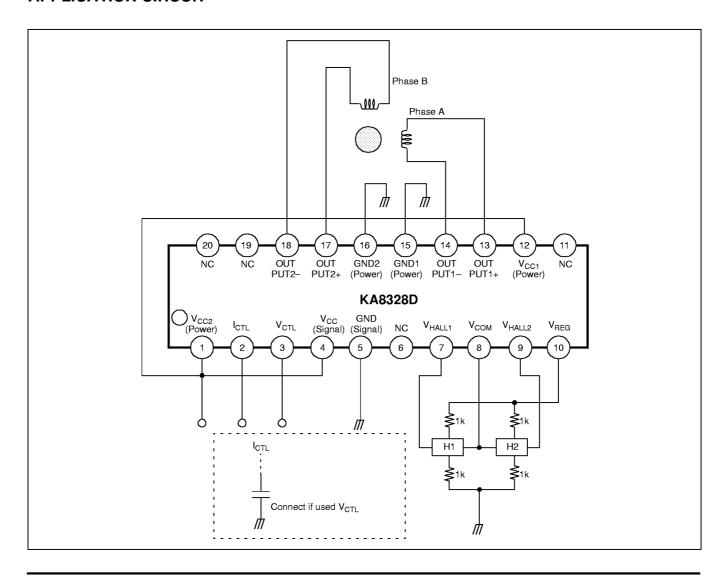
## **TEST CIRCUIT**



## **OUTPUT WAVEFORMS**



# **APPLICATION CIRCUIT**





# **NOTES**



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