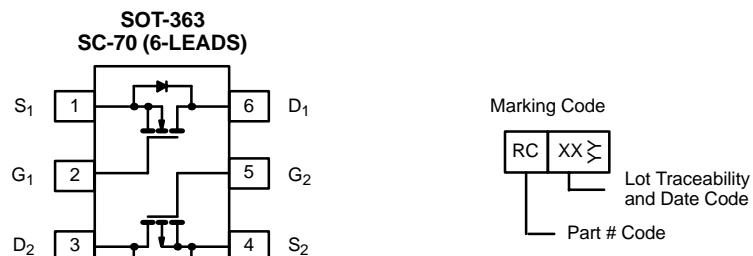


## Complementary 30-V (D-S) MOSFET

**TrenchFET®  
Power MOSFETs**

<b>PRODUCT SUMMARY</b>			
	<b>V<sub>DS</sub> (V)</b>	<b>r<sub>DS(on)</sub> (Ω)</b>	<b>I<sub>D</sub> (A)</b>
N-Channel	30	0.480 @ V <sub>GS</sub> = 10 V	0.63
		0.700 @ V <sub>GS</sub> = 4.5 V	0.52
P-Channel	-30	0.940 @ V <sub>GS</sub> = -10 V	-0.45
		1.700 @ V <sub>GS</sub> = -4.5 V	-0.33



<b>ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)</b>								
<b>Parameter</b>		<b>Symbol</b>	<b>N-Channel</b>		<b>P-Channel</b>		<b>Unit</b>	
			<b>5 secs</b>	<b>Steady State</b>	<b>5 secs</b>	<b>Steady State</b>		
Drain-Source Voltage		V <sub>DS</sub>	30		-30		V	
Gate-Source Voltage		V <sub>GS</sub>	±20					
Continuous Drain Current (T <sub>J</sub> = 150°C) <sup>a</sup>	T <sub>A</sub> = 25°C	I <sub>D</sub>	0.63	0.54	-0.45	-0.42	A	
	T <sub>A</sub> = 85°C		0.45	0.43	-0.32	-0.31		
Pulsed Drain Current		I <sub>DM</sub>	1.0					
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	0.25	0.23	-0.25	-0.23		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25°C	P <sub>D</sub>	0.30	0.27	0.30	0.27	W	
	T <sub>A</sub> = 85°C		0.16	0.14	0.16	0.14		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150				°C	

<b>THERMAL RESISTANCE RATINGS</b>					
<b>Parameter</b>		<b>Symbol</b>	<b>Typical</b>	<b>Maximum</b>	<b>Unit</b>
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 sec	R <sub>thJA</sub>	360	415	°C/W
	Steady State		400	460	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	300	350	

Notes

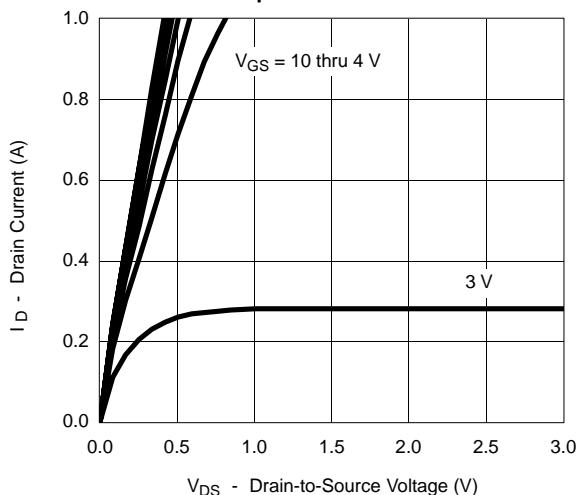
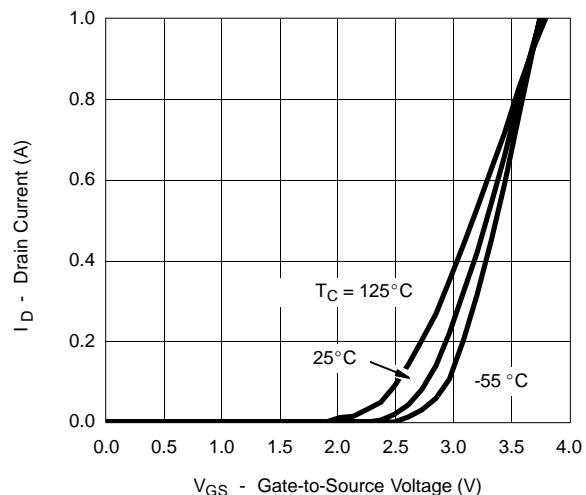
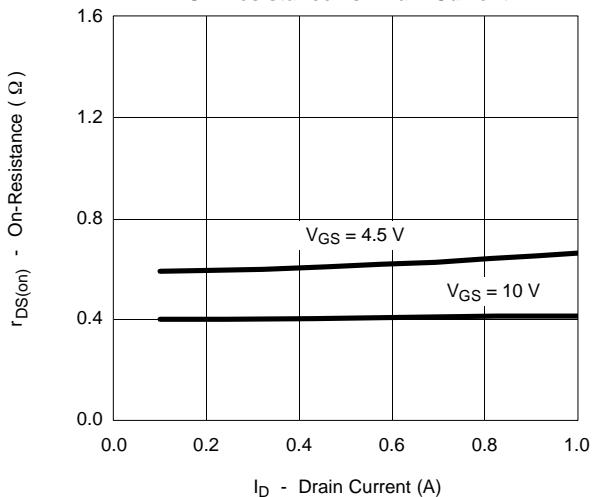
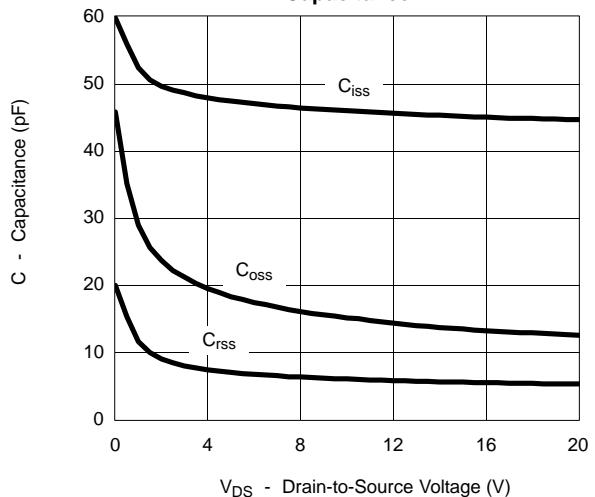
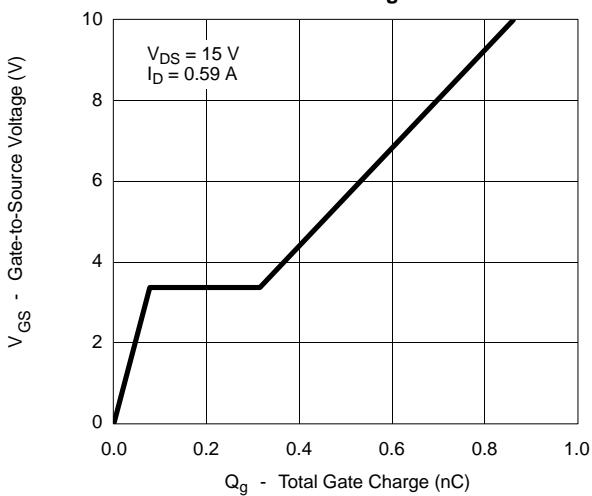
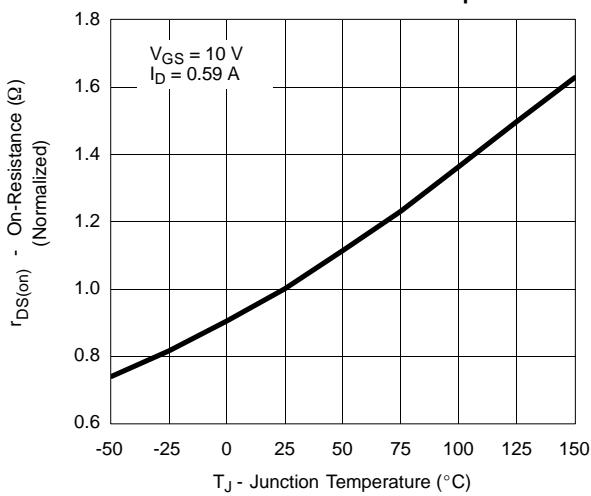
a. Surface Mounted on 1" x 1" FR4 Board.

**SPECIFICATIONS (T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit	
<b>Static</b>							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 µA	N-Ch	1.0			
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 µA	P-Ch	-1.0		V	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V	N-Ch P-Ch		±100 ±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V	N-Ch		1		
		V <sub>DS</sub> = -24 V, V <sub>GS</sub> = 0 V	P-Ch		-1	µA	
		V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85°C	N-Ch		5		
		V <sub>DS</sub> = -24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85°C	P-Ch		-5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	N-Ch	1.0		A	
		V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = -10 V	P-Ch	-1.0			
Drain-Source On-State Resistance <sup>a</sup>	r <sub>D(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.59 A	N-Ch		0.410	0.480	
		V <sub>GS</sub> = -10 V, I <sub>D</sub> = -0.42 A	P-Ch		0.800	0.940	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.2 A	N-Ch		0.600	0.700	
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -0.2 A	P-Ch		1.5	1.700	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.59 A	N-Ch		0.75		
		V <sub>DS</sub> = -15 V, I <sub>D</sub> = -0.42 A	P-Ch		0.5	S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 0.23 A, V <sub>GS</sub> = 0 V	N-Ch		0.8	1.2	
		I <sub>S</sub> = -0.23 A, V <sub>GS</sub> = 0 V	P-Ch		-0.86	-1.2	
<b>Dynamic<sup>b</sup></b>							
Total Gate Charge	Q <sub>g</sub>	N-Channel V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.59 A  P-Channel V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -0.42 A	N-Ch P-Ch		0.86 0.9	1.4 1.4	nC
Gate-Source Charge	Q <sub>gs</sub>		N-Ch P-Ch		0.24 0.21		
Gate-Drain Charge	Q <sub>gd</sub>		N-Ch P-Ch		0.08 0.17		
Turn-On Delay Time	t <sub>d(on)</sub>		N-Ch P-Ch		5 4	10 10	
Rise Time	t <sub>r</sub>	N-Channel V <sub>DD</sub> = 15 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≈ 0.5 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 6 Ω  P-Channel V <sub>DD</sub> = -15 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≈ -0.5 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω	N-Ch P-Ch		8 8	15 15	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		N-Ch P-Ch		8 5	15 10	
Fall Time	t <sub>f</sub>		N-Ch P-Ch		7 7	15 15	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		N-Ch P-Ch		15 20	30 40	

Notes

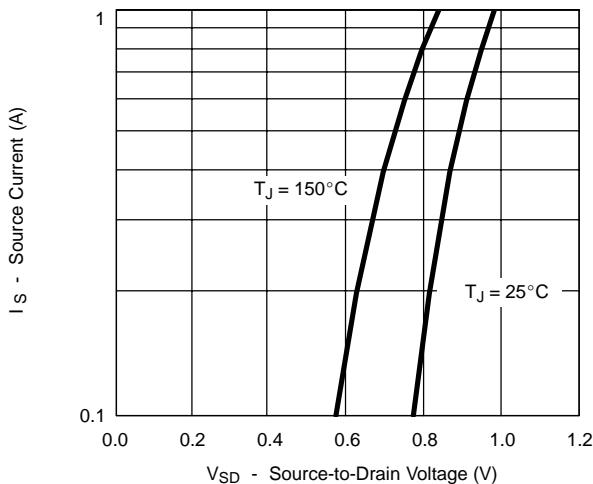
- a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**N-CHANNEL**
**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**


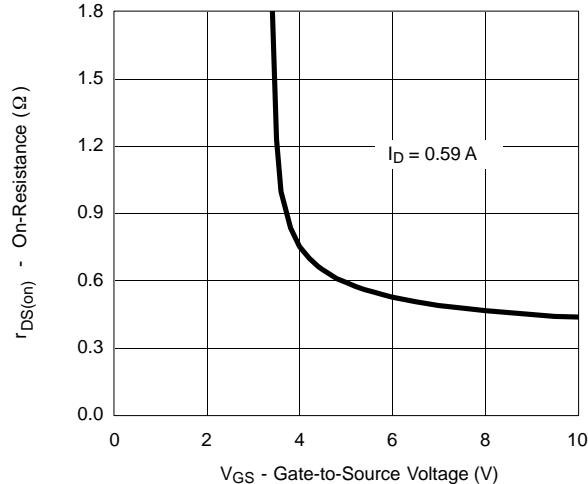
**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

**N-CHANNEL**

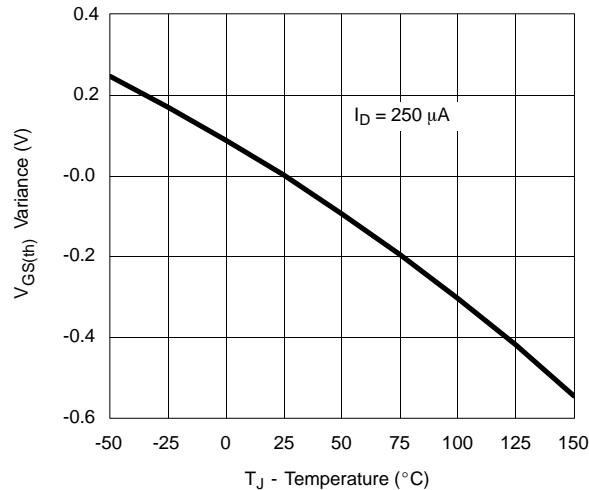
Source-Drain Diode Forward Voltage



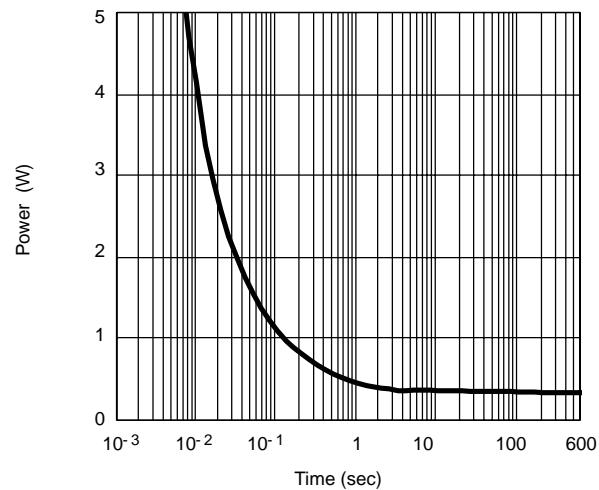
On-Resistance vs. Gate-to-Source Voltage



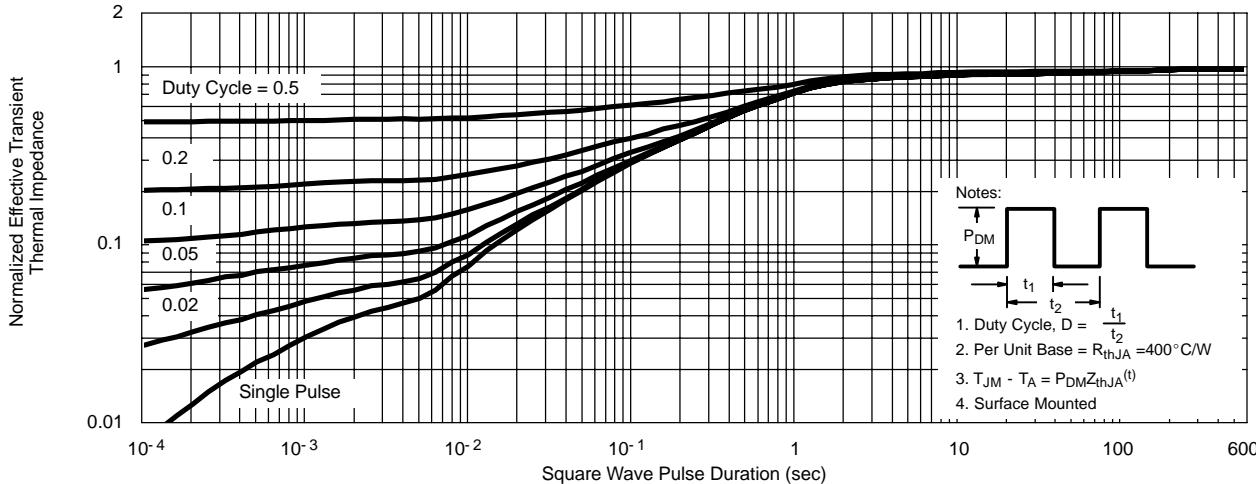
Threshold Voltage

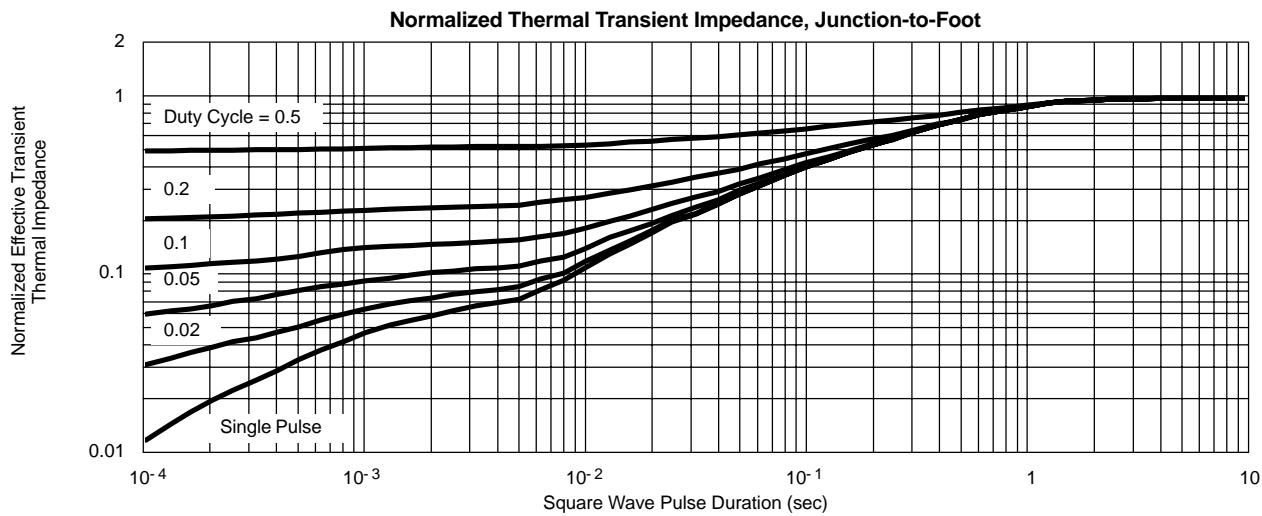
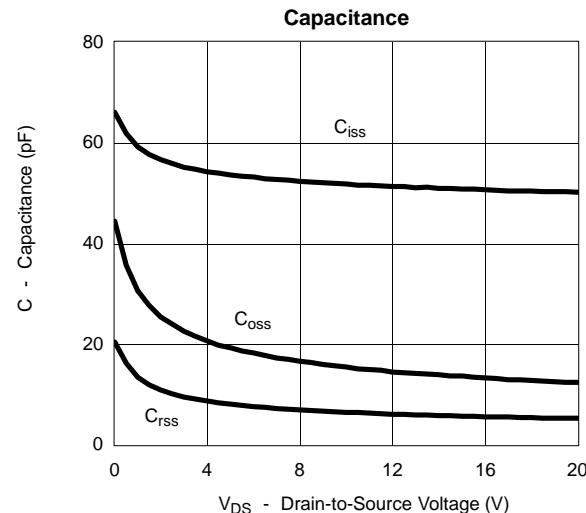
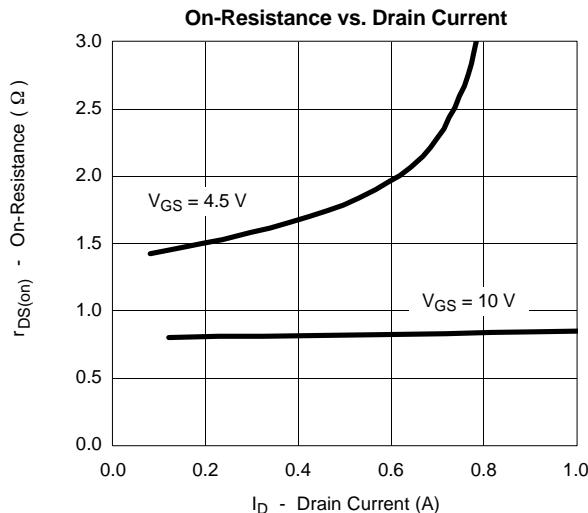
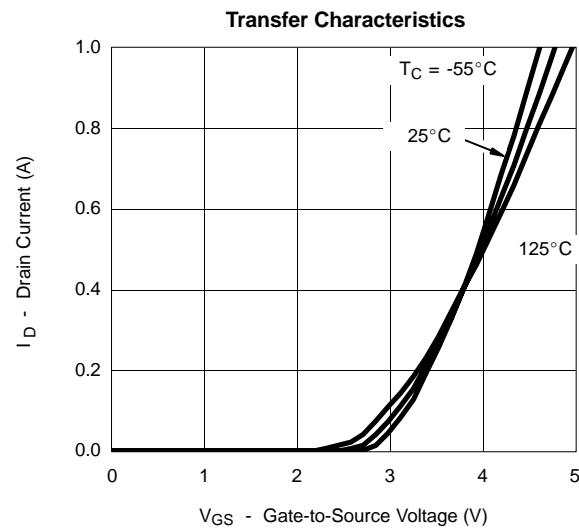
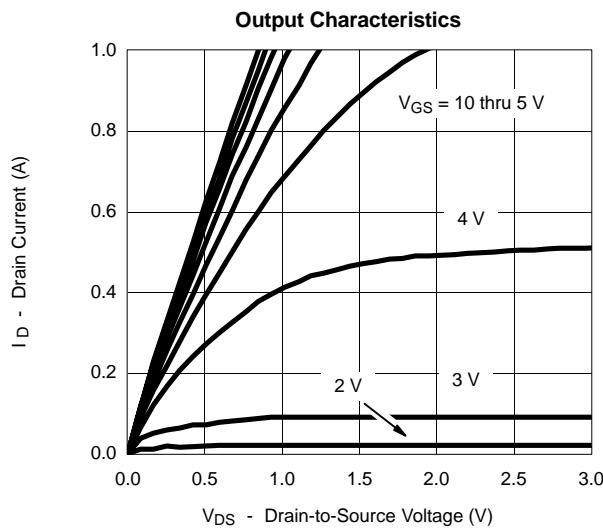


Single Pulse Power



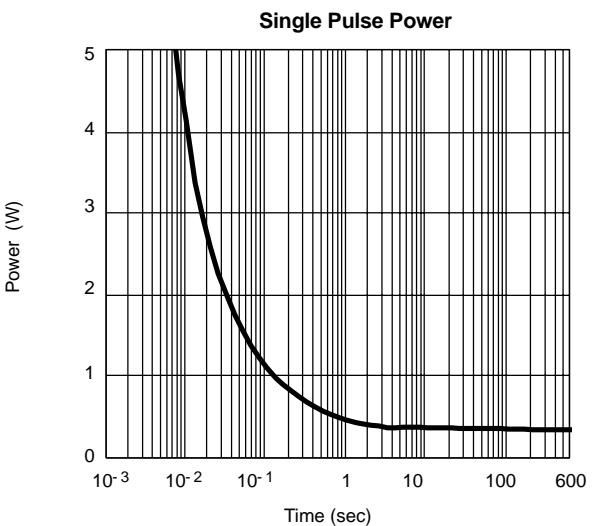
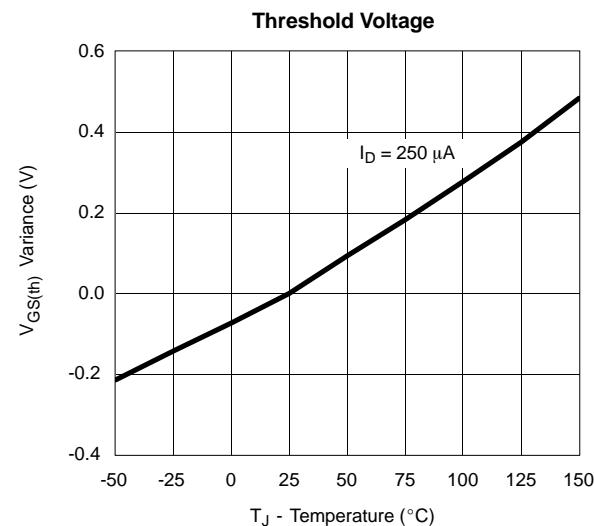
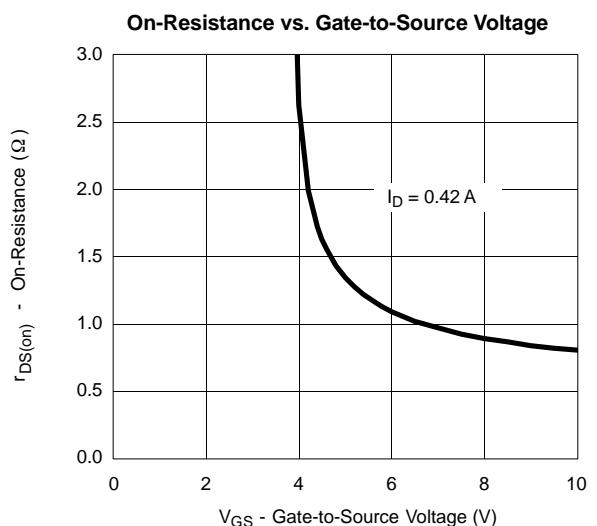
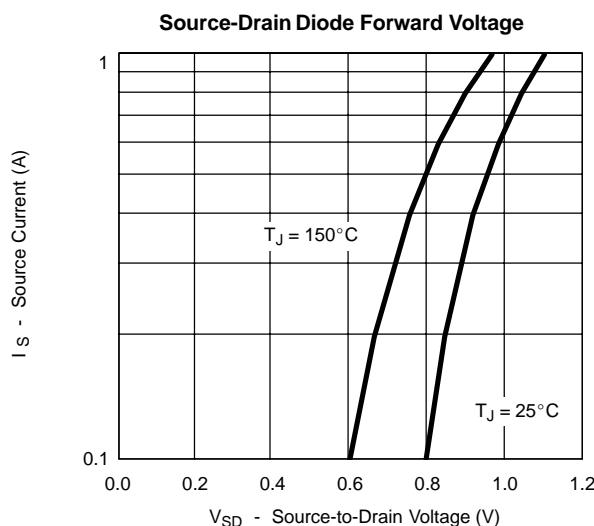
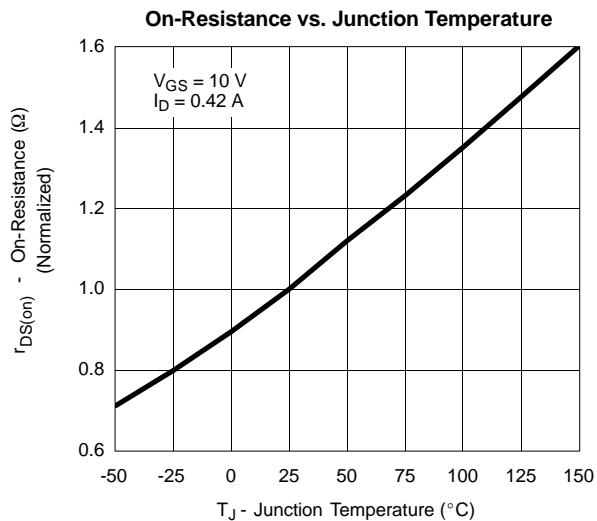
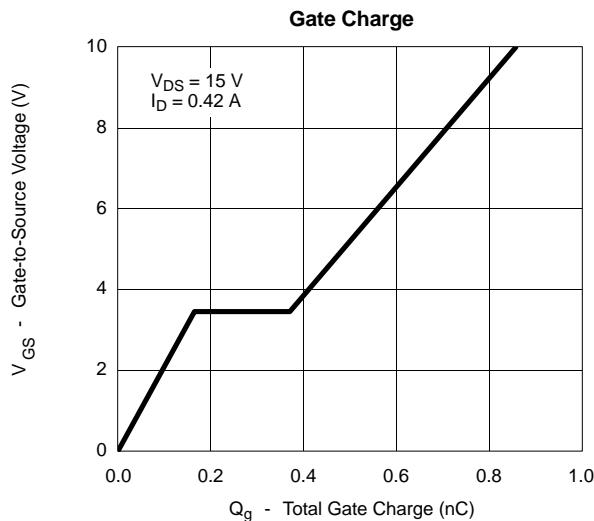
Normalized Thermal Transient Impedance, Junction-to-Ambient



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**N-CHANNEL**

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**P-CHANNEL**


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

**P-CHANNEL**



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**P-CHANNEL**
