



## N-Channel 30-V (D-S) MOSFET

## PRODUCT SUMMARY

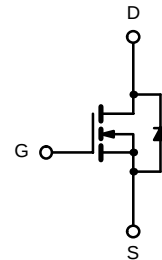
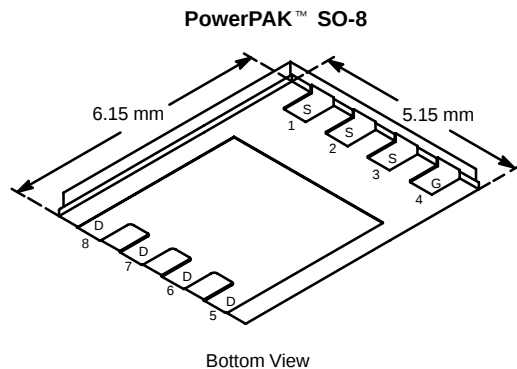
| $V_{DS}$ (V) | $r_{DS(on)}$ ( $\Omega$ ) | $I_D$ (A) |
|--------------|---------------------------|-----------|
| 30           | 0.0045 @ $V_{GS} = 10$ V  | 25        |
|              | 0.0055 @ $V_{GS} = 4.5$ V | 23        |

## FEATURES

- TrenchFET® Power MOSFET
- Optimized for "Low Side" Synchronous Rectifier Operation
- New Low Thermal Resistance PowerPAK™ Package with Low 1.07-mm Profile

## APPLICATIONS

- DC/DC Converters
- Synchronous Rectifiers



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)

| Parameter                                                      |                       | Symbol                            | 10 secs    | Steady State | Unit |
|----------------------------------------------------------------|-----------------------|-----------------------------------|------------|--------------|------|
| Drain-Source Voltage                                           |                       | V <sub>DS</sub>                   | 30         |              | V    |
| Gate-Source Voltage                                            |                       | V <sub>GS</sub>                   | ± 12       |              |      |
| Continuous Drain Current (T <sub>J</sub> = 150°C) <sup>a</sup> | T <sub>A</sub> = 25°C | I <sub>D</sub>                    | 25         | 15           | A    |
|                                                                | T <sub>A</sub> = 70°C |                                   | 20         | 12           |      |
| Pulsed Drain Current (10 μs Pulse Width)                       |                       | I <sub>DM</sub>                   | 60         |              |      |
| Continuous Source Current (Diode Conduction) <sup>a</sup>      |                       | I <sub>S</sub>                    | 4.5        | 1.6          |      |
| Maximum Power Dissipation <sup>a</sup>                         | T <sub>A</sub> = 25°C | P <sub>D</sub>                    | 5.4        | 1.9          | W    |
|                                                                | T <sub>A</sub> = 70°C |                                   | 3.4        | 1.2          |      |
| Operating Junction and Storage Temperature Range               |                       | T <sub>J</sub> , T <sub>stg</sub> | -55 to 150 |              | °C   |

## THERMAL RESISTANCE RATINGS

| Parameter                                |                 | Symbol     | Typical | Maximum | Unit               |
|------------------------------------------|-----------------|------------|---------|---------|--------------------|
| Maximum Junction-to-Ambient <sup>a</sup> | $t \leq 10$ sec | $R_{thJA}$ | 18      | 23      | $^\circ\text{C/W}$ |
|                                          | Steady State    |            | 50      | 65      |                    |
| Maximum Junction-to-Case (Drain)         | Steady State    | $R_{thJC}$ | 1.0     | 1.5     |                    |

Notes

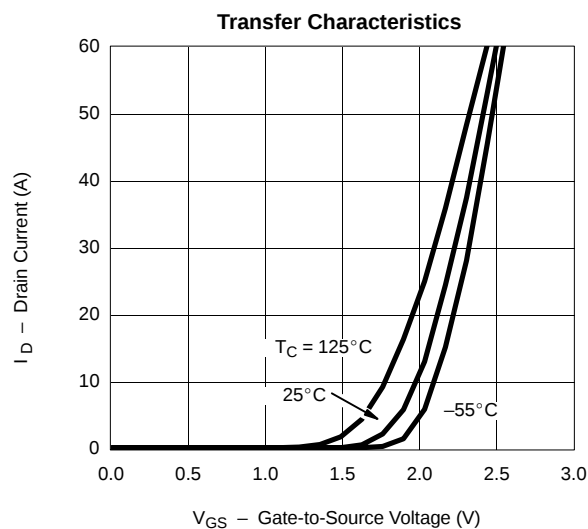
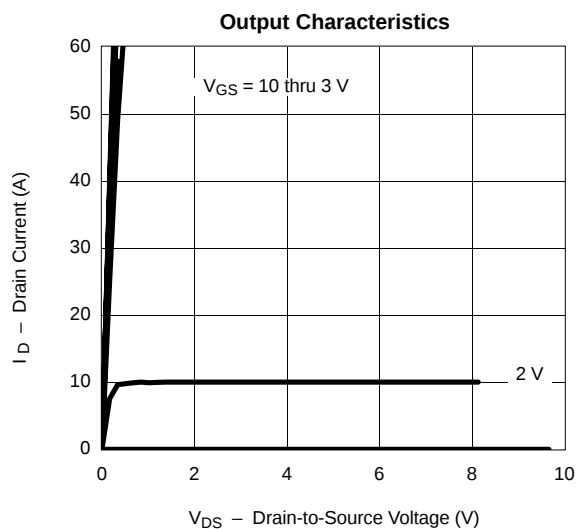
a. Surface Mounted on 1" x 1" FR4 Board.

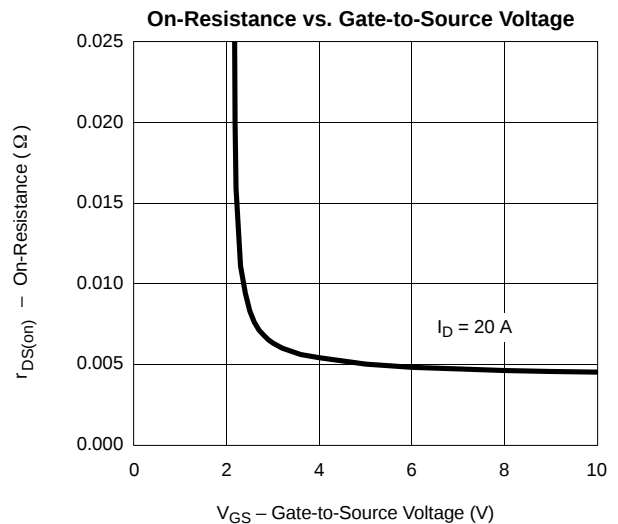
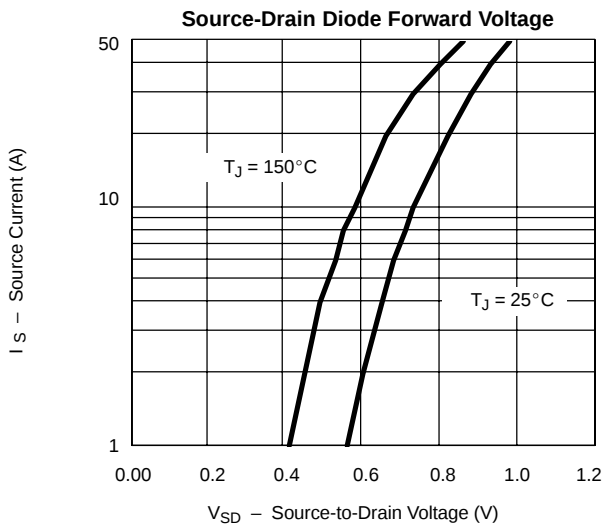
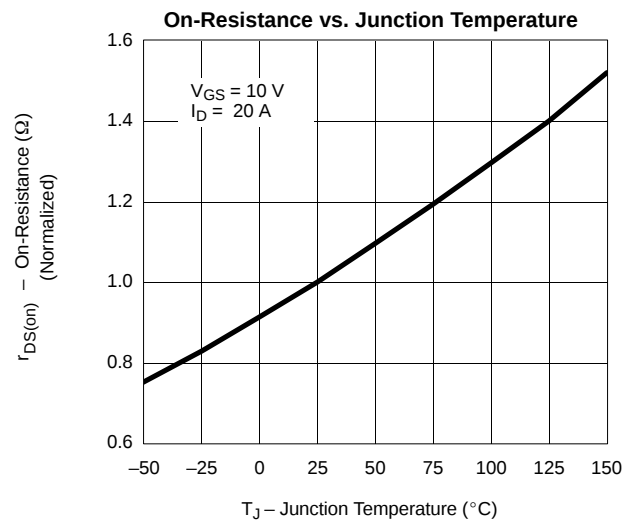
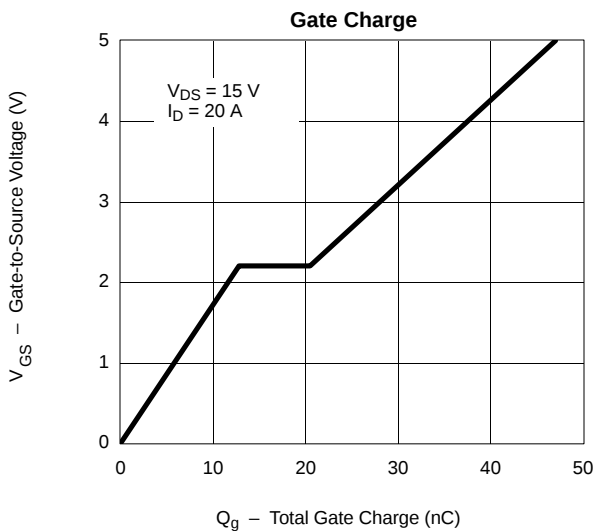
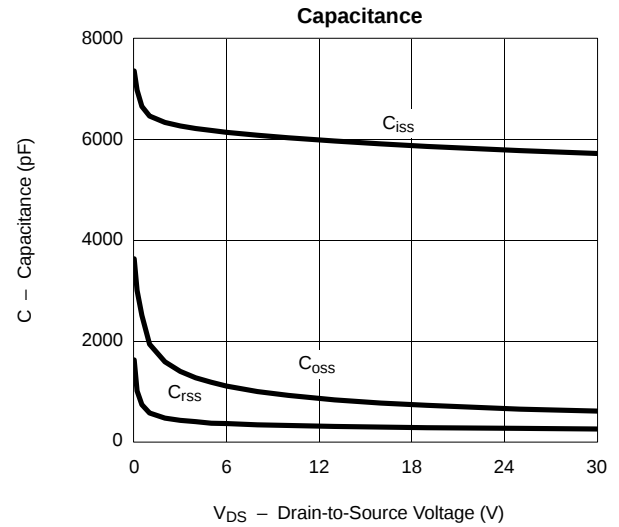
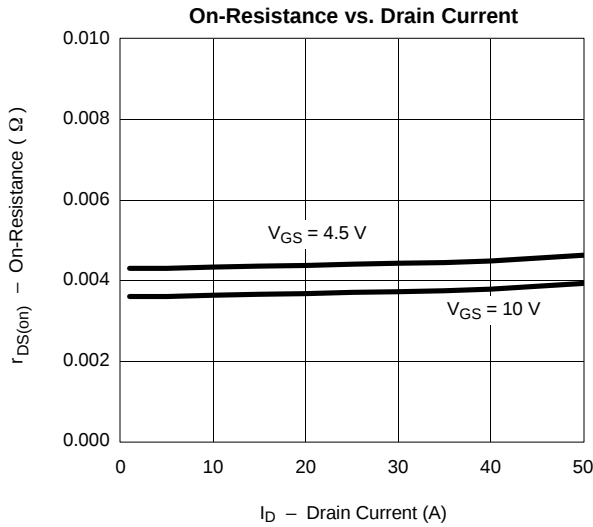
**SPECIFICATIONS ( $T_J = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)**

| Parameter                                     | Symbol       | Test Condition                                                                                                         | Min | Typ    | Max       | Unit          |
|-----------------------------------------------|--------------|------------------------------------------------------------------------------------------------------------------------|-----|--------|-----------|---------------|
| <b>Static</b>                                 |              |                                                                                                                        |     |        |           |               |
| Gate Threshold Voltage                        | $V_{GS(th)}$ | $V_{DS} = V_{GS}$ , $I_D = 250\ \mu\text{A}$                                                                           | 0.6 | 1      | 1.4       | V             |
| Gate-Body Leakage                             | $I_{GSS}$    | $V_{DS} = 0\ \text{V}$ , $V_{GS} = \pm 12\ \text{V}$                                                                   |     |        | $\pm 100$ | nA            |
| Zero Gate Voltage Drain Current               | $I_{DSS}$    | $V_{DS} = 24\ \text{V}$ , $V_{GS} = 0\ \text{V}$                                                                       |     |        | 1         | $\mu\text{A}$ |
|                                               |              | $V_{DS} = 24\ \text{V}$ , $V_{GS} = 0\ \text{V}$ , $T_J = 55^\circ\text{C}$                                            |     |        | 5         |               |
| On-State Drain Current <sup>a</sup>           | $I_{D(on)}$  | $V_{DS} \geq 5\ \text{V}$ , $V_{GS} = 10\ \text{V}$                                                                    | 30  |        |           | A             |
| Drain-Source On-State Resistance <sup>a</sup> | $r_{DS(on)}$ | $V_{GS} = 10\ \text{V}$ , $I_D = 25\ \text{A}$                                                                         |     | 0.0035 | 0.0045    | $\Omega$      |
|                                               |              | $V_{GS} = 4.5\ \text{V}$ , $I_D = 23\ \text{A}$                                                                        |     | 0.0042 | 0.0055    |               |
| Forward Transconductance <sup>a</sup>         | $g_{fs}$     | $V_{DS} = 15\ \text{V}$ , $I_D = 25\ \text{A}$                                                                         |     | 90     |           | S             |
| Diode Forward Voltage <sup>a</sup>            | $V_{SD}$     | $I_S = 2.9\ \text{A}$ , $V_{GS} = 0\ \text{V}$                                                                         |     | 0.75   | 1.1       | V             |
| <b>Dynamic<sup>b</sup></b>                    |              |                                                                                                                        |     |        |           |               |
| Total Gate Charge                             | $Q_g$        | $V_{DS} = 15\ \text{V}$ , $V_{GS} = 4.5\ \text{V}$ , $I_D = 25\ \text{A}$                                              |     | 42     | 55        | nC            |
| Gate-Source Charge                            | $Q_{gs}$     |                                                                                                                        |     | 12.8   |           |               |
| Gate-Drain Charge                             | $Q_{gd}$     |                                                                                                                        |     | 7.7    |           |               |
| Gate Resistance                               | $R_G$        |                                                                                                                        |     | 1.3    |           | $\Omega$      |
| Turn-On Delay Time                            | $t_{d(on)}$  | $V_{DD} = 15\ \text{V}$ , $R_L = 15\ \Omega$<br>$I_D \cong 1\ \text{A}$ , $V_{GEN} = 10\ \text{V}$ , $R_G = 6\ \Omega$ |     | 17     | 30        | ns            |
| Rise Time                                     | $t_r$        |                                                                                                                        |     | 14     | 25        |               |
| Turn-Off Delay Time                           | $t_{d(off)}$ |                                                                                                                        |     | 158    | 230       |               |
| Fall Time                                     | $t_f$        |                                                                                                                        |     | 43     | 65        |               |
| Source-Drain Reverse Recovery Time            | $t_{rr}$     | $I_F = 2.9\ \text{A}$ , $di/dt = 100\ \text{A}/\mu\text{s}$                                                            |     | 50     | 80        |               |

## Notes

- a. Pulse test; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.

**TYPICAL CHARACTERISTICS ( $25^\circ\text{C}$  UNLESS NOTED)**

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