

SWITCHING REGULATOR POWER OUTPUT STAGES

DESCRIPTION

The SM600/601/602 and SM610/611/612 series of Power Output Stages are especially designed to be driven with standard PWM integrated circuits to form an efficient switching power supply. The SM600, SM601 and SM602 are optimized for non-isolated Buck and Buck-Boost application, where SM610, SM611 and SM612 are best suited for DC-DC Boost type applications as well as negative output Buck Converters. The hybrid circuit construction utilizes thick film resistors on a beryllia substrate for maximum thermal conductivity and resultant low thermal impedance. All of the active elements in the hybrid are fully passivated.

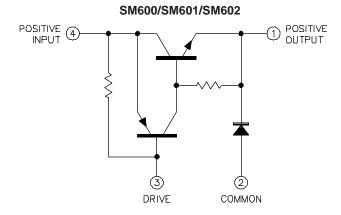
FEATURES

- Equivalent to the Unitrode PIC 600, 601, 602, 610, 611, 612
- 5A current capability
- Designed and characterized for switching regulator applications such as Buck, Boost, and Buck-Boost type
- Cost saving design reduces size, improves efficiency, reduces noise and RFI
- High operating efficiency at 2A typical performance -Rise and fall time < 75ns
 Efficiency > 85%
- Electrically isolated, 4-pin, TO-66 hermetic case

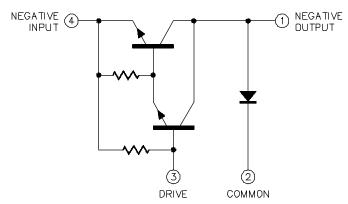
HIGH RELIABILITY FEATURES

♦ Available with high reliability processing

FUNCTIONAL DIAGRAM



SM610/SM611/SM612



SM600/SM601/SM602/SM610/SM611/SM612

ABSOLUTE MAXIMUM RATINGS (Note 1)	SM600	SM601	SM602	SM610	SM611	SM612	
Input Voltage, V ₄₋₂		80V	100V	-60V	-80V	-100V	
Output Voltage, V ₁₋₂	60V	80V	100V	-60V	-80V	-100V	
Drive Input Reverse Voltage, V ₃₋₄	5V	5V	5V	-5V	-5V	-5V	
Output Current, I,	5A	5A	5A	-5A	-5A	-5A	
Drive Current, I ₃	-0.2A	-0.2A	-0.2A	0.2A	0.2A	0.2A	
Thermal Resistance		Operating	g Junction ⁻	Temperatur	е		
Power Switch, $\theta_{\text{L-C}}$	4.0°C/W	Herme	tic (R Pack	age)			150°C
Commutating Diode	4.0°C/W	Storage	Temperatur	e Range		65°C to	150°C
Case to Ambient, $\theta_{\text{C-A}}$	0.0°C/W	Lead Ter	mperature (Soldering, 1	10 Seconds)	300°C
Note 1. Exceeding these ratings could cause damage to the	device.						
THERMAL DATA							

R Package:

Note A. Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$. Note B. The above numbers for θ_{JC} are maximums for the limiting thermal resistance of the package in a standard mount-

thermal resistance of the package in a standard mounting configuration. The θ_{JA} numbers are meant to be guidelines for the thermal performance of the device/pcboard system. All of the above assume no ambient airflow.

RECOMMENDED OPERATING CONDITIONS (Note 2)

	SM600	SM601	SM602	SM610	SM611	SM612
Input Voltage, V ₄₋₂	50V	70V	90V	-50V	-70V	-90V
Output Voltage, V ₁₋₂	50V	70V	90V	-50V	-70V	-90V
Drive Input Reverse Voltage, V ₃₋₄		3V	3V	-3V	-3V	-3V
Output Current, I,	4A	4A	4A	-4A	-4A	-4A
Drive Current, I,	-0.1A	-0.1A	-0.1A	0.1A	0.1A	0.1A

Operating Ambient Temperature Range SM6XXR0°C to 70°C SM6XXHRR55°C to 125°C

Note 2. Range over which the device is functional.

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, these specifications apply for the operating ambient temperature of $T_A = 25$ °C. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

Parameter	Test Conditions	SG6	SG600/601/602			SG610/611/612		
raiailletei	rest Conditions		Тур.	Max.	Min.	Тур.	Max.	Units
On-State Voltage (Note 3)	$I_4 = 2A(-2A), I_3 = -20mA (20mA)$		1.0	1.5		-1.0	-1.5	V
	$I_4 = 5A(-5A), I_3 = -20mA (20mA)$		2.5	3.5		-2.5	-3.5	V
Diode Forward Voltage (Note 3)	$I_{4} = 2A(-2A)$		0.8	1.0		-0.8	-1.0	V
_	$I_{4} = 5A(-5A)$		1.0	1.5		-1.0	-1.5	V
Off-State Current	V ₄ = Rated input voltage		0.1	10		-0.1	-10	μΑ
	V ₄ = Rated input voltage, T _A = 125°C		10			-10		μΑ
Diode Reverse Current	V ₁ = Rated output voltage		1.0	10		-1.0	-10	μΑ
	$V_1 = Rated output voltage, T_A = 125 °C$		500			-500		μΑ

Note 3. Pulse test: Duration = $300\mu s$, Duty Cycle $\leq 2\%$.

SM600/SM601/SM602/SM610/SM611/SM612

ELECTRICAL CHARACTERISTICS (continued)

Parameter	Test Conditions -	SG6	SG600/601/602			SG610/611/612		
		Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Dynamic Characteristics (See Figures 1 & 2) (Notes 4 & 5)								
Current Delay Time			20	40		20	40	ns
Current Rise Time			50	75		50	75	ns
Voltage Rise Time			30	50		30	50	ns
Voltage Storage Time			450			450		ns
Voltage Fall Time			50	75		50	75	ns
Current Fall Time			70	150		70	150	ns
Efficiency (Note 5)			85			85		%

AC TEST CIRCUIT AND SWITCHING TIME WAVEFORMS (Note 6)

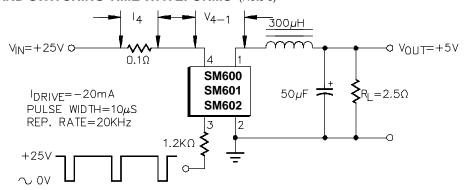


FIGURE 1 - SM600/601/602 SWITCHING SPEED CIRCUIT

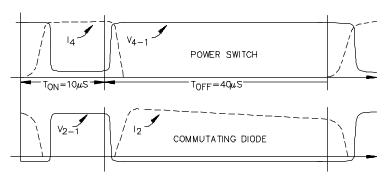


FIGURE 2 - SM600/601/602 SWITCHING WAVEFORMS

- Note 4. In switching an inductive load, the current will lead the voltage on turn-on and lag the voltage on turn-off (see Figure 2). Therefore, Voltage Delay Time $(t_{DV}) \cong t_{d} + t_{n}$ and Current Storage Time $(t_{s}) \cong t_{sv} + t_{N}$. Note 5. The efficiency is a measure of internal power losses and is equal to Output Power divided by Input Power. The switching speed circuit of Figure
- 1, in which the efficiency measured, is representaive of typical operating conditions for the SM600 series switching regulators.
- Note 6. SM610/611/612 Test Circuit and waveforms are identical but of opposite polarity ($V_N = -25V$, $V_{OUT} = -5V$, $I_{DRIVE} = +20$ mA).

APPLICATION CIRCUITS

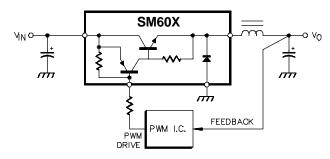


FIGURE 3 - STEP DOWN (BUCK) CONVERTER

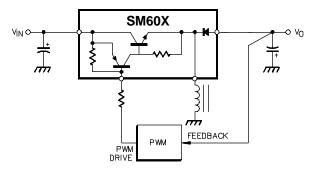


FIGURE 4 - NEGATIVE OUTPUT DOWN/UP (BUCK-BOOST) CONVERTER

SM600/SM601/SM602/SM610/SM611/SM612

APPLICATION CIRCUITS (continued)

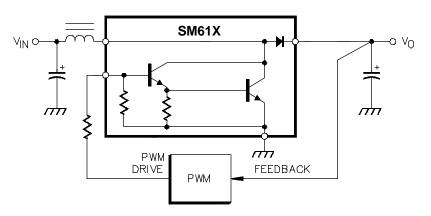


FIGURE 5 - STEP UP (BOOST) CONVERTER

CONNECTION DIAGRAMS & ORDERING INFORMATION (See Note Below)

Package	Part No.	Ambient Temperature Range	Connection Diagram
4-PIN TO-66 METAL CAN R - PACKAGE	SM600R SM600HRR SM601R SM601HRR SM602R SM602HRR	0°C to 70°C -55°C to 125°C 0°C to 70°C -55°C to 125°C 0°C to 70°C -55°C to 125°C	POSITIVE OUTPUT 1 4 2 3 COMMON DRIVE
	SM610R SM610HRR SM611R SM611HRR SM612R SM612HRR	0°C to 70°C -55°C to 125°C 0°C to 70°C -55°C to 125°C 0°C to 70°C -55°C to 125°C	NEGATIVE OUTPUT 1 4 COMMON DRIVE

Note 1. All packages are viewed from the top.

- 2. Case is electrically isolated.
- 3. Consult factory for additional screening available.