

DESCRIPTION

The M56754SP is a semiconductor IC developed as the motor driver used for CD-ROM and CD player.

This circuit includes 4-Channel BTL Driver that is able to drive four actuator with one IC.

FEATURES

- Low Output saturation voltage(typ:1.35V at 0.5A)
- Large operating supply voltage range(4.5–13.2V)
- Minimum Crossover distortion
- Built-in general operational amplifier

APPLICATION

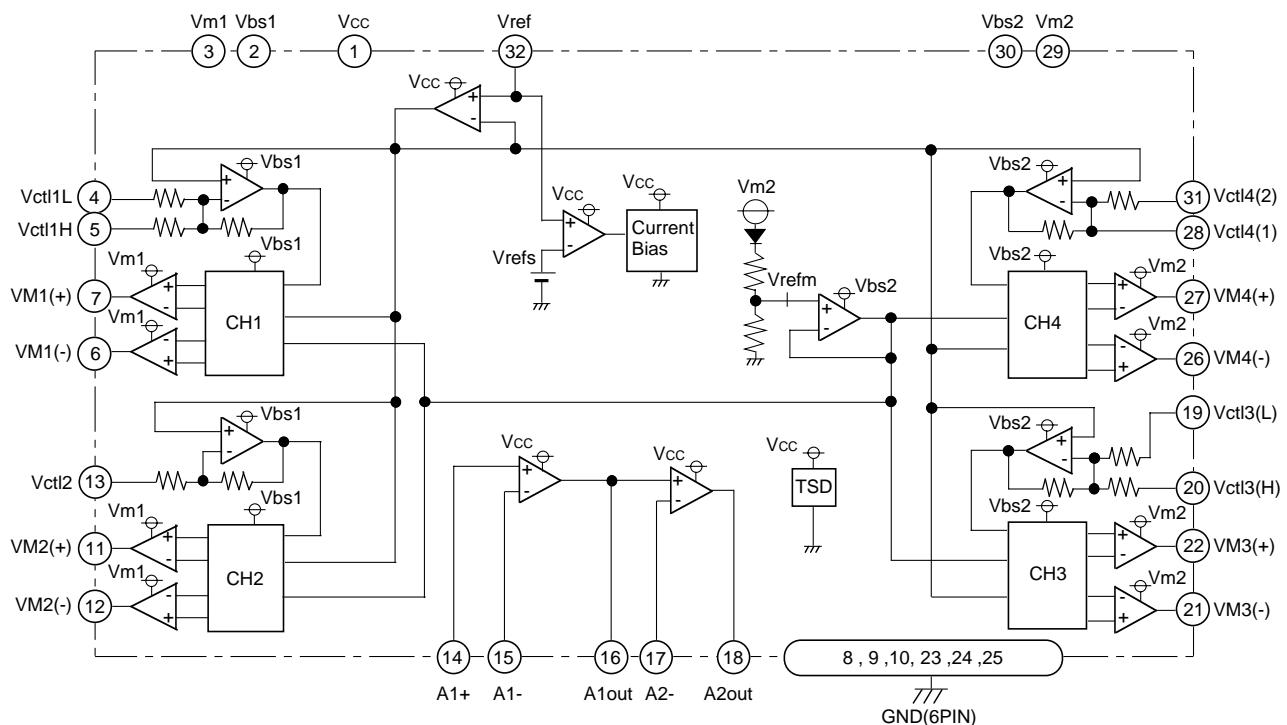
CD-ROM, CD player

PIN CONFIGURATION (TOP VIEW)

Vcc	1	Vref
Vbs1	2	Vctl4(2)
Vm1	3	30 Vbs2
Vctl1L	4	29 Vm2
Vctl1H	5	28 Vctl4(1)
VM1(-)	6	27 VM4(+)
VM1(+)	7	26 VM4(-)
GND	8	25
	9	24 GND
	10	23
VM2(+)	11	22 VM3(+)
VM2(-)	12	21 VM3(-)
Vctl2	13	20 Vctl3H
A1+	14	19 Vctl3L
A1-	15	18 A2out
A1out	16	17 A2-

M56754SP

Outline 32P4B

BLOCK DIAGRAM

PIN DESCRIPTIONS

Pin No.	Symbol	Function	Pin No.	Symbol	Function
①	Vcc	5V power supply	⑯	A2-	OP amp. 2 inverted input terminal
②	Vbs1	Boot strap power supply terminal 1	⑯	A2out	OP amp. 2 output terminal
③	Vm1	Motor power supply terminal 1	⑯	Vctl3L	CH3 amp. L gain input terminal
④	Vctl1L	CH1 amp. L gain input terminal	⑯	Vctl3H	CH3 amp. H gain input terminal
⑤	Vctl1H	CH1 amp. H gain input terminal	⑯	VM3(-)	CH3 amp. inverted output terminal
⑥	VM1(-)	CH1 amp. inverted output terminal	⑯	VM3(+)	CH3 amp. non-inverted output terminal
⑦	VM1(+)	CH1 amp. non-inverted output terminal	⑯-⑯	GND	GND
⑧-⑩	GND	GND	⑯	VM4(-)	CH4 amp. inverted output terminal
⑪	VM2(+)	CH2 amp. non-inverted output terminal	⑯	VM4(+)	CH4 amp. non-inverted output terminal
⑫	VM2(-)	CH2 amp. inverted output terminal	⑯	Vctl4(1)	CH4 amp. input terminal 1
⑬	Vctl2	CH2 amp. input terminal	⑯	Vm2	Motor power supply terminal 2
⑭	A1+	OP amp. 1 non-inverted input terminal	⑯	Vbs2	Boot strap power supply terminal 2
⑮	A1-	OP amp. 1 inverted input terminal	⑯	Vctl4(2)	CH4 amp. input terminal 2
⑯	A1out	OP amp. 1 output terminal	⑯	Vref	Reference voltage input terminal

ABSOLUTE MAXIMUM RATING (Ta=25°C)

Symbol	Parameter	Condition	Rating	Unit
Vbs	Supply voltage	②, ⑯ pin	15	V
Vm	Supply voltage	③, ⑯ pin	15	V
Vcc	Supply voltage	① pin	7	V
Io	Output current		700	mA
Vin	Maximum Input Voltage of each Terminal	④, ⑤, ⑬, ⑭, ⑮, ⑯, ⑯, ⑯, ⑯, ⑯, ⑯, ⑯ pin	0-Vcc	V
Pt	Power Dissipation	With infinite heatsink Without heatsink	4.5 1.75	W
Kθ	Thermal derating	With infinite heatsink Without heatsink	36 14	mW/°C
Tj	Junction temperature		150	°C
Topr	Operating temperature		-10-75	°C
Tstg	Storage temperature		-40-125	°C

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Limits			Unit
		Min.	Typ.	Max.	
Vcc	5V Power Supply Voltage	4.5	5.0	5.5	V
Vm1, Vm2	Motor Power Supply Voltage	—	5.0	—	V
Vbs1, Vbs2	Boot Strap Supply Voltage	—	Vm+1	—	V

ELECTRICAL CHARACTERISTICS

(Ta=25°C, Vcc=Vbs1=Vbs2=Vm1=Vm2=5V, no-load unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
Icc1	Circuit current 1 on no-signal	(②,③,⑨,⑩) pin current	—	32	44	mA
Icc2	Circuit current 2 on no-signal	(①) pin current	—	10	14	mA
Vrefm	Vrefm voltage		—	2.15	—	V
Vospm	Spindle driver Output voltage D range	Io=0.5A Vrefm reference	-1.55	—	1.55	V
Vsat1	Output saturation voltage	Top and bottom saturation voltage of output power Tr (Io=0.5A)	—	1.35	1.9	V
Vsat2	Output saturation voltage (At boot)	Top and bottom saturation voltage of output power Tr (Io=0.5A)	—	0.7	1.0	V
Vofs1.2.3	Output amp., Offset voltage	1,2,3ch amp., Vref=2.5V, 10kΩ connection between Vctl and Vref	-40	—	+40	mV
Vofs4	Output amp., Offset voltage	4ch amp., Vref=2.5V 10kΩ connection between Vctl and Vref	-70	—	+70	mV
Gain1L	Gain between input and output	Gain L between CH1 input and output $\frac{\{VM1(+)-VM1(-)\}}{(Vctl1L-Vref)}$ (Vctl1L=④pin)	6.2	6.7	7.2	dB
Gain1H		Gain H between CH1 input and output $\frac{\{VM1(+)-VM1(-)\}}{(Vctl1H-Vref)}$ (Vctl1H=⑤pin)	18.8	19.3	19.8	dB
Gain2		Gain between CH2 input and output $\frac{\{VM2(+)-VM2(-)\}}{(Vctl2-Vref)}$ (Vctl2=⑬pin)	31.8	32.3	32.8	dB
Gain3L		Gain L between CH3 input and output $\frac{\{VM3(+)-VM3(-)\}}{(Vctl3L-Vref)}$ (Vctl3L=⑯pin)	5.1	5.6	6.1	dB
Gain3H		Gain H between CH3 input and output $\frac{\{VM3(+)-VM3(-)\}}{(Vctl3H-Vref)}$ (Vctl3H=⑰pin)	10.2	10.7	11.2	dB
Gain41		Gain 1 between CH4 input and output $\frac{\{VM4(+)-VM4(-)\}}{(Vctl4(1)-Vref)}$ (10kΩ connection with Vctl4(1)=⑲pin)	22.6	25.0	27.2	dB
Gain42		Gain 2 between CH4 input and output $\frac{\{VM4(+)-VM4(-)\}}{(Vctl4(2)-Vref)}$ (Vctl4L=⑳pin)	36.5	37.0	37.5	dB
Vrefin	Vref amp.	Vref amp. input voltage range	Vrefs	2.5	Vcc-1.2	V
Vrefs		Vref supervisory voltage	—	1.4	1.7	V
Vopin	OP amp.	Operational amp. input voltage range	0.5	—	Vcc-1.0	V
Vopout		Operational amp. output voltage range Io=±2mA	0.5	—	Vcc-1.0	V
Vofsop		Operational amp. offset voltage Vin=2.5V	-10	—	+10	mV
Ibop		Operational amp. input current Vin=2.5V	-1	-0.15	0	μA
Ibofs		Operational amp. input current offset	-100	0	+100	nA
GB		Operational amp. GB product	2.3	4	—	MHz

CHECK ITEMS FOR EVALUATION

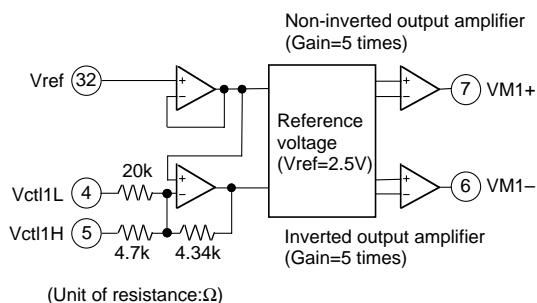
	Symbol	Parameter	Test conditions	Limits			Unit
				Min.	Typ.	Max.	
1	Ttsd	Thermal Shutdown Operating Temperature		—	165	—	°C
2	THtsd	Thermal Shutdown Hysteresis Temperature		—	40	—	°C

The thermal shutdown temperature shown in this table does not assure the above thermal shutdown operating temperature range of the device operation. The operating assurance range of the device covers up to Tjmax defined with the absolute maximum rating. The thermal shutdown operation shown in this table is a thermal protection circuit applied when the temperature exceeds this Tjmax by mistake. Therefore, be sure to operate this device at the temperature lower than or equal to Tj=150°C.

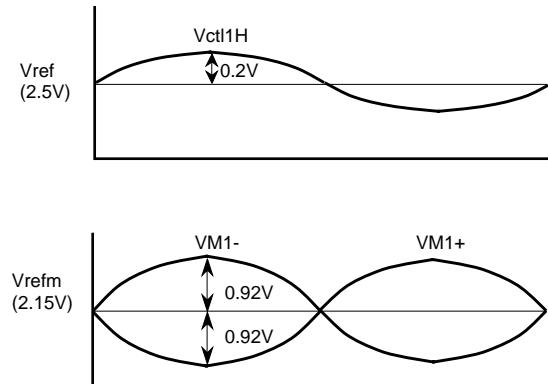
BASIC CHARACTERISTICS

Input/output Characteristics of Amplifier

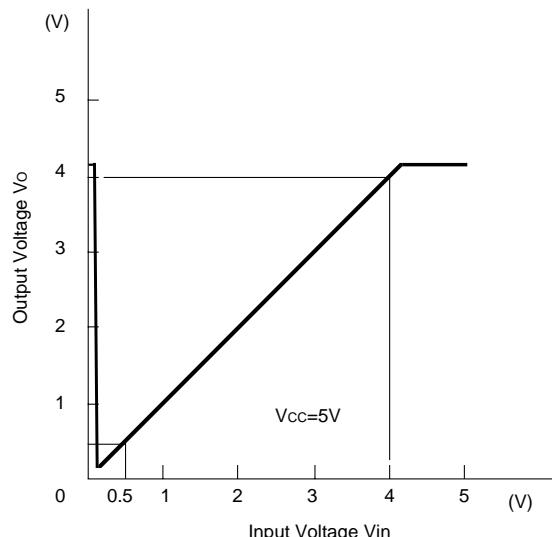
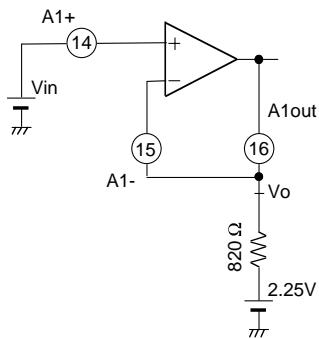
CH1 AMP.



Input/output voltage for V_{ctl1H} (5) pin input

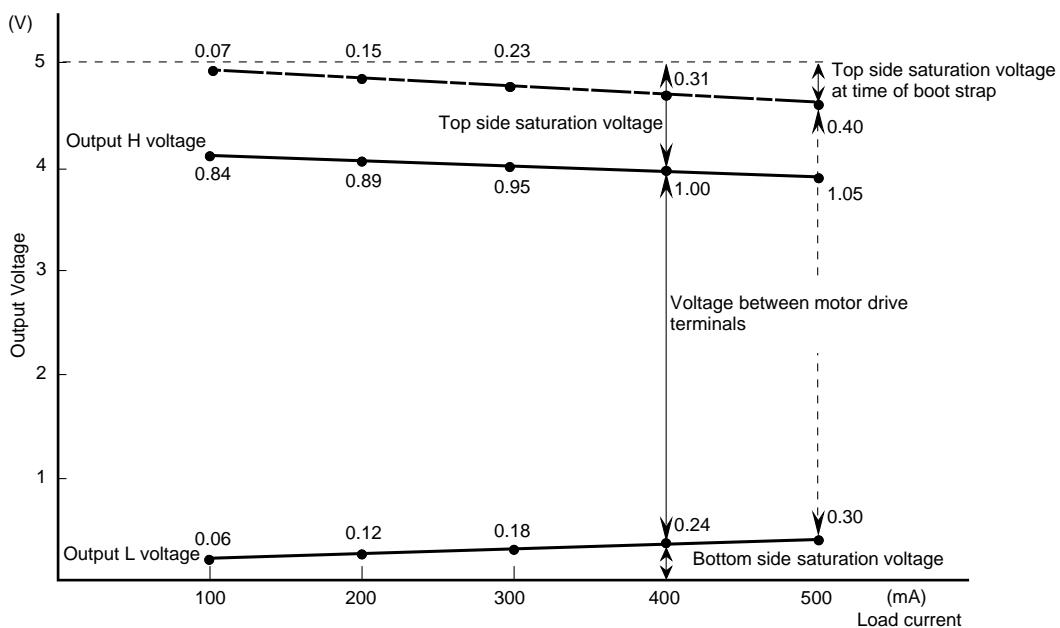
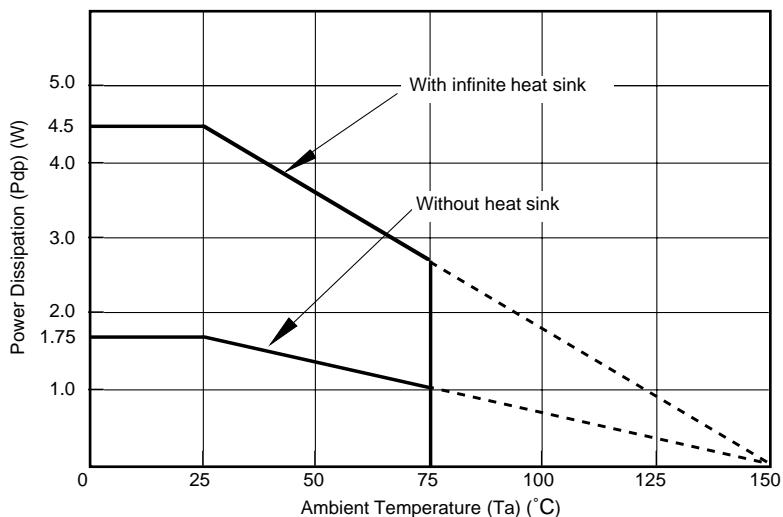


OPERATIONAL AMP.



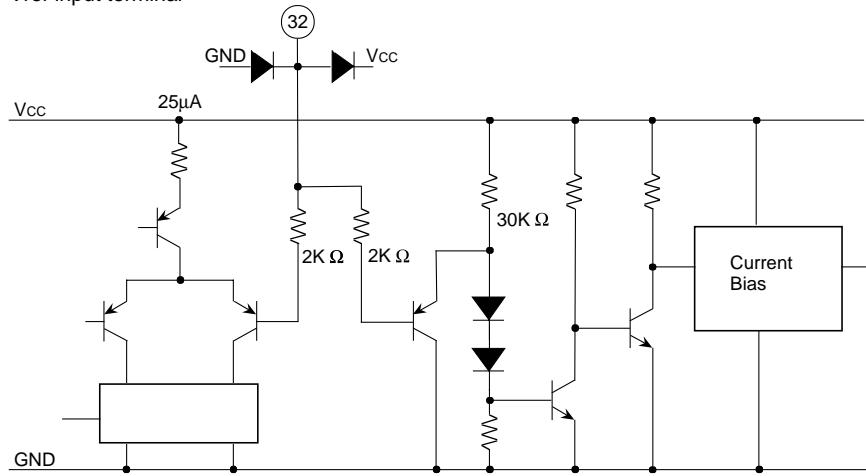
Output Saturation Voltage–Load Current Characteristics

($V_{bs}=V_m=V_{cc}=5V$) : ——————
 ($V_{bs}=12V, V_m=V_{cc}=5V$) at time of Boot strap : - - - - -

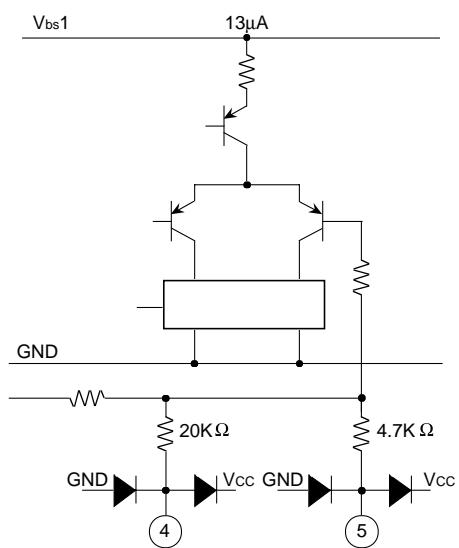
**Thermal derating**

INPUT/OUTPUT EQUIVALENT CIRCUIT

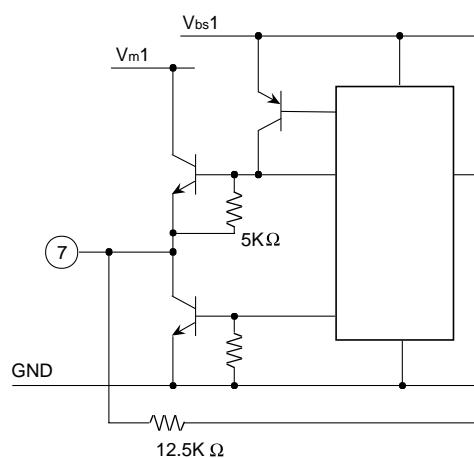
Vref input terminal



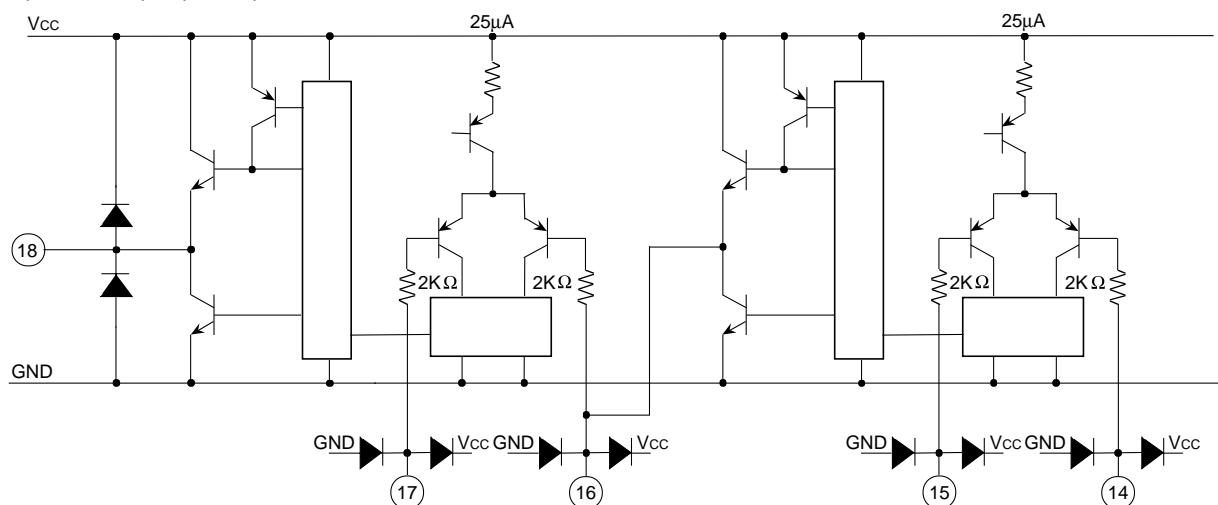
CH1 amp. input terminal



BTL driver output terminal



Operation Amp. Input/Output Terminal



4-CHANNEL ACTUATOR MOTOR DRIVER

APPLICATION EXAMPLE

