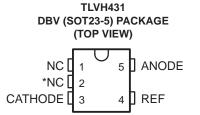
TLVH431, TLVH431A, TLVH431B TLVH432, TLVH432A, TLVH432B LOW-VOLTAGE ADJUSTABLE PRECISION SHUNT REGULATOR

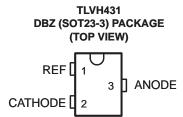
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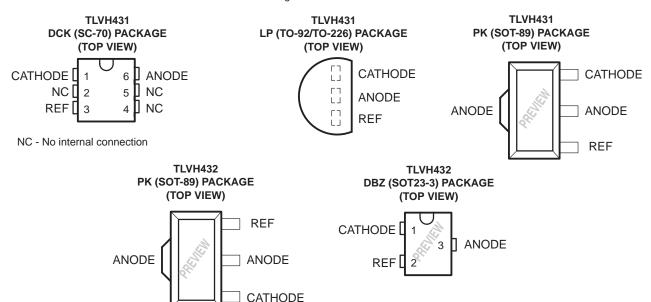
- Low-Voltage Operation . . . Down to 1.24 V
- Reference Voltage Tolerances at 25°C
 - 0.5% for B Grade
 - 1% for A Grade
 - 1.5% for Standard Grade
- Adjustable Output Voltage, V_O = V_{REF} to 18 V
- Wide Operating Cathode Current Range . . .
 55 μA to 80 mA
- 0.25-Ω Typical Output Impedance
- -40°C to 125°C Specifications
- TLVH432 Provides Alternative Pinouts for SOT-23-3 and SOT-89 Packages
- Ultra-Small SC-70 Package Offers 40% Smaller Footprint Than SOT-23-3



NC - No internal connection

* Pin 2 is connected internally to ANODE
(die substrate). Pin 2 should be
connected to ANODE or left floating.





description/ordering information

The TLVH431 and TLVH432 are low-voltage 3-terminal adjustable voltage references with specified thermal stability over applicable industrial and commercial temperature ranges. Output voltage can be set to any value between V_{REF} (1.24 V) and 18 V with two external resistors (see Figure 2). These devices operate from a lower voltage (1.24 V) than the widely used TL431 and TL1431 shunt-regulator references.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



TLVH431, TLVH431A, TLVH431B TLVH432, TLVH432A, TLVH432B LOW-VOLTAGE ADJUSTABLE PRECISION SHUNT REGULATOR

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description/ordering information (continued)

When used with an optocoupler, the TLVH431 and TLVH432 are ideal voltage references in isolated feedback circuits for 3-V to 3.3-V switching-mode power supplies. They have a typical output impedance of 0.25 Ω . Active output circuitry provides a very sharp turn-on characteristic, making the TLVH431 and TLVH432 excellent replacements for low-voltage Zener diodes in many applications, including on-board regulation and adjustable power supplies.

The TLVH432 is identical to the TLVH431, but is offered with different pinouts for the SOT23-3 and SOT-89 packages.



ORDERING INFORMATION

TJ	V _{REF} TOLERANCE	PACK	AGE†	ORDERABLE PART NUMBER	TOP-SIDE MARKING [‡]
		00.70 (DOM)	Reel of 3000	TLVH431BCDCKR	VIII
		SC-70 (DCK)	Reel of 250	TLVH431BCDCKT	YH_
		COT 02 F (DD)()	Reel of 3000	TLVH431BCDBVR	Y3J_
		SOT-23-5 (DBV)	Reel of 250	TLVH431BCDBVT	PREVIEW
			D 1 (0000	TLVH431BCDBZR	Y3J_
	0.59/	007.00.0 (DD7)	Reel of 3000	TLVH432BCDBZR	
0.5%	0.5%	SOT-23-3 (DBZ)		TLVH431BCDBZT	PREVIEW
			Reel of 250	TLVH432BCDBZT	1
				TLVH431BCPK	DDEVIEW
		SOT-89 (PK)	Reel of 1000	TLVH432BCPK	PREVIEW
		TO 00 (I D)	Bulk of 1000	TLVH431BCLP	74.404B
		TO-92 (LP)	Reel of 2000	TLVH431BCLPR	ZA431B
			Reel of 3000	TLVH431ACDCKR	DDE\((E\)
		SC-70 (DCK)	Reel of 250	TLVH431ACDCKT	PREVIEW
			Reel of 3000	TLVH431ACDBVR	DDE\//E\/
		SOT-23-5 (DBV)	Reel of 250	TLVH431ACDBVT	PREVIEW
				TLVH431ACDBZR	
		SOT-23-3 (DBZ)	Reel of 3000	TLVH432ACDBZR	1
0°C to 70°C	1%		Reel of 250	TLVH431ACDBZT	PREVIEW
				TLVH432ACDBZT	1
			1	TLVH431ACPK	DD E\
		SOT-89 (PK)	Reel of 1000	TLVH432ACPK	PREVIEW
			Bulk of 1000	TLVH431ACLP	DDE\/IEW
		TO-92 (LP)	Reel of 2000	TLVH431ACLPR	PREVIEW
			Reel of 3000	TLVH431CDCKR	DDE\//E\A/
		SC-70 (DCK)	Reel of 250	TLVH431CDCKT	PREVIEW
			Reel of 3000	TLVH431CDBVR	DDE\/IEW
		SOT-23-5 (DBV)	Reel of 250	TLVH431CDBVT	PREVIEW
			l	TLVH431CDBZR	
			Reel of 3000	TLVH432CDBZR	DDE///EW
	1.5%	SOT-23-3 (DBZ)		TLVH431CDBZT	PREVIEW
			Reel of 250	TLVH432CDBZT	1
				TLVH431CPK	
		SOT-89 (PK)	Reel of 1000	TLVH432CPK	PREVIEW
			Bulk of 1000	TLVH431CLP	DDE\/IE\//
		TO-92 (LP)	Reel of 2000	TLVH431CLPR	PREVIEW

[†]Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



[‡] DBV/DBZ/DCK: The actual top-side marking has one additional character that designates the assembly/test site.

ORDERING INFORMATION (continued)

ТЈ	V _{REF} TOLERANCE	PACKA	GEŤ	ORDERABLE PART NUMBER	TOP-SIDE MARKING [‡]
		00.70 (DOM)	Reel of 3000	TLVH431BIDCKR	V.1
		SC-70 (DCK)	Reel of 250	TLVH431BIDCKT	YJ_
		COT 02 5 (DD)//	Reel of 3000	TLVH431BIDBVR	Y3K_
		SOT-23-5 (DBV)	Reel of 250	TLVH431BIDBVT	PREVIEW
			D1 -1 0000	TLVH431BIDBZR	Y3K_
	0.5%	007.00.0 (DD7)	Reel of 3000	TLVH432BIDBZR	
	0.5%	SOT-23-3 (DBZ)	D 1 (050	TLVH431BIDBZT	PREVIEW
			Reel of 250	TLVH432BIDBZT	
		()		TLVH431BIPK	DDEVIEW
		SOT-89 (PK)	Reel of 1000	TLVH432BIPK	PREVIEW
		TO 00 (LP)	Bulk of 1000	TLVH431BILP	7D404D
		TO-92 (LP)	Reel of 2000	TLVH431BILPR	ZB431B
			Reel of 3000	TLVH431AIDCKR	
		SC-70 (DCK)	Reel of 250	TLVH431AIDCKT	PREVIEW
			Reel of 3000	TLVH431AIDBVR	
		SOT-23-5 (DBV)	Reel of 250	TLVH431AIDBVT	PREVIEW
				TLVH431AIDBZR	
	1%	SOT-23-3 (DBZ)	Reel of 3000	TLVH432AIDBZR	
-40°C to 85°C			Reel of 250	TLVH431AIDBZT	PREVIEW
				TLVH432AIDBZT	
				TLVH431AQPK	DDE\//EW
		SOT-89 (PK)	Reel of 1000	TLVH432AQPK	PREVIEW
			Bulk of 1000	TLVH431AILP	DDEVIEW
		TO-92 (LP)	Reel of 2000	TLVH431AILPR	PREVIEW
			Reel of 3000	TLVH431IDCKR	
		SC-70 (DCK)	Reel of 250	TLVH431IDCKT	PREVIEW
			Reel of 3000	TLVH431IDBVR	DDEVIEW
		SOT-23-5 (DBV)	Reel of 250	TLVH431IDBVT	PREVIEW
			1	TLVH431IDBZR	DDEVIEW
			Reel of 3000	TLVH432IDBZR	PREVIEW
	1.5%	SOT-23-3 (DBZ)		TLVH431IDBZT	DDE\/IE\
			Reel of 250	TLVH432IDBZT	PREVIEW
			1	TLVH431QPK	DDEVIEW
		SOT-89 (PK)	Reel of 1000	TLVH432IPK	PREVIEW
			Bulk of 1000	TLVH431ILP	DDE\//E\/
		TO-92 (LP)	Reel of 2000	TLVH431ILPR	PREVIEW

[†]Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



DBV/DBZ/DCK: The actual top-side marking has one additional character that designates the assembly/test site.

ORDERING INFORMATION (continued)

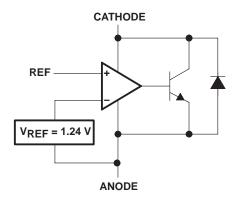
TJ	V _{REF} TOLERANCE	PACKA	AGE†	ORDERABLE PART NUMBER	TOP-SIDE MARKING [‡]
		00.70 (DOIA)	Reel of 3000	TLVH431BQDCKR	\/\/
		SC-70 (DCK)	Reel of 250	TLVH431BQDCKT	YK_
		COT 00 F (DD)/)	Reel of 3000	TLVH431BQDBVR	Y3L_
		SOT-23-5 (DBV)	Reel of 250	TLVH431BQDBVT	PREVIEW
			D1 - (0000	TLVH431BQDBZR	Y3L_
	0.5%	007.00.0 (DD7)	Reel of 3000	TLVH432BQDBZR	
	0.5%	SOT-23-3 (DBZ)		TLVH431BQDBZT	PREVIEW
			Reel of 250	TLVH432BQDBZT	
				TLVH431BQPK	DDEVIEW
		SOT-89 (PK)	Reel of 1000	TLVH432BQPK	PREVIEW
		TO 00 (I D)	Bulk of 1000	TLVH431BQLP	70.4040
		TO-92 (LP)	Reel of 2000	TLVH431BQLPR	ZD431B
			Reel of 3000	TLVH431AQDCKR	DDEVIEW
		SC-70 (DCK)	Reel of 250	TLVH431AQDCKT	PREVIEW
			Reel of 3000	TLVH431AQDBVR	
		SOT-23-5 (DBV)	Reel of 250	TLVH431AQDBVT	PREVIEW
				TLVH431AQDBZR	
			Reel of 3000	TLVH432AQDBZR	DD E\/!E\4/
-40°C to 125°C	1%	SOT-23-3 (DBZ)	Reel of 250	TLVH431AQDBZT	PREVIEW
				TLVH432AQDBZT	1
				TLVH431AQPK	DD E\/!E\//
		SOT-89 (PK)	Reel of 1000	TLVH432AQPK	PREVIEW
			Bulk of 1000	TLVH431AQLP	
		TO-92 (LP)	Reel of 2000	TLVH431AQLPR	PREVIEW
			Reel of 3000	TLVH431QDCKR	DDE\/!E\#/
		SC-70 (DCK)	Reel of 250	TLVH431QDCKT	PREVIEW
			Reel of 3000	TLVH431QDBVR	DDEVIEW
		SOT-23-5 (DBV)	Reel of 250	TLVH431QDBVT	PREVIEW
			<u> </u>	TLVH431QDBZR	
			Reel of 3000	TLVH432QDBZR	DDEVIEW
	1.5%	SOT-23-3 (DBZ)		TLVH431QDBZT	PREVIEW
			Reel of 250	TLVH432QDBZT	1
				TLVH431QPK	DDEV//EW
		SOT-89 (PK)	Reel of 1000	TLVH432QPK	PREVIEW
			Bulk of 1000	TLVH431QLP	
		TO-92 (LP)	Reel of 2000	TLVH431QLPR	PREVIEW

[†]Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

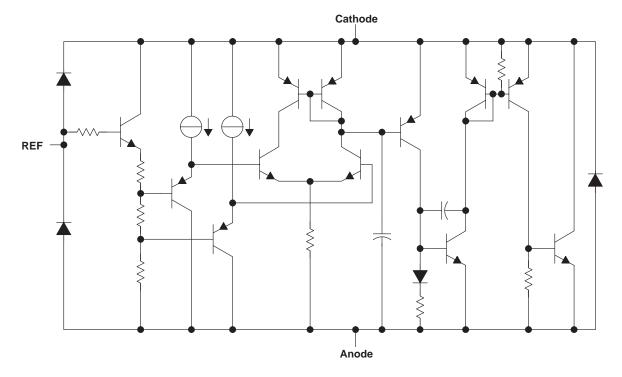


[‡] DBV/DBZ/DCK: The actual top-side marking has one additional character that designates the assembly/test site.

logic block diagram



equivalent schematic





TLVH431, TLVH431A, TLVH431B TLVH432, TLVH432A, TLVH432B LOW-VOLTAGE ADJUSTABLE PRECISION SHUNT REGULATOR

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Cathode voltage, V _{KA} (see Note 1)		
Reference current range, I _{ref}		
Package thermal impedance, θ_{IA} (see Notes 2 and 3):		
, , ,	DBZ package	206°C/W
	DCK package	252°C/W
	LP package	140°C/W
	PK package	52°C/W
Operating virtual junction temperature		150°C
Storage temperature range, T _{stg}		–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. Voltage values are with respect to the anode terminal, unless otherwise noted.
 - 2. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
 - 3. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions

			MIN	MAX	UNIT
VKA	Cathode voltage		VREF	18	V
lκ	Cathode current (continuous)		0.1	80	mA
		TLVH43X_C	0	70	
TA	Operating free-air temperature range	TLVH43X_I	-40	85	oC .
		TLVH43X_Q	-40	125	

TLVH431, TLVH431A, TLVH431B TLVH432, TLVH432A, TLVH432B

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TLVH431 electrical characteristics at 25°C free-air temperature (unless otherwise noted)

	24244555		ANDITIONS		TLVH4	131, TL\	/H432	
	PARAMETER	IEST	CONDITIONS		MIN	TYP	MAX	UNIT
			T _A = 25°C		1.222	1.24	1.258	
\/	Reference voltage	V _{KA} = V _{REF} ,	T _A = full range	TLVH431C	1.21		1.27	.,
VREF		$I_K = 10 \text{ mA}$	(see Note 4 and	TLVH431I	1.202		1.278	V
			Figure 1)	TLVH431Q	1.194		1.286	
				TLVH431C		4	12	
V _{REF(dev)}	V _{REF} deviation over full temperature range (see Note 5)	$V_{KA} = V_{REF}, I_{K}$ (see Note 4 and		TLVH431I		6	20	mV
, ,	temperature range (see Note s)	(See Note 4 and	rigaro i)	TLVH431Q		11	31	
$\frac{\Delta V_{RE}}{\Delta V_{KA}}$ F	Ratio of V _{REF} change to cathode voltage change	I _K = 10 mA (see Figure 2)	V _{KA} = V _{REF} to 18 V			-1.5	-2.7	mV/V
I _{ref}	Reference terminal current	I _K = 10 mA, R1 = (see Figure 2)	= 10 kΩ, R2 = open			0.1	0.5	μΑ
				TLVH431C		0.05	0.3	
I _{ref(dev)}	I _{ref} deviation over full temperature range (see Note 5)	I _K = 10 mA, R1 = (see Note 4 and	= 10 k Ω , R2 = open	TLVH431I		0.1	0.4	μА
, ,	range (see Note 3)	(See Note 4 and	rigure z)	TLVH431Q		0.15	0.5	
I _{K(min)}	Minimum cathode current for regulation	V _{KA} = V _{REF} (see Figure 1)				60	100	μА
I _{K(off)}	Off-state cathode current	V _{REF} = 0, V _{KA}	= 18 V (see Figure 3)			0.02	0.1	μΑ
z _K A	Dynamic impedance (see Note 6)	$V_{KA} = V_{REF}, f \le I_{K} = 0.1 \text{ mA to } 80$	1 kHz, 0 mA (see Figure 1)			0.25	0.4	Ω

NOTES: 4. Full temperature ranges are: -40°C to 125°C for TLVH431Q, -40°C to 85°C for TLVH431I, and 0°C to 70°C for the TLVH431C.

 The deviation parameters V_{REF(dev)} and I_{ref(dev)} are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

is defined as:
$$|\alpha V_{REF}| \left(\frac{ppm}{^{\circ}C}\right) = \frac{\left(\frac{V_{REF}(dev)}{V_{REF}\left(T_{A} = 25^{\circ}C\right)}\right) \times 10^{6}}{\Delta T_{A}}$$

where $\Delta T_{\mbox{\scriptsize A}}$ is the rated operating free-air temperature range of the device.

 α_{VREF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF} , respectively, occurs at the lower temperature.

lower temperature.

6. The dynamic impedance is defined as: $|z_{ka}| = \frac{\Delta V_{KA}}{\Delta I_{K}}$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is defined as:

$$|z_{ka}| = \frac{\Delta V}{\Delta I} \approx |z_{ka}| \times \left(1 + \frac{R1}{R2}\right)$$

TLVH431, TLVH431A, TLVH431B TLVH432, TLVH432A, TLVH432B LOW-VOLTAGE ADJUSTABLE PRECISION SHUNT REGULATOR

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TLVH431A electrical characteristics at 25°C free-air temperature (unless otherwise noted)

	PARAMETER		CONDITIONS		TLVH431A TLVH432A			UNIT
					MIN	TYP	MAX	
			T _A = 25°C		1.228	1.24	1.252	
.,	Defenses as well-	VKA = VREF,	T _A = full range	TLVH431AC	1.221		1.259	.,
V _{REF}	Reference voltage	$I_K = 10 \text{ mA}$	(see Note 4 and	TLVH431AI	1.215		1.265	V
			Figure 1)	TLVH431AQ	1.209		1.271	
				TLVH431AC		4	12	
\/DEE(V _{REF} deviation over full temperature range (see Note 5)	$V_{KA} = V_{REF}, I_{K}$ (see Note 4 and	TLVH431AI		6	20	mV	
(***)	temperature range (see Note 3)	(See Note 4 and 1 igure 1)		TLVH431AQ		11		31
ΔV _{RE} F ΔV _K A	Ratio of V _{REF} change to cathode voltage change	I _K = 10 mA (see Figure 2)	V _{KA} = V _{REF} to 18 V			-1.5	-2.7	mV/V
I _{ref}	Reference terminal current	I _K = 10 mA, R1 = (see Figure 2)	= 10 kΩ, R2 = open			0.1	0.5	μΑ
				TLVH431AC		0.05	0.3	
I _{ref(dev)}	I _{ref} deviation over full temperature range (see Note 5)	I _K = 10 mA, R1	= 10 k Ω , R2 = open	TLVH431AI		0.1	0.4	μА
(11)	range (see Note 3)	(See Note 4 and	rigure 2)	TLVH431AQ		0.15	0.5	
I _{K(min)}	Minimum cathode current for regulation	V _{KA} = V _{REF} (see Figure 1)				60	100	μΑ
IK(off)	Off-state cathode current	V _{REF} = 0, V _{KA}	= 18 V (see Figure 3)			0.02	0.1	μΑ
z _K A	Dynamic impedance (see Note 6)	$V_{KA} = V_{REF}, f \le I_K = 0.1 \text{ mA to } 80$	1 kHz, 0 mA (see Figure 1)			0.25	0.4	Ω

NOTES: 4. Full temperature ranges are: -40°C to 125°C for TLVH431AQ, -40°C to 85°C for TLVH431AI, and 0°C to 70°C for the TLVH431AC.

 The deviation parameters V_{REF(dev)} and I_{ref(dev)} are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$|\alpha V_{REF}| \left(\frac{ppm}{^{\circ}C}\right) = \frac{\left(\frac{V_{REF}(dev)}{V_{REF} (T_A = 25^{\circ}C)}\right) \times 10^{6}}{\Delta T_A}$$

where ΔT_A is the rated operating free-air temperature range of the device.

 α_{VREF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF} , respectively, occurs at the lower temperature.

lower temperature. 6. The dynamic impedance is defined as: $|z_{ka}| = \frac{\Delta V_{KA}}{\Delta I_{K}}$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is defined as:

as: $\left|z_{ka}\right| = \frac{\Delta V}{\Delta I} \approx \left|z_{ka}\right| \times \left(1 + \frac{R1}{R2}\right)$



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TLVH431B electrical characteristics at 25°C free-air temperature (unless otherwise noted)

	PARAMETER	TEST C	CONDITIONS		TLVH431B TLVH432B			UNIT	
					MIN	TYP	MAX		
			T _A = 25°C		1.234	1.24	1.246		
.,	Defenses as well as	VKA = VREF,	T _A = full range	TLVH431BC	1.227		1.253	.,	
VREF	Reference voltage	IK = 10 mA	(see Note 4 and Figure 1)	TLVH431BI	1.224		1.259	V	
				TLVH431BQ	1.221		1.265		
				TLVH431BC		4	12		
VREF(dev)	V _{REF} deviation over full temperature range (see Note 5)	$V_{KA} = V_{REF}, I_{K}$ (see Note 4 and		TLVH431BI		6	20	mV	
(***)	temperature range (see Note 3)	(See Note 4 and	rigule i)	TLVH431BQ		11	31		
ΔV _{RE} F ΔV _K A	Ratio of V _{REF} change to cathode voltage change	I _K = 10 mA (see Figure 2)	V _{KA} = V _{REF} to 18 V			-1.5	-2.7	mV/V	
I _{ref}	Reference terminal current	I _K = 10 mA, R1 =	= 10 kΩ (see Figure 2)			0.1	0.5	μΑ	
				TLVH431BC		0.05	0.3		
I _{ref(dev)}	I _{ref} deviation over full temperature range (see Note 5)	I _K = 10 mA, R1 = (see Note 4 and	= 10 k Ω , R2 = open	TLVH431BI		0.1	0.4	μΑ	
(11)	range (see Note 3)	(See Note 4 and	rigure 2)	TLVH431BQ		0.15	0.5	1	
I _{K(min)}	Minimum cathode current for regulation	V _{KA} = V _{REF} (se	ee Figure 1)			60	100	μА	
I _{K(off)}	Off-state cathode current	$V_{REF} = 0, V_{KA}$	= 18 V (see Figure 3)			0.02	0.1	μΑ	
z _K A	Dynamic impedance (see Note 6)	$V_{KA} = V_{REF}, f \le I_{K} = 0.1 \text{ mA to } 80$	1 kHz, 0 mA (see Figure 1)			0.25	0.4	Ω	

NOTES: 4. Full temperature ranges are: -40°C to 125°C for TLVH431BQ, -40°C to 85°C for TLVH431BI, and 0°C to 70°C for the TLVH431BC.

 The deviation parameters V_{REF(dev)} and I_{ref(dev)} are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$|\alpha \text{V}_{\text{REF}}| \! \left(\! \frac{\text{ppm}}{^{\circ}\text{C}} \! \right) = \frac{ \left(\frac{\text{V}_{\text{REF}}(\text{dev})}{\text{V}_{\text{REF}} \left(\text{T}_{\text{A}} \! = \! 25^{\circ}\text{C} \right)} \right) \ \times \ 10^{6}}{\Delta \text{T}_{\text{A}}}$$

where ΔT_A is the rated operating free-air temperature range of the device.

 α_{VREF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF} , respectively, occurs at the lower temperature.

6. The dynamic impedance is defined as: $|z_{ka}| = \frac{\Delta V_{KA}}{\Delta I_{K}}$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is defined as:

 $|z_{ka}| = \frac{\Delta V}{\Delta I} \approx |z_{ka}| \times (1 + \frac{R1}{R2})$



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PARAMETER MEASUREMENT INFORMATION

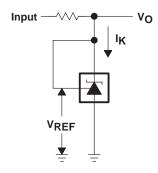


Figure 1. Test Circuit for $V_{KA} = V_{REF}$, $V_O = V_{KA} = V_{REF}$

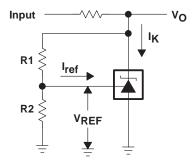


Figure 2. Test Circuit for $V_{KA} > V_{REF}$ $V_O = V_{KA} = V_{REF} \times (1 + R1/R2) + I_{ref} \times R1$

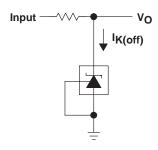
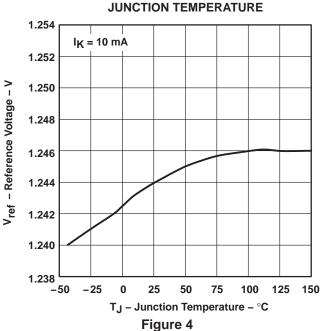


Figure 3. Test Circuit for I_{K(off)}

REFERENCE VOLTAGE VS



REFERENCE INPUT CURRENT

vs JUNCTION TEMPERATURE

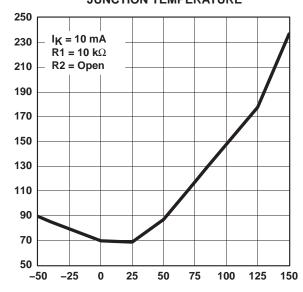
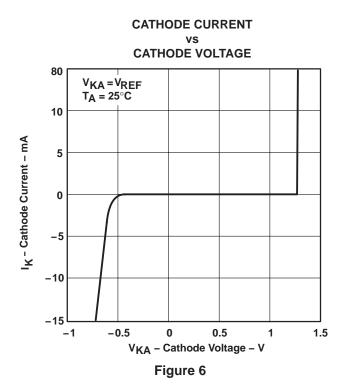


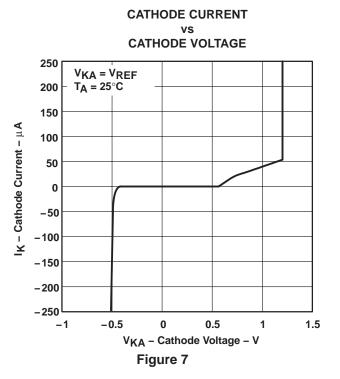
Figure 5



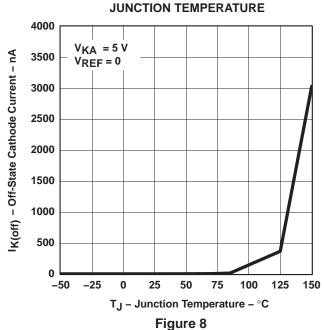
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PARAMETER MEASUREMENT INFORMATION[†]





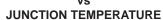
OFF-STATE CATHODE CURRENT vs

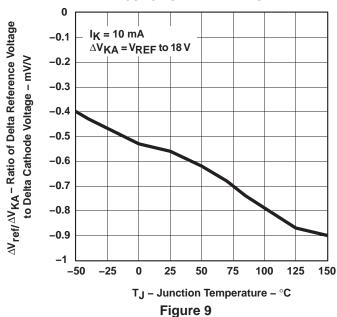




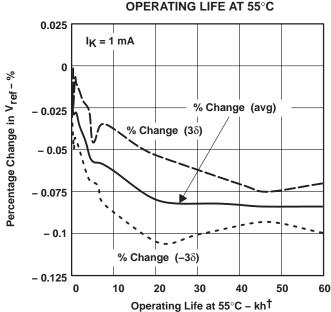
PARAMETER MEASUREMENT INFORMATION[†]

RATIO OF DELTA REFERENCE VOLTAGE TO DELTA CATHODE VOLTAGE





PERCENTAGE CHANGE IN V_{REF} vs



[†] Extrapolated from life-test data taken at 125°C; the activation energy assumed is 0.7 eV.

Figure 10

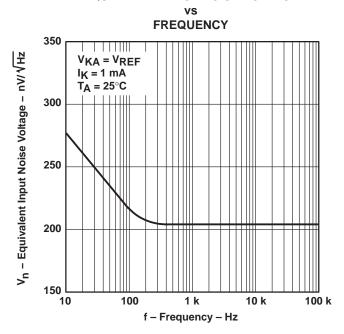
[†] Operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.

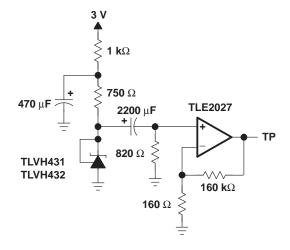


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PARAMETER MEASUREMENT INFORMATION

EQUIVALENT INPUT NOISE VOLTAGE

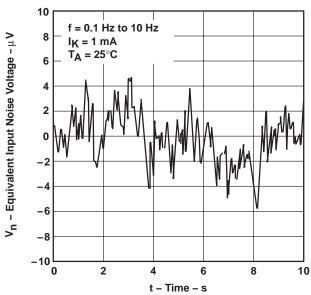


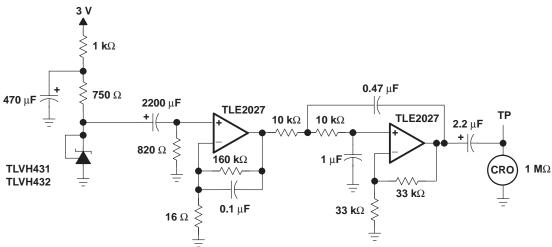


TEST CIRCUIT FOR EQUIVALENT INPUT NOISE VOLTAGE

Figure 11

EQUIVALENT INPUT NOISE VOLTAGE OVER A 10-SECOND PERIOD



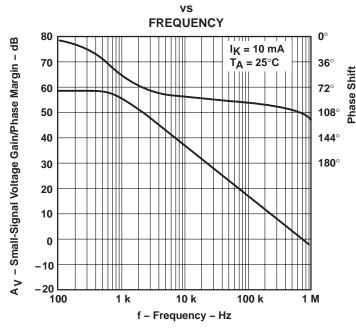


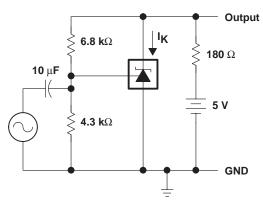
TEST CIRCUIT FOR 0.1-Hz TO 10-Hz EQUIVALENT NOISE VOLTAGE

Figure 12



SMALL-SIGNAL VOLTAGE GAIN /PHASE MARGIN

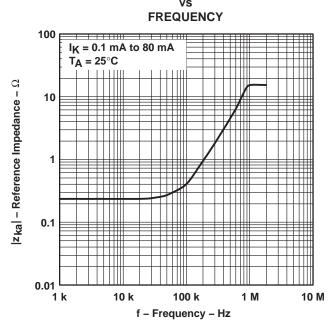


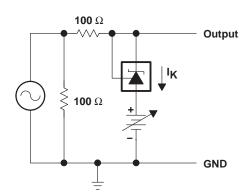


TEST CIRCUIT FOR VOLTAGE GAIN AND PHASE MARGIN

Figure 13

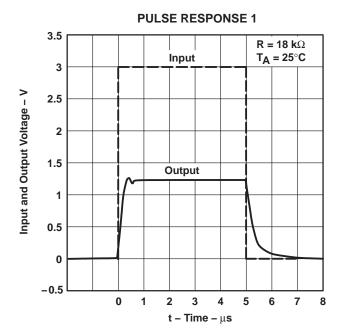
REFERENCE IMPEDANCE

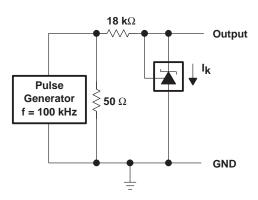




TEST CIRCUIT FOR REFERENCE IMPEDANCE

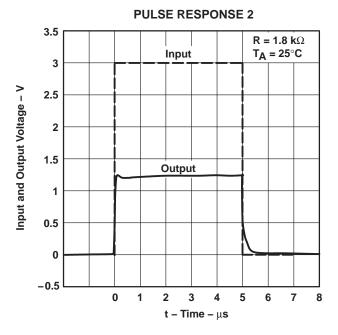
Figure 14

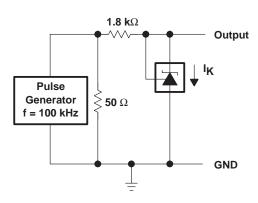




TEST CIRCUIT FOR PULSE RESPONSE 1

Figure 15





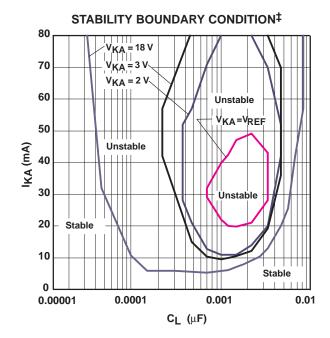
TEST CIRCUIT FOR PULSE RESPONSE 2

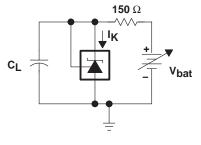
Figure 16

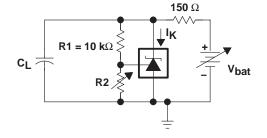


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PARAMETER MEASUREMENT INFORMATION[†]







TEST CIRCUIT FOR V_{KA} = V_{REF}

TEST CIRCUIT FOR V_{KA} = 2 V, 3 V

† Operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.

Figure 17

[‡] The areas enclosed by the curves represent conditions that may cause the device to oscillate. For V_{KA} = 2-V, 3-V, and 18-V curves, R2 and V_{bat} were adjusted to establish the initial V_{KA} and I_{K} conditions with C_{L} = 0. V_{bat} and C_{L} then were adjusted to determine the ranges of stability.

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APPLICATION INFORMATION

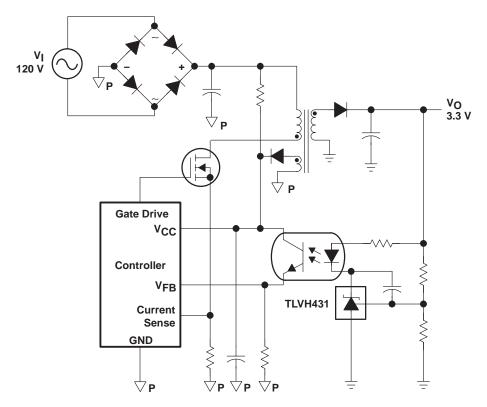


Figure 18. Flyback With Isolation Using TLVH431 and TLVH432 as Voltage Reference and Error Amplifier

Figure 18 shows the TLVH431 used in a 3.3-V isolated flyback supply. Output voltage V_O can be as low as reference voltage V_{REF} (1.24 V). The output of the regulator plus the forward voltage drop of the optocoupler LED (1.24 + 1.4 = 2.64 V) determine the minimum voltage that can be regulated in an isolated supply configuration. Regulated voltage as low as 2.7 Vdc is possible in the topology shown in Figure 18.





PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	n MSL Peak Temp (3)
TLVH431ACDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431ACDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431ACDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH431ACDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431ACDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431ACDCKT	PREVIEW	SC70	DCK	6	250	TBD	Call TI	Call TI
TLVH431ACLP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431ACLPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431ACPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH431AIDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431AIDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431AIDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH431AIDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431AIDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431AILP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431AILPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431AIPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH431AQDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431AQDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431AQDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH431AQDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431AQDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431AQDCKT	PREVIEW	SC70	DCK	6	250	TBD	Call TI	Call TI
TLVH431AQLP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431AQLPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431AQPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH431BCDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCDBZR	ACTIVE	SOT-23	DBZ	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCDBZT	ACTIVE	SOT-23	DBZ	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCDCKR	ACTIVE	SC70	DCK	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCDCKT	ACTIVE	SC70	DCK	6	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BCLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431BCLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431BIDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BIDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM





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Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³
TLVH431BIDBZR	PREVIEW	SOT-23	DBZ	3		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BIDBZT	ACTIVE	SOT-23	DBZ	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BIDCKR	ACTIVE	SC70	DCK	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BIDCKT	ACTIVE	SC70	DCK	6	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BILP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431BILPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431BQDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQDBZR	ACTIVE	SOT-23	DBZ	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQDBZT	ACTIVE	SOT-23	DBZ	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQDCKR	ACTIVE	SC70	DCK	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQDCKT	ACTIVE	SC70	DCK	6	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLVH431BQLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431BQLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
TLVH431CDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431CDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431CDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH431CDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431CDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431CDCKT	PREVIEW	SC70	DCK	6	250	TBD	Call TI	Call TI
TLVH431CLP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431CLPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431CPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH431IBQDBZR	PREVIEW	SOT-23	DBZ	3		TBD	Call TI	Call TI
TLVH431IDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431IDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431IDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH431IDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431IDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431IDCKT	PREVIEW	SC70	DCK	6	250	TBD	Call TI	Call TI
TLVH431ILP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431ILPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431IPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH431QDBVR	PREVIEW	SOT-23	DBV	5	3000	TBD	Call TI	Call TI
TLVH431QDBVT	PREVIEW	SOT-23	DBV	5	250	TBD	Call TI	Call TI
TLVH431QDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI





www.ti.com 30-Mar-2005

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TLVH431QDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH431QDCKR	PREVIEW	SC70	DCK	6	3000	TBD	Call TI	Call TI
TLVH431QDCKT	PREVIEW	SC70	DCK	6	250	TBD	Call TI	Call TI
TLVH431QLP	PREVIEW	TO-92	LP	3	1000	TBD	Call TI	Call TI
TLVH431QLPR	PREVIEW	TO-92	LP	3	2000	TBD	Call TI	Call TI
TLVH431QPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432ACDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432ACDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432ACPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432AIDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432AIDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432AIPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432AQDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432AQDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432AQPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432BCDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432BCDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432BCPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432BIDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432BIDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432BIPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432BQDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432BQDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432BQPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432CDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432CDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432CPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432IDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432IDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432IPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI
TLVH432QDBZR	PREVIEW	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI
TLVH432QDBZT	PREVIEW	SOT-23	DBZ	3	250	TBD	Call TI	Call TI
TLVH432QPK	PREVIEW	SOT-89	PK	3	1000	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.



PACKAGE OPTION ADDENDUM

30-Mar-2005

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

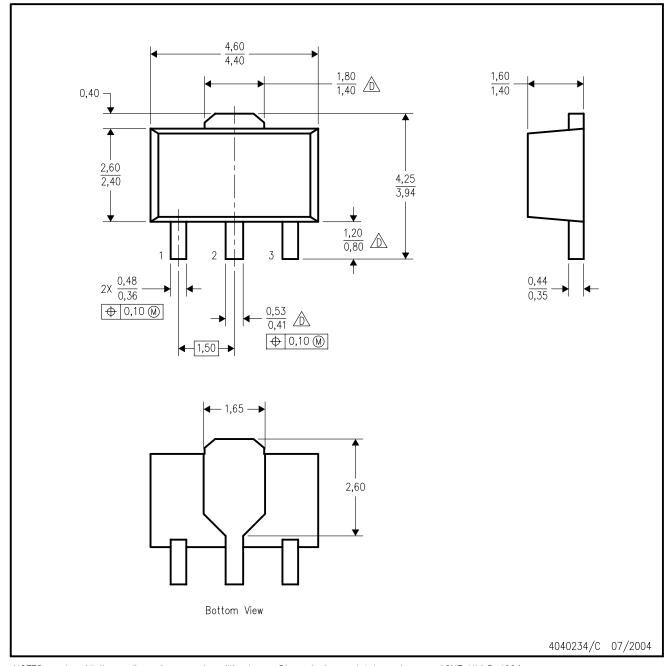
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PK (R-PSSO-F3)

PLASTIC SINGLE-IN-LINE PACKAGE



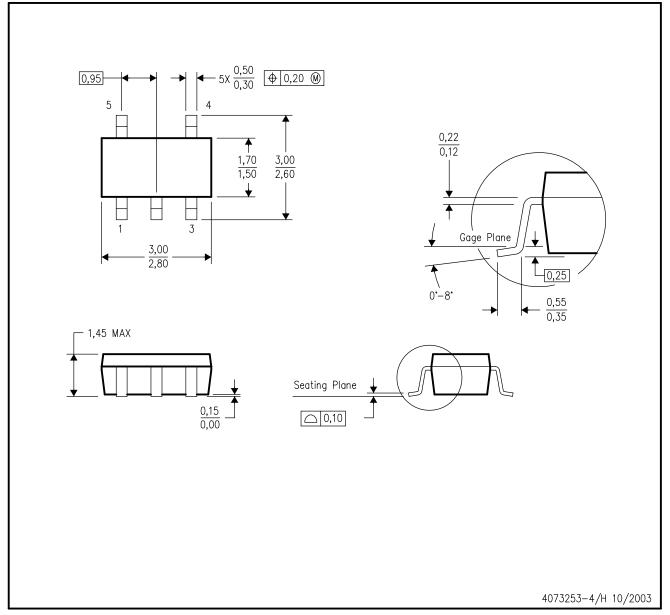
NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5—1994.

- B. This drawing is subject to change without notice.
- C. The center lead is in electrical contact with the tab.
- Falls within JEDEC TO-243 variation AA, except minimum lead length, pin 2 minimum lead width, and minimum tab width.



DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



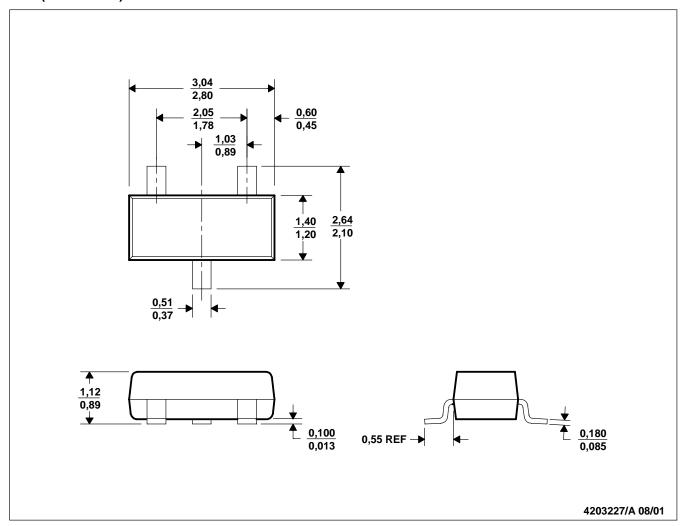
NOTES:

- All linear dimensions are in millimeters.
- This drawing is subject to change without notice.
- C. Body dimensions do not include mold fla D. Falls within JEDEC MO—178 Variation AA. Body dimensions do not include mold flash or protrusion.



DBZ (R-PDSO-G3)

PLASTIC SMALL-OUTLINE

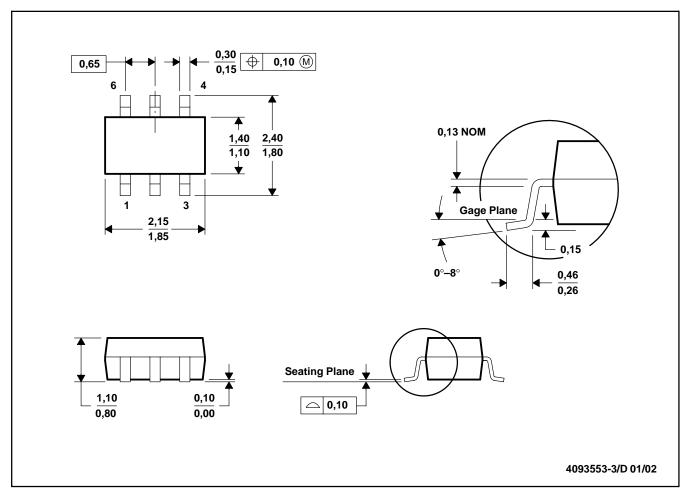


NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Dimensions are inclusive of plating.
- D. Dimensions are exclusive of mold flash and metal burr.

DCK (R-PDSO-G6)

PLASTIC SMALL-OUTLINE PACKAGE

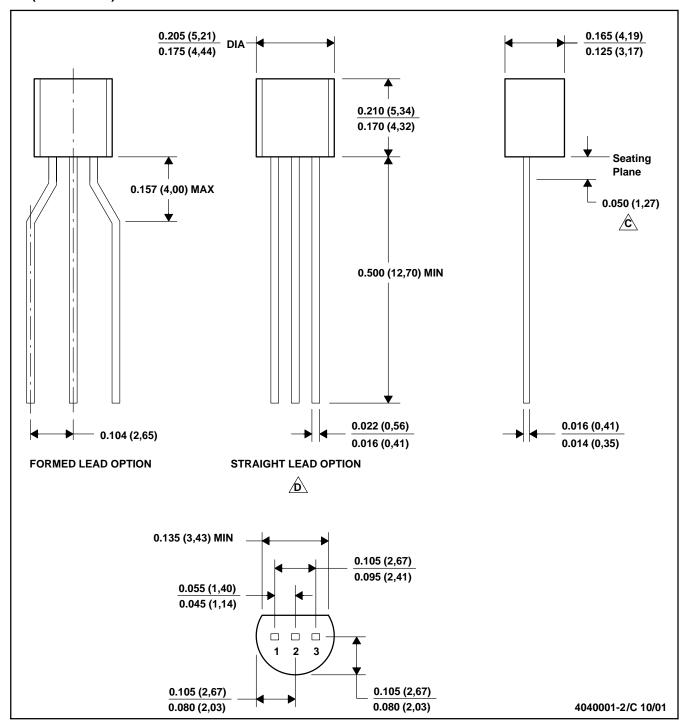


NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. Falls within JEDEC MO-203

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice. $\hfill \hfill \$

C.\ Lead dimensions are not controlled within this area

D. FAlls within JEDEC TO -226 Variation AA (TO-226 replaces TO-92)

E. Shipping Method:

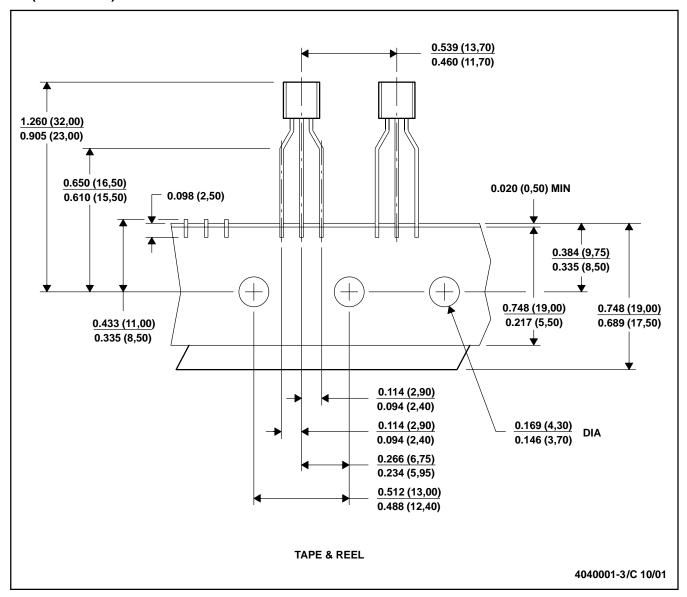
Straight lead option available in bulk pack only.

Formed lead option available in tape & reel or ammo pack.



LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Tape and Reel information for the Format Lead Option package.

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