TOSHIBA Multi-Chip Device Silicon P-Channel MOS Type (U-MOS II) + N-Channel MOS Type (Planer)

SSM6E01TU

Load Switch Applications

Unit: mm

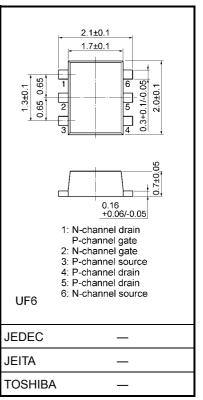
- P-channel MOSFET and N-channel MOSFET incorporated into one package.
- Low power dissipation due to P-channel MOSFET that features low RDS (ON) and low-voltage operation

Q1 Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source voltage		V_{DS}	-12	V
Gate-Source voltage		V_{GSS}	±12	V
Drain current	DC	ID	-1.0	Α
	Pulse	I _{DP} (Note 2)	-2.0	A

Q2 Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source voltage		V_{DS}	20	V
Gate-Source voltage		V _{GSS}	10	V
Drain current	DC	I _D	0.05	Α
	Pulse	I _{DP} (Note 2)	0.2	A



Weight: 7.0 mg (typ.)

Maximum Ratings (Q1, Q2 common) (Ta = 25°C)

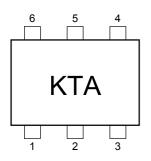
Characteristics	Symbol	Rating	Unit
Drain power dissipation	P _D (Note 1)	0.5	W
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C

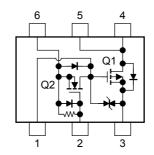
Note 1: Mounted on an FR4 board (25.4 mm \times 25.4 mm \times 1.6 t, Cu pad: 645 mm²)

Note 2: Pulse width limited by maximum channel temperature.

Marking

Equivalent Circuit (top view)





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Handling Precaution

This product has a MOS structure and is sensitive to electrostatic discharge. When handling individual devices (that have not yet been mounted on a PCB), ensure that the environment is protected against static electricity. Operators should wear anti-static clothing, containers and other objects which may come into direct contact with devices should be made of anti-static materials.

Thermal resistance R_{th} (j-a) and drain power dissipation P_D vary depending on board material, board area, board thickness and pad area. When using this device, please take heat dissipation into consideration.

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Q1 Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage (diode)	V _{DSF}	$I_{DR} = 1.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V
Gate leakage current	I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$	_	_	±1	μА
Drain-Source breakdown voltage	V _{(BR)DSS}	$I_D = -1 \text{ mA}, V_{GS} = 0$	-12	_	_	V
Drain cut-off current	I _{DSS}	$V_{DS} = -12 \text{ V}, V_{GS} = 0$	_	_	-1	μА
Gate threshold voltage	V _{th}	$V_{DS} = -3 \text{ V}, I_D = -0.1 \text{ mA}$	-0.4	_	-1.1	V
Forward transfer admittance	Y _{fs}	$V_{DS} = -3 \text{ V}, I_D = -0.5 \text{ A}$ (Note 3)	1.3	2.5	_	S
Drain-Source ON resistance	Pro (OV)	$I_D = -0.5 \text{ A}, V_{GS} = -4 \text{ V}$ (Note 3)	_	125	160	mΩ
	R _{DS} (ON)	$I_D = -0.5 \text{ A}, V_{GS} = -2.5 \text{ V}$ (Note 3)	_	180	240	1115.2
Input capacitance	C _{iss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		310	_	pF

Note 3: Pulse test

Q2 Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V _{GS} = 10 V, V _{DS} = 0	_	_	15	μА
Drain-Source breakdown voltage	V (BR) DSS	$I_D = 0.1 \text{ mA}, V_{GS} = 0$	20	_	_	V
Drain cut-off current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0	_	_	1	μА
Gate threshold voltage	V_{th}	$V_{DS} = 3 \text{ V}, I_D = 0.1 \text{ mA}$	0.7	_	1.3	V
Forward transfer admittance	Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 10 \text{ mA}$ (Note 3)	25	50	_	mS
Drain-Source ON resistance	R _{DS (ON)}	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$ (Note 3)	_	4	10	Ω
Input capacitance	C _{iss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	11	_	pF
Gate-Source resistance	R _{GS}	V _{GS} = 0~10 V	0.7	1.0	1.3	ΜΩ

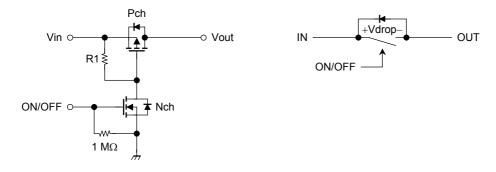
Note 3: Pulse test

Precaution

 V_{th} can be expressed as voltage between gate and source when low operating current value is $I_D = \pm 100~\mu A$ for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} . (Relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on))

Please take this into consideration for using the device. 2.5~V or higher is recommended for V_{GS} voltage to turn on the N-channel MOSFET of this product.

Load Switch Application



Load Switch Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Input voltage	V _{in}	2.5~12	V
ON/OFF voltage	V _{on/off}	2.5~10	V
Load current (DC)	ΙL	1	Α
Load current (pulse)	I _{LP} (Note 4)	2	Α
Channel temperature	T _{ch}	150	°C

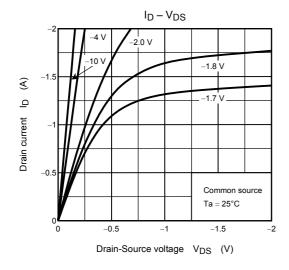
Note 4: Pulse width limited by maximum channel temperature.

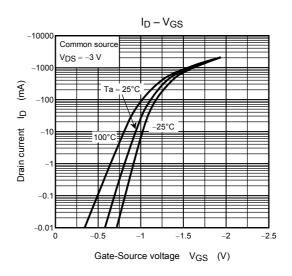
Load Switch Electrical Characteristics (Ta = 25°C)

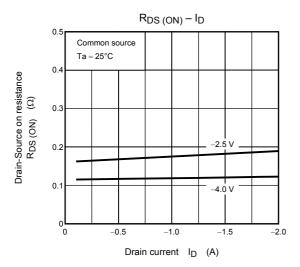
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Leakage current	I _{FL}	V _{in} = 8 V, V _{ON/OFF} = 0	_	_	1	μΑ
P-channel drop voltage	V _{DROP (1)}	$V_{in} = 3.0 \text{ V}, V_{ON/OFF} = 2.5 \text{ V}, I_L = 0.5 \text{ A}$	_	0.09	0.12	· V
	V _{DROP (2)}	$V_{in} = 5.0 \text{ V}, V_{ON/OFF} = 2.5 \text{ V}, I_L = 1.0 \text{ A}$	_	0.13	0.16	
N-channel drive voltage	V _{on/off}	$V_{DS} = 3 \text{ V}, I_D = 0.1 \text{ mA}$	0.7	_	1.3	V

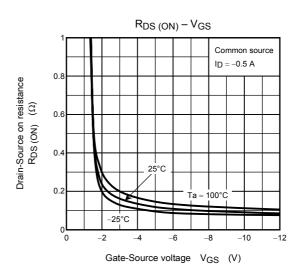
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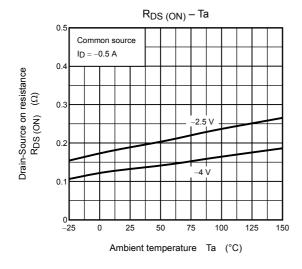
Q1 (Pch MOSFET)

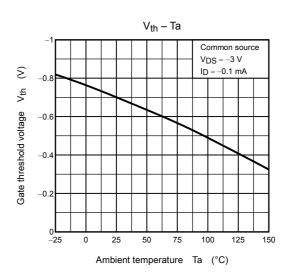




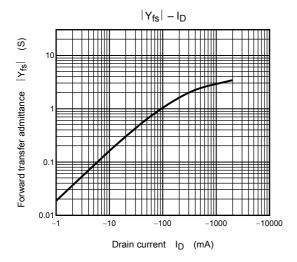


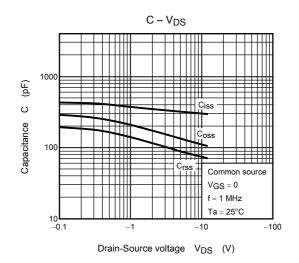


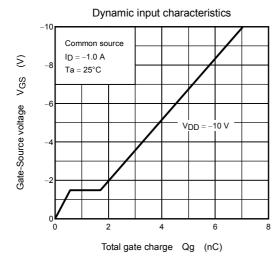


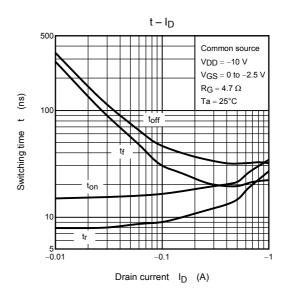


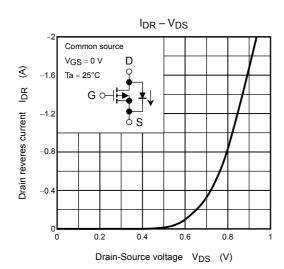
Q1 (Pch MOSFET)



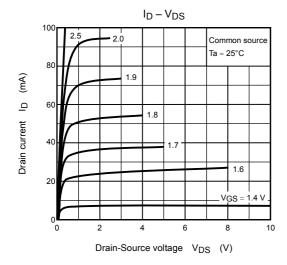


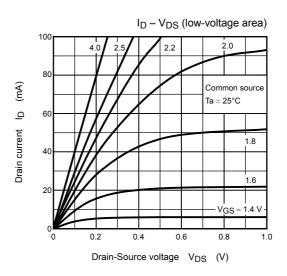


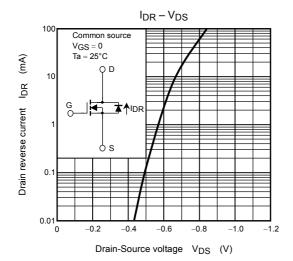


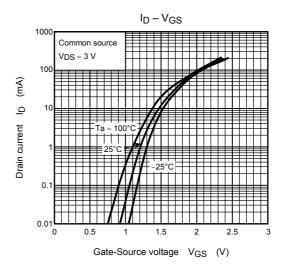


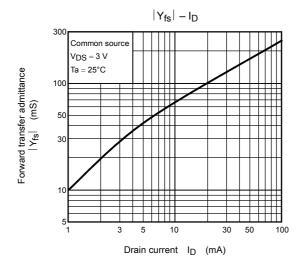
Q2 (Nch MOSFET)

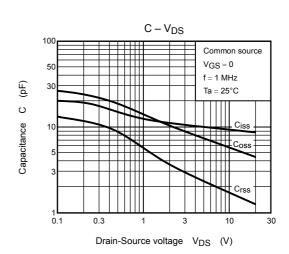




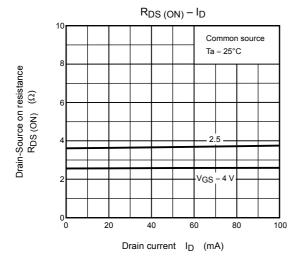


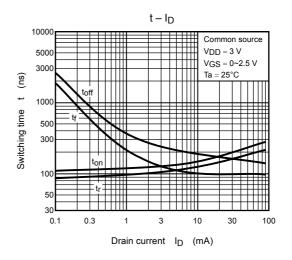


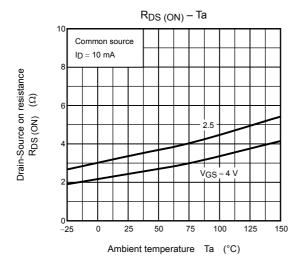




Q2 (Nch MOSFET)







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