TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC83220-0029

## TC83220-0029 CMOS Single-Chip LSI for FL (fluorescent) Calculator

The Toshiba printing/display calculator circuit TC83220-0029 is 10/12-digit calculator on single-chip CMOS LSI.

TC83220-0029 can drive the printing machine (M400A/M401A/M400E/M80*; EPSON) with magnet driver circuit, and can drive the fluorescent display tube with DC-DC converter.

It contains a 4 K-word ROM, a $256 \times 4$-bit RAM.
Note 1: Print font number: M400A 001-300
$\begin{array}{ll}\text { M401A } & 001-330 \\ \text { M400E } & 001-310\end{array}$


Weight: 4.12 g (typ.)

## Features

## Operational Features

- Print: $11 / 13$ digits of data.
(including decimal point 2 digit of operational symbol.)
3 digits of commas.
- Display: 10/12 digits of data. (including punctuation in each digit.)

1 digit of floating minus sign, memory load, error symbol.
3 digits of commas.

- Decimal output: Decimal setting lock key controls output format.

Fixed decimal setting ("0", " 1 ", " 2 ", " 3 ", " 4 ", " 6 "), full floating decimal, and ADD mode.

- Key input buffer: 8 stages
- Function: 4 basic arithmetic functions (+,,$- \times, \div$ ).

Repeat addition and subtraction.
Automatic constants in multiplication, division, percent calculation, calculations.
Automatic percent add-on and percent discount calculation.
Memory calculation.
Automatic accumulating calculation.
Gross margin profit calculation.
Delta percent calculation.
Tax calculation.
Grand total calculation.
Currency conversion calculation.
Two-key rollover.

- Item counter: $0 \sim 999$ count up or -999~0~999 count up/down by depressing of $\boxed{+}$ or $\stackrel{+}{=}, \square$ or $\square \overline{=}$ key.
- Punctuation: Commas or space for thousands on display
- Kinds of touch key:

- Kinds of lock key :"NP" printing mode selectable switch.
" $\Sigma$ " summation mode selectable switch.
" $5 / 4$ " "CUT" "UP" rounding switch.
Fixed point mode selectable switch.
" 0 ", " 1 ", " 2 ", " $3 ", ~ " 4 ", ~ " ~ 6 ", ~ " F ", ~ " A D D+", ~ " A D D X " . ~$
"IC + " "IC $\pm$ " item counter mode selectable switch.
"GT" grand total memory selectable switch.
- Duty of display: Duty = $1 / 16.5$
- Leading zero suppression
- Trailing zero suppression
- Tax calculation: +TAX key is calculation for included tax.
-TAX key is calculation for excluded tax.
SET key is store the tax rate to memory.
Depression of +TAX or -TAX after clear function, recall tax rate and into the setting mode.
Depression of SET stores number of display to memory at the setting mode.
Depression of +TAX following data key performs the calculating included tax.
Depression of -TAX following data key performs the calculating excluded tax.
- Currency conversion

Calculation: E to H key is calculation for home currency.
H to E key is calculation for Euro currency.
SET key is store the currency rate for Euro to memory (ex. 1 Euro = 1.23456).
Depression of E to H or H to E after clear function.
Recall currency rate and into the setting mode.
Depression of SET stores number of display to memory at the setting mode.
Depression of E to H following data key performs the conversion Euro to Home currency.
Depression of H to E following data key performs the conversion Home to Euro currency.

## Electrical Features

- P-MOS output buffer with pull down resistor for direct driving of fluorescent display tube.
- Dual in line package.


## Protection

(1) In the overflow condition, all key except "C", "C/CE", "CE", "Feed", " $\rightarrow$ " key are inoperative.
(2) Key bouncing protection (at 4 MHz clock)

Key read in: 15 ms
Key off: 40 ms

## Function Select

(1) " $10 / 12$ " selectable with auto power off mode

ON. $\qquad$ 10-digit calculated
OFF 12-digit calculated

Pin Assignment (top view)

TOSHIBA
System Diagram


## Key Connection



## Lock Key



Touch Key
TOSHIBA

Operation Example


Note 6: $\qquad$ Paper feed

PRINT COLOR R: Red
.......No mark: Black


Note 6:
Paper feed
PRINT COLOR R: Red
No mark: Black


Note 6: <PF> $\qquad$ Paper feed
PRINT COLOR
R: Red
.......No mark: Black


Note 6:
Paper feed
PRINT COLOR........R: Red

[^0]

Note 6: <PF>
Paper feed
PRINT COLOR R: Red
.......No mark: Black


Note 6: <PF>
Paper feed
PRINT COLOR
R: Red
.......No mark: Black

Maximum Ratings ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}$ )

| Characteristics | Symbol | Rating | Unit |
| :---: | :---: | :---: | :---: |
| Supply voltage 1 | $V_{\text {DD }}$ | -0.5~7 | V |
| Supply voltage 2 | VKK | $-40 \sim+0.5$ | V |
| Input voltage | $\mathrm{V}_{\mathrm{IN}}$ | $-35 \sim V_{\text {DD }}+0.5$ | V |
| Output voltage | V OUT | $-35 \sim V_{\text {DD }}+0.5$ | V |
| Output current | IOUT | -10 | mA |
| Power dissipation ( $\left.\mathrm{T}_{\mathrm{opr}}=70^{\circ} \mathrm{C}\right)$ | PD | 600 | mW |
| Soldering temperature, time | $\mathrm{T}_{\text {sld }}$ | 260 (10 s) | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | -55~125 | ${ }^{\circ} \mathrm{C}$ |
| Operating temperature | $\mathrm{T}_{\text {opr }}$ | 0~40 | ${ }^{\circ} \mathrm{C}$ |

Recommended Operating Conditions ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}$ )

| Characteristics |  | Symbol | Test Circuit | Test Condition | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating temperature |  | $\mathrm{T}_{\text {opr }}$ | - | - | 0 | 40 | ${ }^{\circ} \mathrm{C}$ |
| Supply voltage |  | $V_{\text {DD }}$ | - | - | 4.5 | 6 | V |
| Supply voltage (FL) |  | $V_{\text {KK }}$ | - | - | -30 | -15 | V |
| Supply voltage (hold) |  | $\mathrm{V}_{\text {DDH }}$ | - | - | 2 | 6 | V |
| Input high voltage (except schmitt circuit input) |  | $\mathrm{V}_{\mathrm{H} 1}$ | - | $\mathrm{V}_{\mathrm{DD}} \geqq 4.5 \mathrm{~V}$ | $\begin{array}{\|c} \mathrm{V}_{\mathrm{DD}} \times \\ 0.7 \end{array}$ | $V_{D D}$ | V |
| Input high voltage (schmitt circuit input) |  | $\mathrm{V}_{\mathrm{H} 2}$ | - |  | $\begin{array}{\|c} \mathrm{V}_{\mathrm{DD}} \times \\ 0.75 \end{array}$ | VDD | V |
| Input high voltage |  | $\mathrm{V}_{\mathrm{IH} 3}$ | - | $\mathrm{V}_{\mathrm{DD}}<4.5 \mathrm{~V}$ | $\begin{gathered} V_{D D} \times \\ 0.9 \end{gathered}$ | $V_{\text {D }}$ | V |
| Input low voltage (except schmitt circuit input) |  | $\mathrm{V}_{\text {IL1 }}$ | - | $V_{D D} \geqq 4.5 \mathrm{~V}$ | VKK | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.3 \end{gathered}$ | V |
| Input low voltage (schmitt circuit input) |  | $\mathrm{V}_{\text {IL2 }}$ | - |  | VKK | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.25 \end{gathered}$ | V |
| Input low voltage |  | VIL3 | - | $\mathrm{V}_{\mathrm{DD}}<4.5 \mathrm{~V}$ | VKK | $\begin{array}{\|c} V_{D D} \times \\ 0.1 \end{array}$ | V |
| Output voltage (source open drain) |  | Vout | - | - | $\begin{array}{\|c} \mathrm{V}_{\mathrm{DD}}- \\ 35 \end{array}$ | $V_{D D}$ | V |
| Clock high pulse width | (Note 7) | TWCH | - | $\mathrm{V}_{\mathbb{I N}}=\mathrm{V}_{1 \mathrm{H}}$ | 80 | - | ns |
| Clock low pulse width | (Note 7) | TWCL | - | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {IL }}$ | 80 | - | ns |

Note 7: In case of the external clock operation.

## Electrical Characteristics

DC Characteristics ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DD}} \pm 10 \%, \mathrm{~T}_{\mathrm{opr}}=0 \sim 40^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Hysteresis voltage (schmitt circuit input) | $\mathrm{V}_{\mathrm{HS}}$ | - | - | - | 0.7 | - | V |
| Input current ( $\overline{\mathrm{RESET}}, \overline{\mathrm{HOLD}}, \overline{\mathrm{TEST}})$ | IN | - | $\mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=5.5 / 0 \mathrm{~V}$ | - | - | $\pm 50$ | $\mu \mathrm{A}$ |
| Output leak current <br> (source open drain) | ILO | - | $\mathrm{V}_{\text {DD }}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=-32 \mathrm{~V}$ | - | - | -10 | $\mu \mathrm{A}$ |
| Output high voltage (P1~P2, R4~R9) | $\mathrm{V}_{\mathrm{OH}}$ | - | $\mathrm{V}_{\mathrm{DD}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{OH}}=-6 \mathrm{~mA}$ | 2.4 | - | - | V |
| Input pull down resistor ( $\mathrm{K}_{0}, \mathrm{R}_{7} \sim \mathrm{R}_{9}$ ) | RIN | - | $V_{\text {DD }}=55 \mathrm{~V}, \mathrm{VK}^{\text {a }}=30 \mathrm{~V}$ | - | 100 | - | k $\Omega$ |
| Pull down resistor (source open drain) | RKK | - | . 5 , V | 50 | 80 | 200 | k $\Omega$ |
| Operating supply current | IDD 0 | - | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}\left(\mathrm{~V}_{\mathrm{DDH}}\right) 5.5 \mathrm{~V}, \mathrm{f}_{\mathrm{C}}=4 \mathrm{MHz} \\ & \mathrm{~V}_{\mathrm{IN}}=5.3 / 0.2 \mathrm{~V} \end{aligned}$ | - | 3 | 6 | mA |
| Supply current (after clear) | $\mathrm{I}_{\text {KK }} 1$ | - | $=-30 \mathrm{~V}, \mathrm{f}_{\mathrm{c}}=4 \mathrm{MHz}$ | - | 0.6 | 0.9 | mA |
| Supply current (shown full digits) | IKk 2 | - |  | - | 3.5 | 6 | mA |
| Holding supply current | IDD H | - | $\mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}$ | - | 0.5 | 10 | $\mu \mathrm{A}$ |

Oscillation Characteristics ( $\mathrm{T}_{\mathrm{opr}}=0 \sim 40^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=4.5 \sim 6.0 \mathrm{~V}$ )

| Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  |  |  |  |

## Package Dimensions

```
SDIP42-P-600-1.78
```

Unit: mm


Weight: 4.12 g (typ.)

## RESTRICTIONS ON PRODUCT USE

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.


[^0]:    No mark: Black

