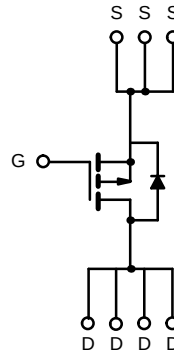
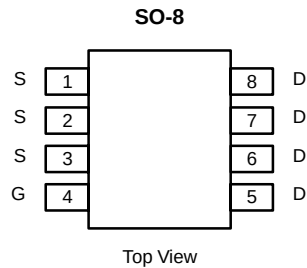




P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-40	0.0155 @ $V_{GS} = -10$ V	-10.5
	0.0225 @ $V_{GS} = -4.5$ V	-8.7

TrenchFET[®]
Power MOSFETs

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V_{DS}	−40		V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current ($T_J = 150^{\circ}\text{C}$) ^a	$T_A = 25^{\circ}\text{C}$	I_D	−10.5	−8.7	A
	$T_A = 70^{\circ}\text{C}$		−8.3	−5.9	
Pulsed Drain Current		I_{DM}	−50		
continuous Source Current (Diode Conduction) ^a		I_S	−2.7	−1.36	
Maximum Power Dissipation ^a	$T_A = 25^{\circ}\text{C}$	P_D	3.0	1.5	W
	$T_A = 70^{\circ}\text{C}$		1.9	0.95	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	−55 to 150		$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	33	42	$^\circ\text{C/W}$
	Steady State		70	84	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	16	21	

Notes

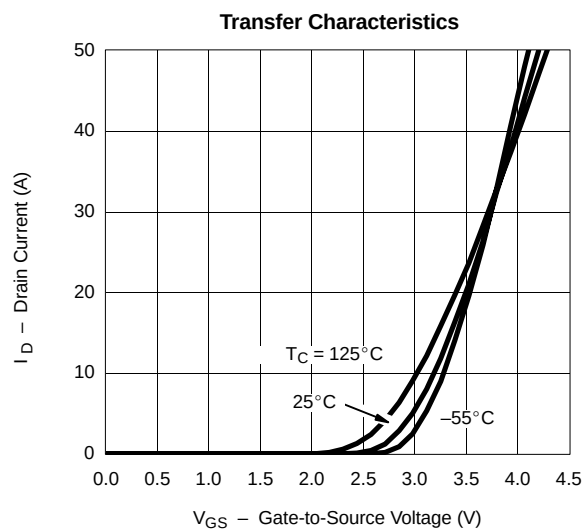
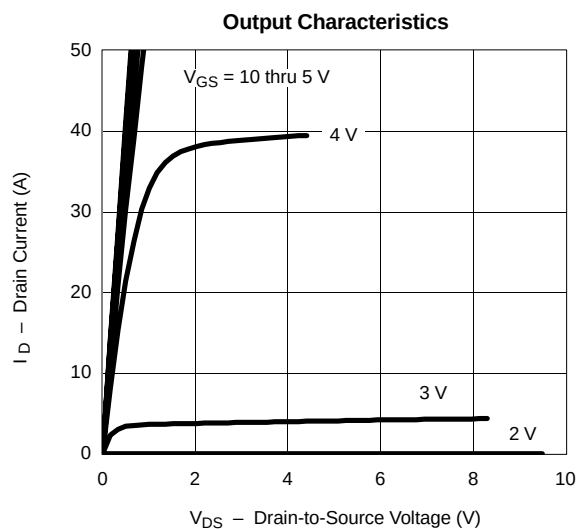
a. Surface Mounted on 1" x 1" FR4 Board.

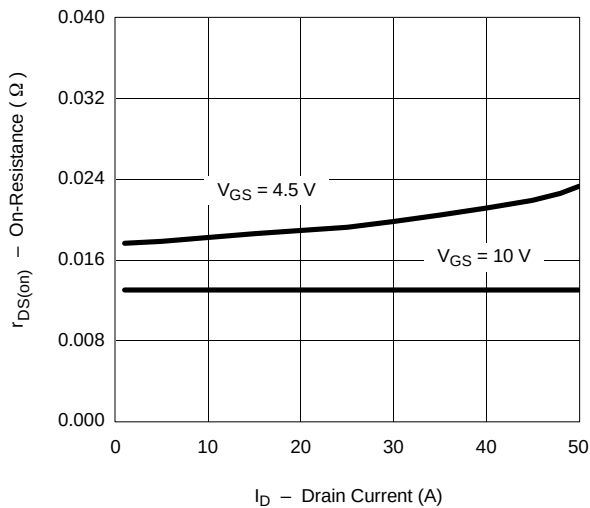
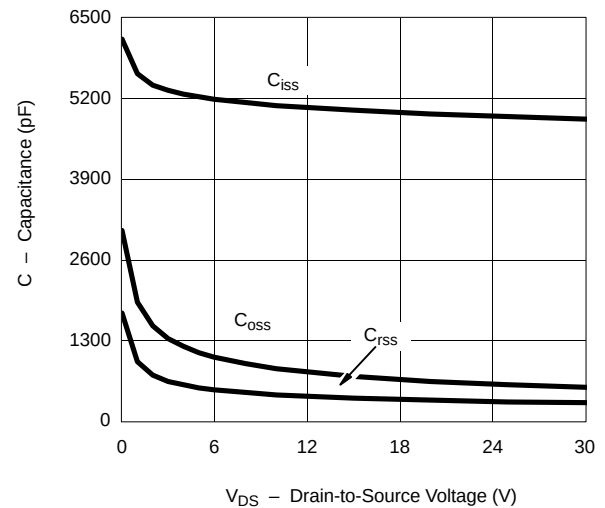
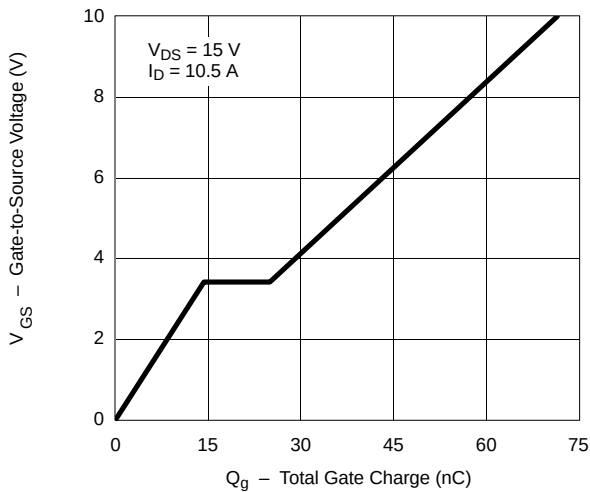
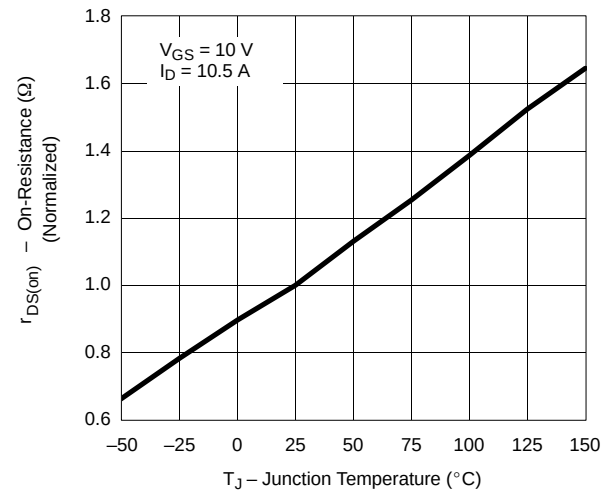
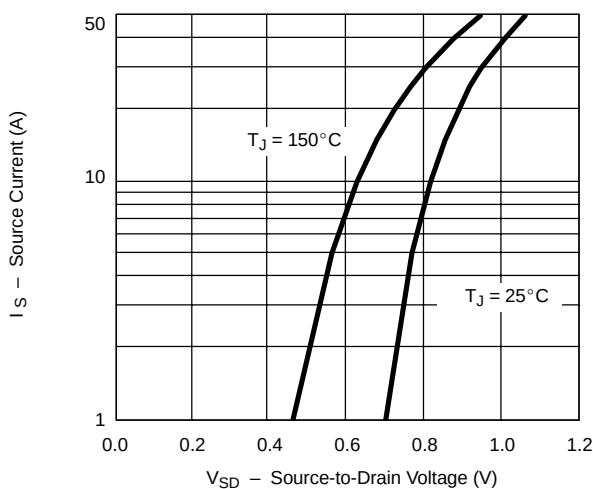
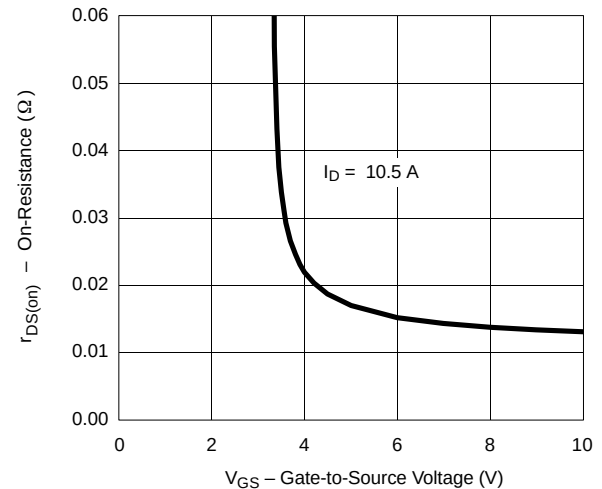
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250\ \mu\text{A}$	-1.0			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 20\ \text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -32\ \text{V}$, $V_{GS} = 0\ \text{V}$			-1	μA
		$V_{DS} = -32\ \text{V}$, $V_{GS} = 0\ \text{V}$, $T_J = 70^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\ \text{V}$, $V_{GS} = -10\ \text{V}$	-30			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = -10\ \text{V}$, $I_D = -10.5\ \text{A}$		0.013	0.0155	Ω
		$V_{GS} = -4.5\ \text{V}$, $I_D = -8.7\ \text{A}$		0.0185	0.0225	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\ \text{V}$, $I_D = -10.5\ \text{A}$		26		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -2.7\ \text{A}$, $V_{GS} = 0\ \text{V}$		-0.74	-1.1	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -15\ \text{V}$, $V_{GS} = -5\ \text{V}$, $I_D = -10.5\ \text{A}$		37.5	50	nC
Gate-Source Charge	Q_{gs}			14.3		
Gate-Drain Charge	Q_{gd}			10.7		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\ \text{V}$, $R_L = 15\ \Omega$ $I_D \cong -1\ \text{A}$, $V_{GEN} = -10\ \text{V}$, $R_G = 6\ \Omega$		17	30	ns
Rise Time	t_r			18	30	
Turn-Off Delay Time	$t_{d(off)}$			122	190	
Fall Time	t_f			55	85	
Gate Resistance	R_g			3.8		Ω
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -2.1\ \text{A}$, $di/dt = 100\ \text{A}/\mu\text{s}$		45		ns

Notes

- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature****Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage**

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)