

PRELIMINARY
 Notice: This is not a final specification.
 Some parametric limits are subject to change.

MITSUBISHI Pch POWER MOSFET

FX30ASJ-03

HIGH-SPEED SWITCHING USE

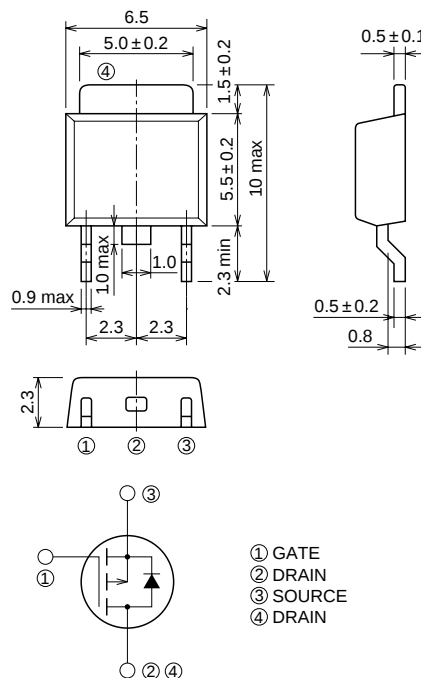
FX30ASJ-03



- 4V DRIVE
- V_{DS} -30V
- $r_{DS(ON)}$ (MAX) 61m Ω
- I_D -30A
- Integrated Fast Recovery Diode (TYP.) 50ns

OUTLINE DRAWING

Dimensions in mm



MP-3

APPLICATION

Motor control, Lamp control, Solenoid control
 DC-DC converter, etc.

MAXIMUM RATINGS (Tc = 25°C)

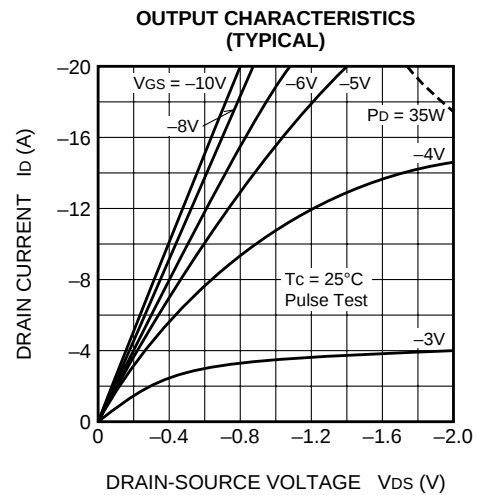
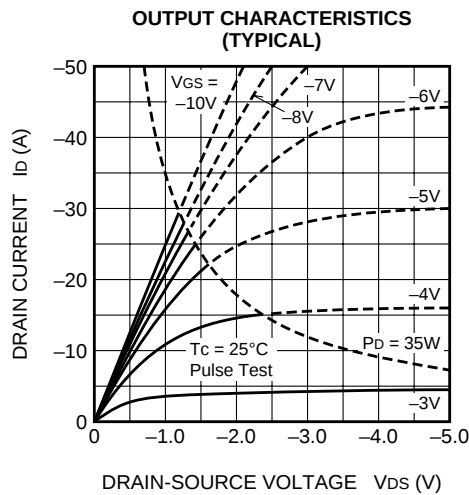
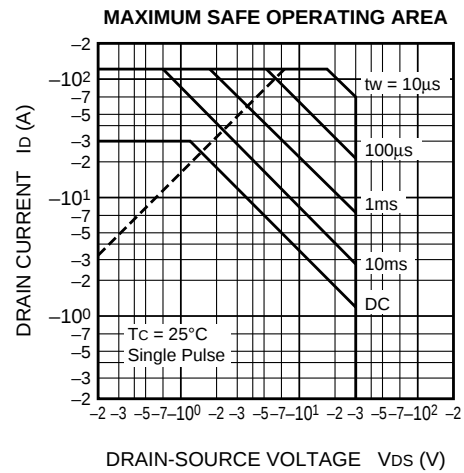
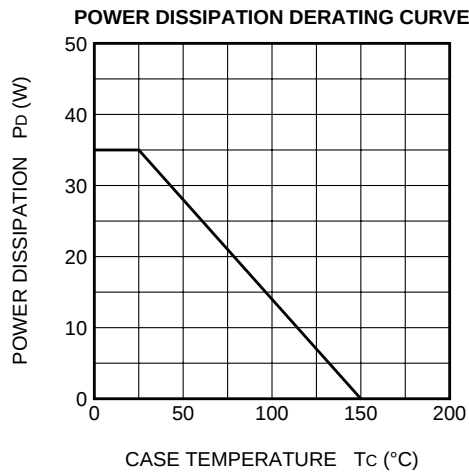
Symbol	Parameter	Conditions	Ratings	Unit
V_{DS}	Drain-source voltage	$V_{GS} = 0V$	-30	V
V_{GSS}	Gate-source voltage	$V_{DS} = 0V$	±20	V
I_D	Drain current		-30	A
I_{DM}	Drain current (Pulsed)		-120	A
I_{bA}	Avalanche drain current (Pulsed)	$L = 10\mu H$	-30	A
I_S	Source current		-30	A
I_{SM}	Source current (Pulsed)		-120	A
P_D	Maximum power dissipation		35	W
T_{ch}	Channel temperature		-55 ~ +150	°C
T_{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.26	g

Jan.1999

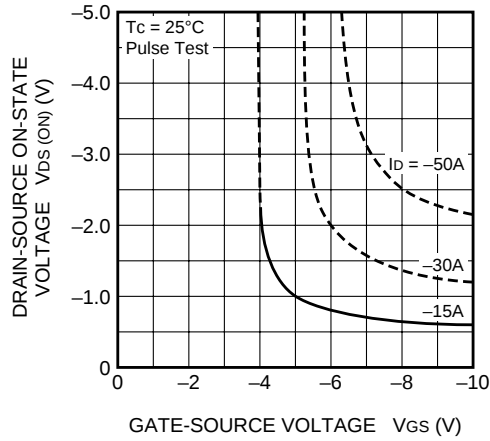
ELECTRICAL CHARACTERISTICS (Tch = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = -1mA, VDs = 0V	-30	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	μA
IDSS	Drain-source leakage current	VDS = -30V, VGS = 0V	—	—	-0.1	mA
VGS (th)	Gate-source threshold voltage	Id = -1mA, VDS = -10V	-1.3	-1.8	-2.3	V
rDS (ON)	Drain-source on-state resistance	Id = -15A, VGS = -10V	—	48	61	mΩ
rDS (ON)	Drain-source on-state resistance	Id = -5A, VGS = -4V	—	96	120	mΩ
VDs (ON)	Drain-source on-state voltage	Id = -15A, VGS = -10V	—	-0.72	-0.92	V
yfs	Forward transfer admittance	Id = -15A, VDS = -10V	—	11.9	—	S
Ciss	Input capacitance	VDS = -10V, VGS = 0V, f = 1MHz	—	2460	—	pF
Coss	Output capacitance		—	410	—	pF
Crss	Reverse transfer capacitance		—	170	—	pF
td (on)	Turn-on delay time	VDD = -15V, Id = -15A, VGS = -10V, RGEN = RGS = 50Ω	—	20	—	ns
tr	Rise time		—	84	—	ns
td (off)	Turn-off delay time		—	123	—	ns
tf	Fall time		—	60	—	ns
VSD	Source-drain voltage	Is = -15A, VGS = 0V	—	-1.0	-1.5	V
Rth (ch-c)	Thermal resistance	Channel to case	—	—	3.57	°C/W
trr	Reverse recovery time	Is = -15A, dis/dt = 50A/μs	—	50	—	ns

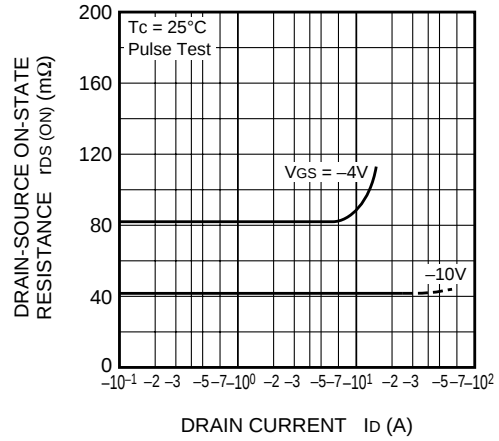
PERFORMANCE CURVES



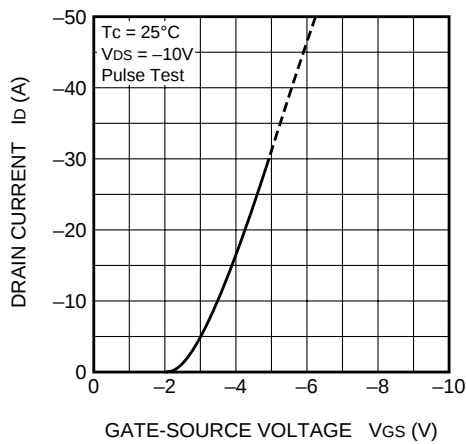
**ON-STATE VOLTAGE VS.
GATE-SOURCE VOLTAGE
(TYPICAL)**



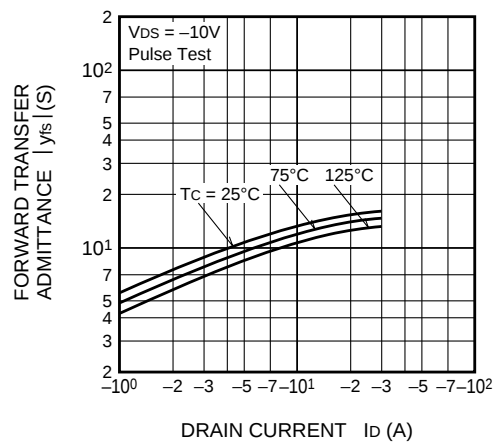
**ON-STATE RESISTANCE VS.
DRAIN CURRENT
(TYPICAL)**



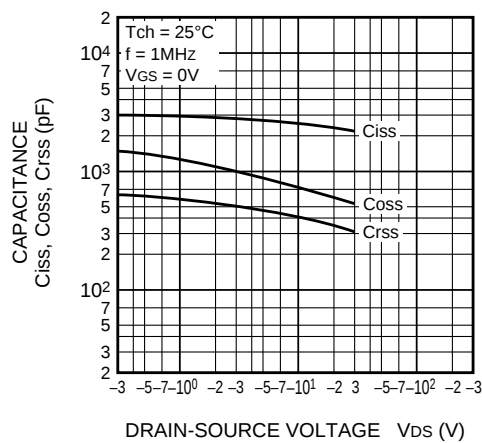
**TRANSFER CHARACTERISTICS
(TYPICAL)**



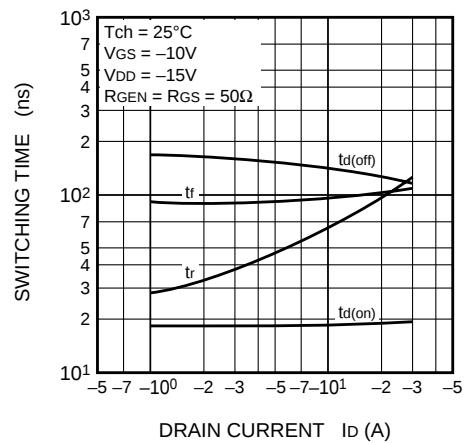
**FORWARD TRANSFER ADMITTANCE
VS. DRAIN CURRENT
(TYPICAL)**



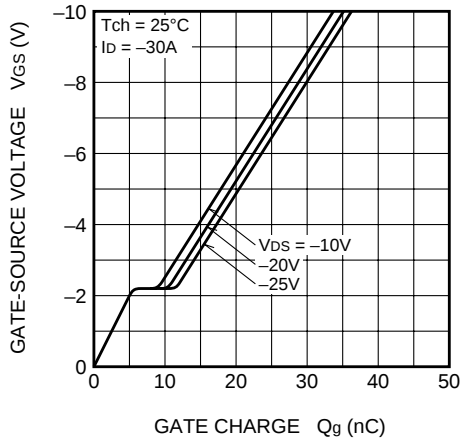
**CAPACITANCE VS.
DRAIN-SOURCE VOLTAGE
(TYPICAL)**



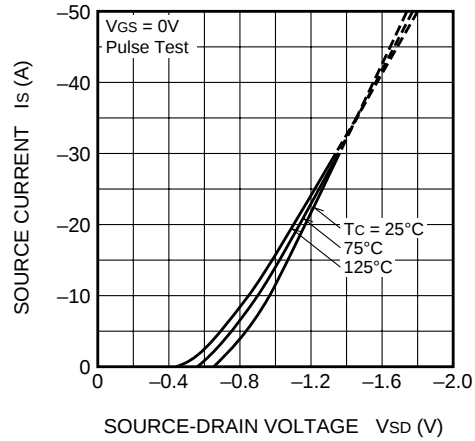
**SWITCHING CHARACTERISTICS
(TYPICAL)**



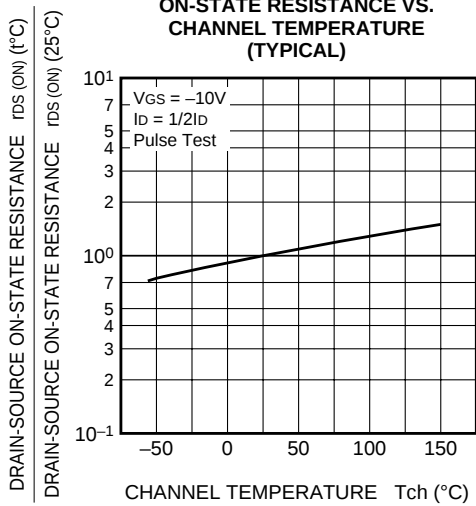
**GATE-SOURCE VOLTAGE
 VS. GATE CHARGE
 (TYPICAL)**



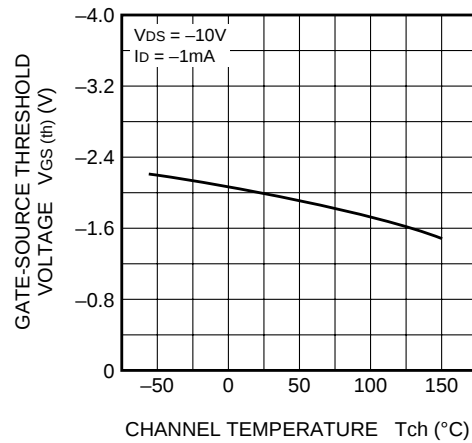
**SOURCE-DRAIN DIODE
 FORWARD CHARACTERISTICS
 (TYPICAL)**



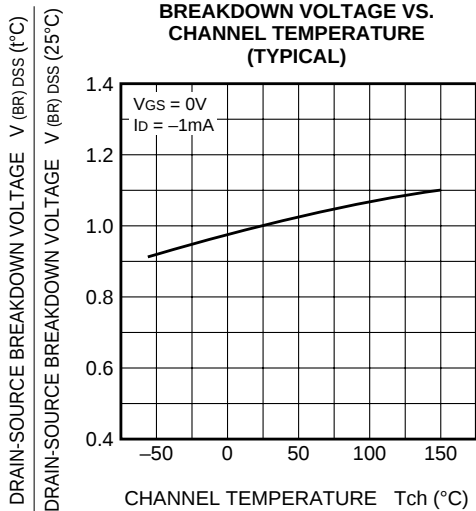
**ON-STATE RESISTANCE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**THRESHOLD VOLTAGE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**BREAKDOWN VOLTAGE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE
 CHARACTERISTICS**

