



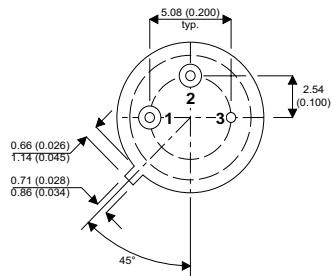
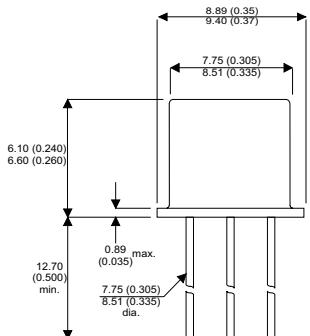
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## MECHANICAL DATA

Dimensions in mm (inches)

## MEDIUM POWER SILICON NPN PLANAR TRANSISTOR



### TO39 PACKAGE

#### Underside View

Pin 1 = Emitter      Pin 2 = Base      Pin 3 = Collector

**General purpose NPN Transistor  
in a hermetic TO39 package.**

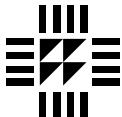
**V<sub>CEO</sub> = 100V**

**I<sub>C</sub> = 1A**

**P<sub>T</sub> = 5W**

## ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25°C unless otherwise stated)

V <sub>CBO</sub>	Collector – Base Voltage	120V
V <sub>CEO</sub>	Collector – Emitter Voltage	100V
V <sub>EBO</sub>	Emitter – Base Voltage	6V
V <sub>CER</sub>	Collector – Emitter Sustaining Voltage	100V
I <sub>C</sub>	Collector Current	1A
P <sub>TOT</sub>	Dissipation @ T <sub>amb</sub> = 25°C	1W
	@ Case Temp. = 100°C	2.9W
	@ Case Temp. = 25°C	5W
	Derating linearly	175°C
T <sub>stg</sub> , T <sub>j</sub>	Storage and Operatuing Junction Temperature	-65 to 175°C



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**ELECTRICAL CHARACTERISTICS** ( $T_{case} = 25^\circ\text{C}$  unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
$V_{CEO(SUS)}$	Collector – Emitter Sustaining Voltage	$I_C = 10\text{mA}$	$I_B = 0$	100			
$V_{CE(sat)}$	Collector – Emitter Saturation Voltage	$I_C = 200\text{mA}$	$I_B = 20\text{mA}$			1.2	V
$V_{BE(sat)}$	Base – Emitter Saturation Voltage	$I_C = 200\text{mA}$	$I_B = 20\text{mA}$			1.5	
$I_{CBO}$	Collector Cut-off Current	$V_{CB}=V_{CE}$	$I_E = 0$			1	
			$T_{amb} = 100^\circ\text{C}$			60	
$I_{EBO}$	Emitter - Base Reverse Current	$V_{EB} = 5\text{V}$	$I_C = 0$			0.1	V
$h_{FE}$	DC Current Gain	$V_{CE} = 10\text{V}$	$I_C = 10\text{mA}$	30			—
		$V_{CE} = 10\text{V}$	$I_C = 200\text{mA}$	40		120	
$f_T$	Gain Bandwidth Product	$V_{CE} = 10\text{V}$	$I_C = 50\text{mA}$ $f = 10\text{MHz}$	60	250		MHz
NF	Noise Figure	$V_{CE} = 10\text{V}$	$I_C = 300\mu\text{A}$ $f = 1\text{KHz}$		6		dB
$C_{ob}$	Output Capacitance	$V_{CB} = 10\text{V}$	$f = 0$			25	pF
$C_{ib}$	Input Capacitance	$V_{EB} = 1\text{V}$	$f = 0$			100	