

OptiMOS[®] Buck converter series

Feature

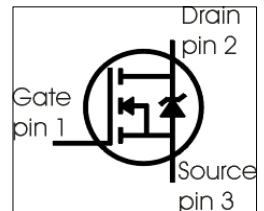
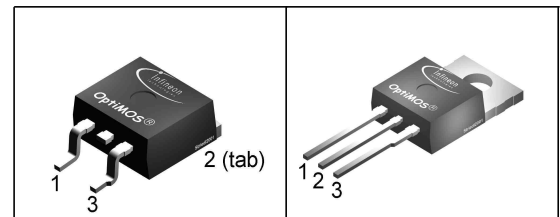
- N-Channel
- Logic Level
- Low On-Resistance $R_{DS(on)}$
- Excellent Gate Charge x $R_{DS(on)}$ product (FOM)
- Superior thermal resistance
- 175°C operating temperature
- Avalanche rated
- dv/dt rated
- Ideal for fast switching buck converters

Product Summary

V_{DS}	30	V
$R_{DS(on)}$ max. SMD version	8.9	mΩ
I_D	73	A

P- TO263 -3-2

P- TO220 -3-1



Type	Package	Ordering Code	Marking
IPP10N03L	P- TO220 -3-1	Q67042-S4040	10N03L
IPB10N03L	P- TO263 -3-2	Q67040-S4346	10N03L

Maximum Ratings, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current ¹⁾ $T_C=25^\circ\text{C}$	I_D	73 63	A
Pulsed drain current $T_C=25^\circ\text{C}$	$I_{D \text{ puls}}$	292	
Avalanche energy, single pulse $I_D=30\text{A}$, $V_{DD}=25\text{V}$, $R_{GS}=25\Omega$	E_{AS}	25	mJ
Repetitive avalanche energy, limited by T_{jmax} ²⁾	E_{AR}	10	
Reverse diode dv/dt $I_S=73\text{A}$, $V_{DS}=24$, $di/dt=200\text{A}/\mu\text{s}$, $T_{jmax}=175^\circ\text{C}$	dv/dt	6	kV/ μs
Gate source voltage	V_{GS}	± 20	V
Power dissipation $T_C=25^\circ\text{C}$	P_{tot}	107	W
Operating and storage temperature	T_j, T_{stg}	-55... +175	$^\circ\text{C}$
IEC climatic category; DIN IEC 68-1		55/175/56	

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Thermal resistance, junction - case	R_{thJC}	-	0.9	1.4	K/W
SMD version, device on PCB:	R_{thJA}				
@ min. footprint		-	-	62	
@ 6 cm ² cooling area ³⁾		-	-	40	

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage $V_{GS}=0V, I_D=1mA$	$V_{(BR)DSS}$	30	-	-	V
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D=60\mu A$	$V_{GS(th)}$	1.2	1.6	2	
Zero gate voltage drain current $V_{DS}=30V, V_{GS}=0V, T_j=25^{\circ}C$ $V_{DS}=30V, V_{GS}=0V, T_j=175^{\circ}C$	I_{DSS}	- -	0.01 10	1 100	μA
Gate-source leakage current $V_{GS}=20V, V_{DS}=0V$	I_{GSS}	-	1	100	
Drain-source on-state resistance $V_{GS}=4.5V, I_D=36A$ $V_{GS}=4.5V, I_D=36A, SMD\ version$	$R_{DS(on)}$	- -	9.9 9.5	13.4 13.1	m Ω
Drain-source on-state resistance ⁴⁾ $V_{GS}=10V, I_D=36A$ $V_{GS}=10V, I_D=36A, SMD\ version$	$R_{DS(on)}$	- -	6.8 6.5	9.2 8.9	

¹Current limited by bondwire ; with an $R_{thJC} = 1.4\text{K/W}$ the chip is able to carry $I_D = 88\text{A}$ at 25°C , for detailed information see app.-note ANPS071E available at www.infineon.com/optimos

²Defined by design. Not subject to production test.

³Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical without blown air.

⁴Diagrams are related to straight lead versions

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Dynamic Characteristics

Transconductance	g_{fs}	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$, $I_D = 63A$	32	63	-	S
Input capacitance	C_{iss}	$V_{GS} = 0V$, $V_{DS} = 25V$, $f = 1MHz$	-	1290	1710	pF
Output capacitance	C_{oss}		-	500	670	
Reverse transfer capacitance	C_{rss}		-	130	190	
Gate resistance	R_G		-	1.4	-	Ω
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 15V$, $V_{GS} = 10V$, $I_D = 18A$, $R_G = 4.7\Omega$	-	7.7	11.6	ns
Rise time	t_r		-	20	30	
Turn-off delay time	$t_{d(off)}$		-	31.5	47.3	
Fall time	t_f		-	19	28.5	

Gate Charge Characteristics

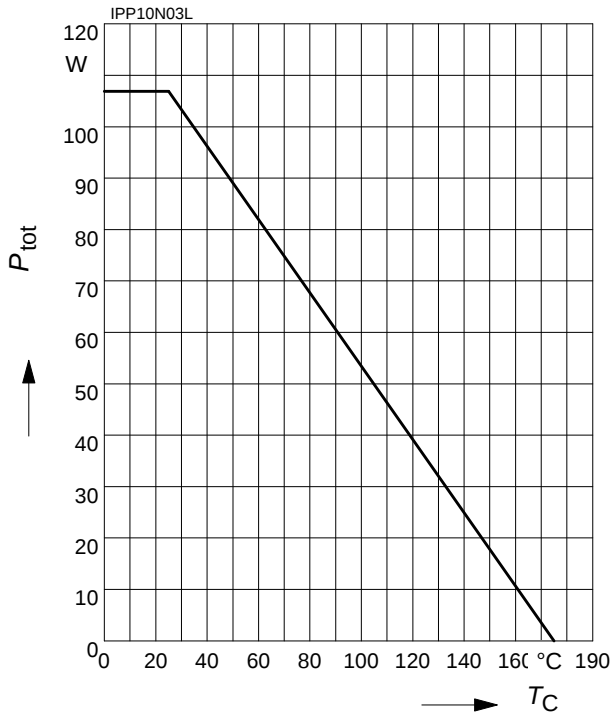
Gate to source charge	Q_{gs}	$V_{DD} = 15V$, $I_D = 36A$	-	4	5	nC
Gate to drain charge	Q_{gd}		-	10.6	13.3	
Gate charge total	Q_g	$V_{DD} = 15V$, $I_D = 36A$, $V_{GS} = 0$ to $5V$	-	19	23.8	
Output charge	Q_{oss}	$V_{DS} = 15V$, $I_D = 36A$, $V_{GS} = 0V$	-	18.2	22.8	nC
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 15V$, $I_D = 36A$	-	3.6	-	V

Reverse Diode

Inverse diode continuous forward current	I_S	$T_C = 25^\circ C$	-	-	73	A
Inv. diode direct current, pulsed	I_{SM}		-	-	292	
Inverse diode forward voltage	V_{SD}	$V_{GS} = 0V$, $I_F = 73A$	-	0.96	1.28	V
Reverse recovery time	t_{rr}	$V_R = 15V$, $I_F = I_S$, $di_F/dt = 100A/\mu s$	-	32.9	41.2	ns
Reverse recovery charge	Q_{rr}		-	33	41	nC

1 Power dissipation

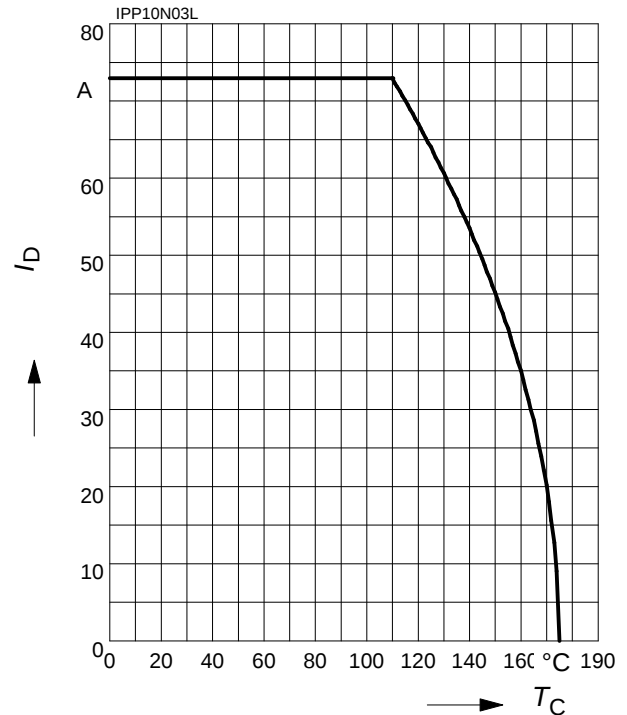
$$P_{\text{tot}} = f(T_C)$$



2 Drain current

$$I_D = f(T_C)$$

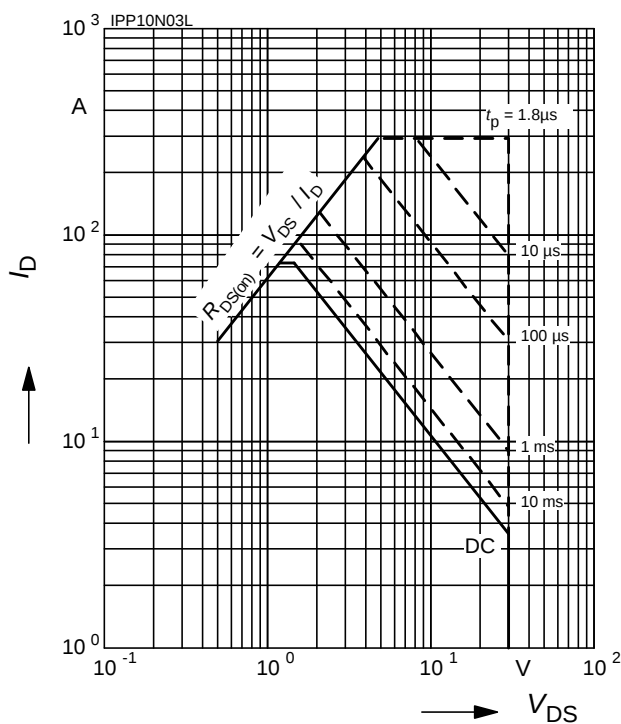
parameter: $V_{GS} \geq 10$ V



3 Safe operating area

$$I_D = f(V_{DS})$$

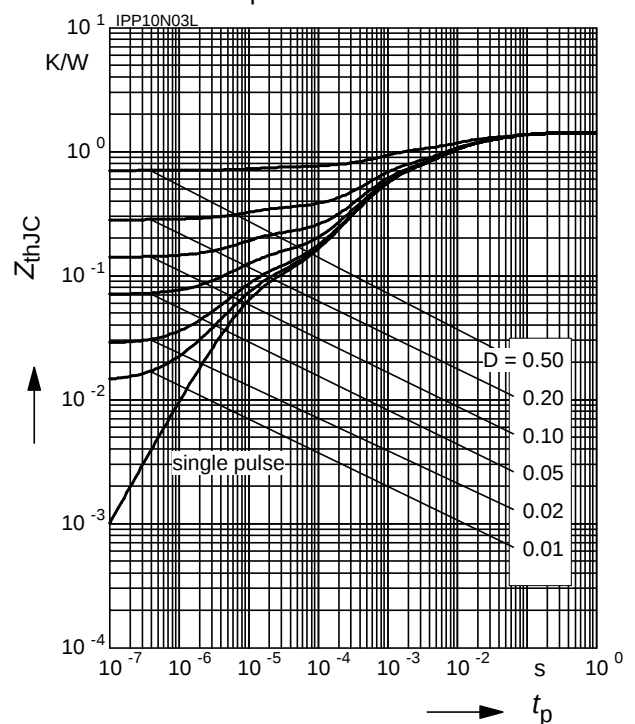
parameter: $D = 0$, $T_C = 25$ °C



4 Max. transient thermal impedance

$$Z_{thJC} = f(t_p)$$

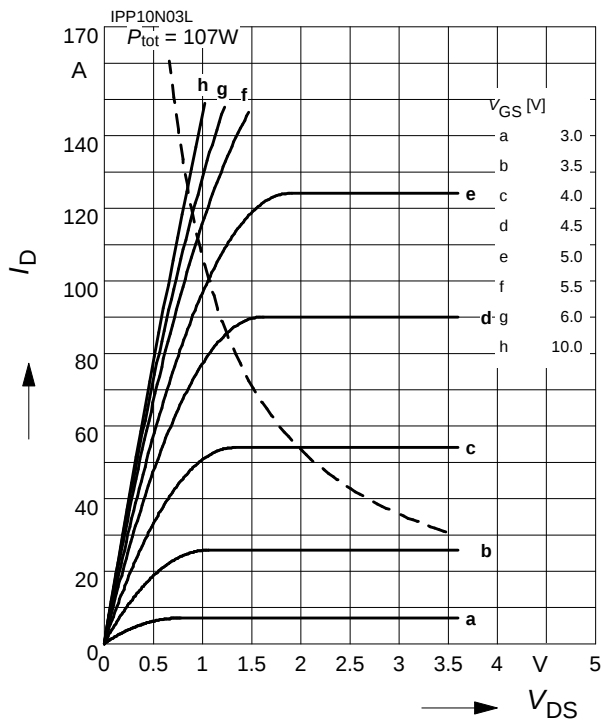
parameter: $D = t_p/T$



5 Typ. output characteristic

$I_D = f(V_{DS})$; $T_j = 25^\circ\text{C}$

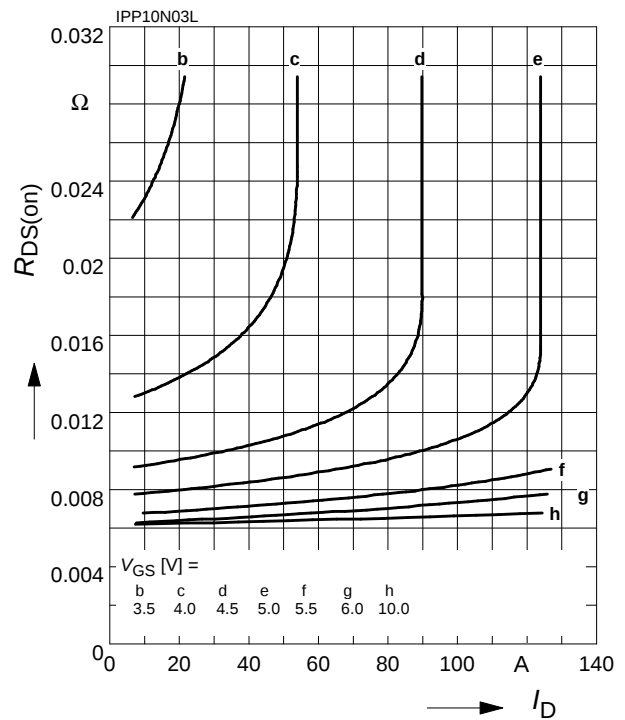
parameter: $t_p = 80 \mu\text{s}$



6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D)$

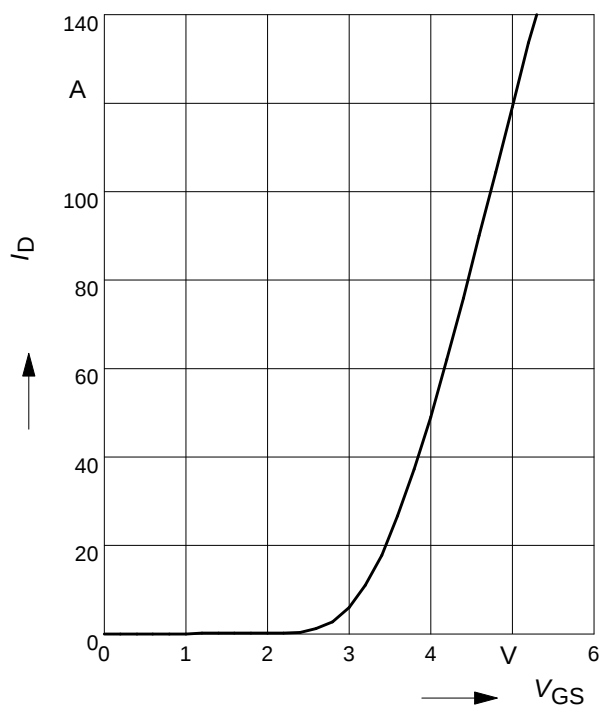
parameter: V_{GS}



7 Typ. transfer characteristics

$I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$

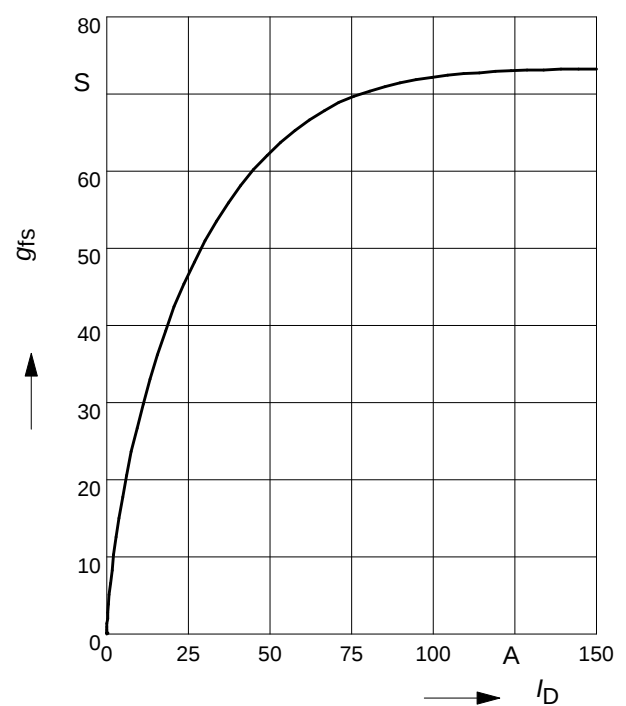
parameter: $t_p = 80 \mu\text{s}$



8 Typ. forward transconductance

$g_{fs} = f(I_D)$; $T_j = 25^\circ\text{C}$

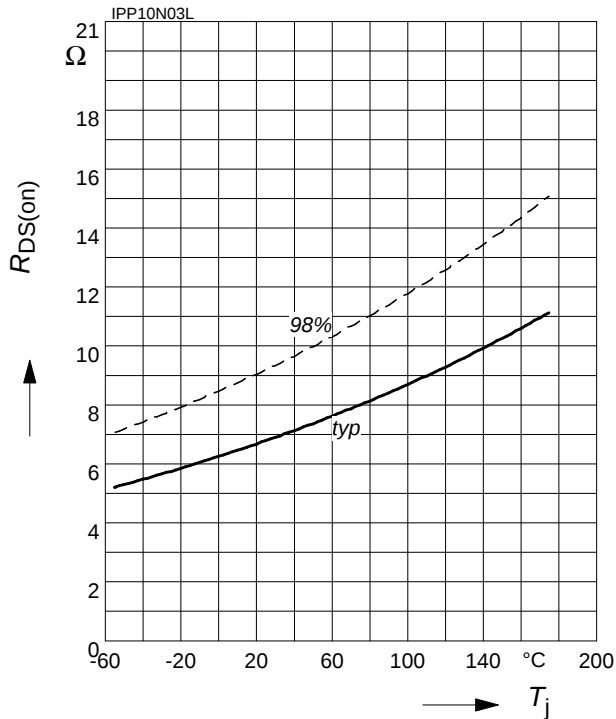
parameter: g_{fs}



9 Drain-source on-state resistance

$$R_{DS(on)} = f(T_j)$$

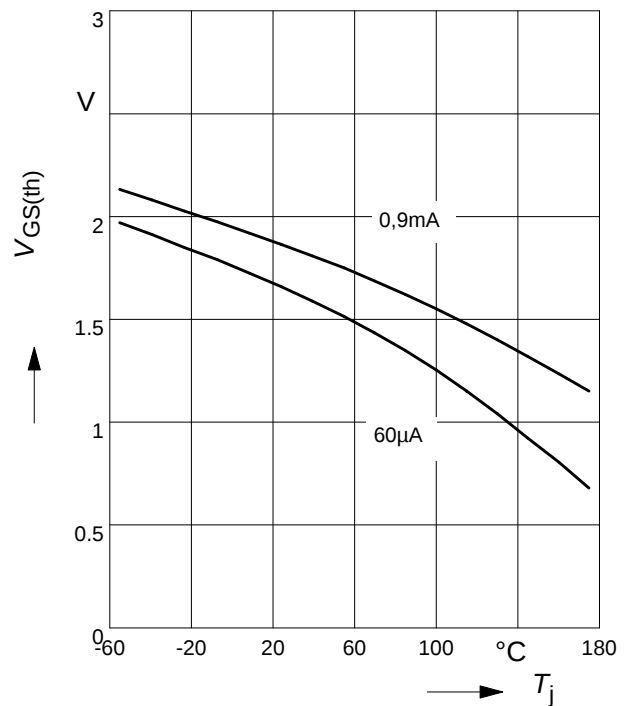
parameter: $I_D = 36\text{ A}$, $V_{GS} = 10\text{ V}$



10 Typ. gate threshold voltage

$$V_{GS(th)} = f(T_j)$$

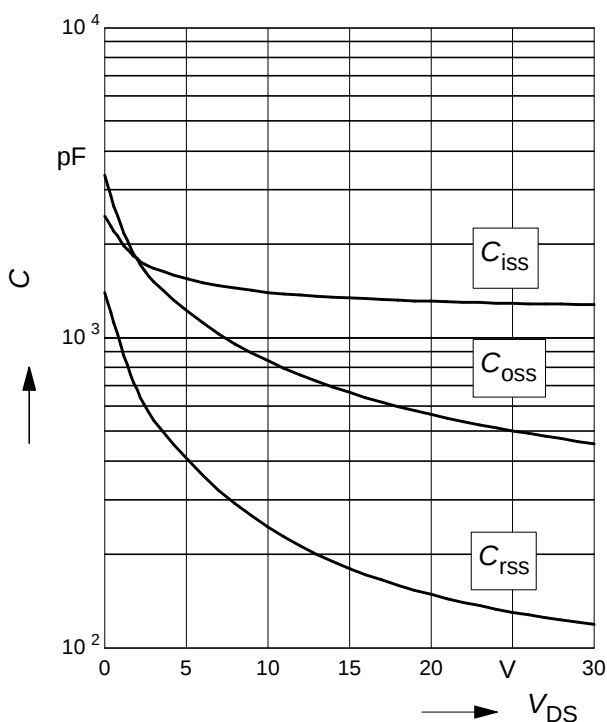
parameter: $V_{GS} = V_{DS}$



11 Typ. capacitances

$$C = f(V_{DS})$$

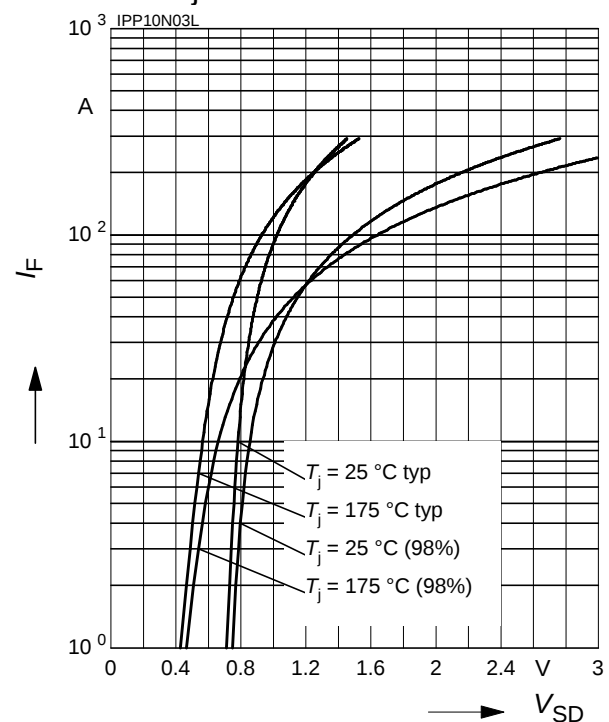
parameter: $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$



12 Forward character. of reverse diode

$$I_F = f(V_{SD})$$

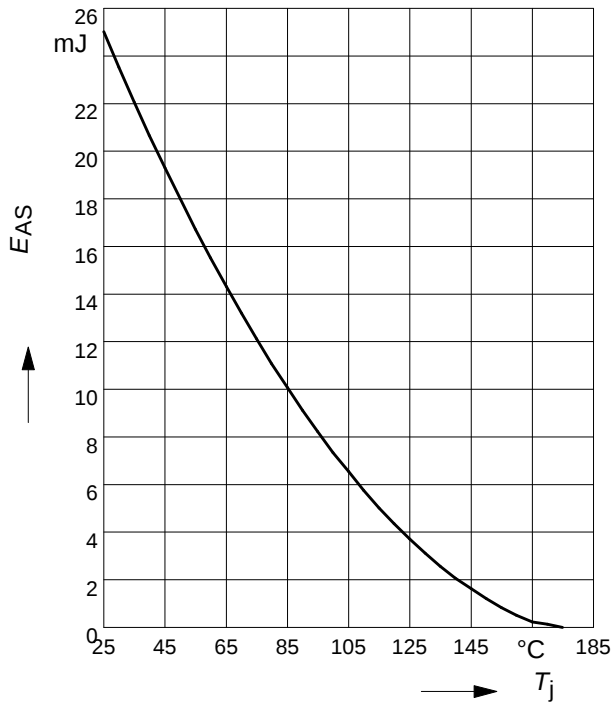
parameter: T_j , $t_p = 80\text{ }\mu\text{s}$



13 Typ. avalanche energy

$$E_{AS} = f(T_j)$$

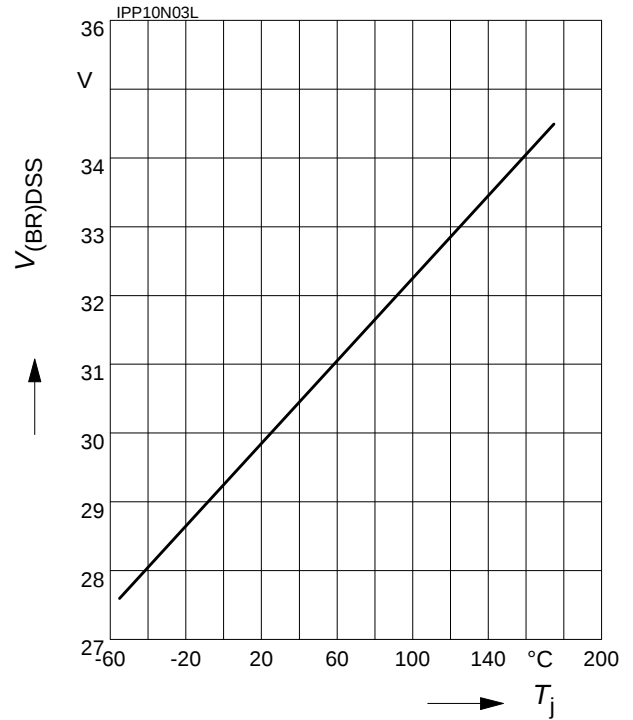
par.: $I_D = 30\text{ A}$, $V_{DD} = 25\text{ V}$, $R_{GS} = 25\ \Omega$



15 Drain-source breakdown voltage

$$V_{(BR)DSS} = f(T_j)$$

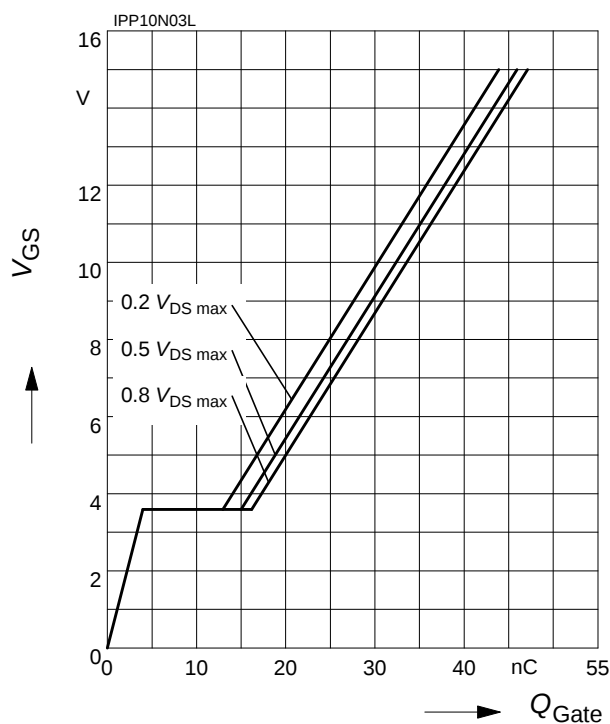
parameter: $I_D = 10\text{ mA}$



14 Typ. gate charge

$$V_{GS} = f(Q_{Gate})$$

parameter: $I_D = 36\text{ A}$ pulsed



Published by
Infineon Technologies AG,
Bereichs Kommunikation
St.-Martin-Strasse 53,
D-81541 München
© Infineon Technologies AG 1999
All Rights Reserved.

Attention please!

The information herein is given to describe certain components and shall not be considered as warranted characteristics.

Terms of delivery and rights to technical change reserved.

We hereby disclaim any and all warranties, including but not limited to warranties of non-infringement, regarding circuits, descriptions and charts stated herein.

Infineon Technologies is an approved CECC manufacturer.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office in Germany or our Infineon Technologies Representatives worldwide (see address list).

Warnings

Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.