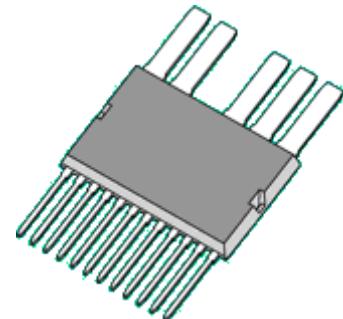
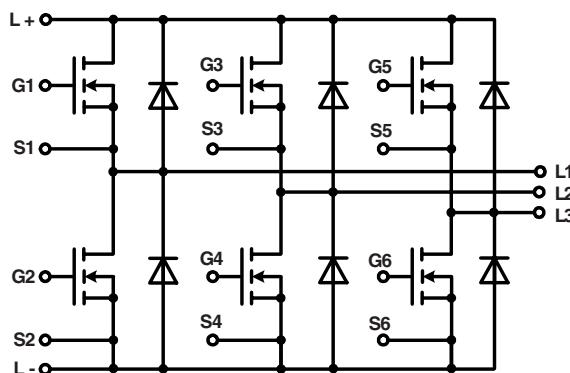


Three phase full bridge

with Trench MOSFETs
in DCB isolated high current package

V_{DSS} = 55 V
 I_{D25} = 160 A
 $R_{DSon\ typ.}$ = 2.3 mΩ

Preliminary data



MOSFETs

Symbol	Conditions	Maximum Ratings		
V_{DSS}	$T_{VJ} = 25^\circ\text{C}$ to 150°C	55		V
V_{GS}		± 20		V
I_{D25}	$T_c = 25^\circ\text{C}$	160		A
I_{D90}	$T_c = 90^\circ\text{C}$	120		A
I_{F25}	$T_c = 25^\circ\text{C}$ (diode)	135		A
I_{F90}	$T_c = 90^\circ\text{C}$ (diode)	90		A

Symbol	Conditions	Characteristic Values		
		($T_{VJ} = 25^\circ\text{C}$, unless otherwise specified)		
R_{DSon}	on chip level at $\left. \begin{array}{l} T_{VJ} = 25^\circ\text{C} \\ V_{GS} = 10 \text{ V} \end{array} \right\} T_{VJ} = 125^\circ\text{C}$	2.3	2.9	mΩ
		3.8		mΩ
V_{GSth}	$V_{DS} = 20 \text{ V}; I_D = 1 \text{ mA}$	2	4	V
I_{DSS}	$V_{DS} = V_{DSS}; V_{GS} = 0 \text{ V}; T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = 125^\circ\text{C}$	0.1	1	μA
				mA
I_{GSS}	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$		0.2	μA
Q_g	$\left. \begin{array}{l} V_{GS} = 10 \text{ V}; V_{DS} = 44 \text{ V}; I_D = 25 \text{ A} \\ \end{array} \right\}$	86		nC
Q_{gs}		18		nC
Q_{gd}		25		nC
$t_{d(on)}$	$\left. \begin{array}{l} V_{GS} = 10 \text{ V}; V_{DS} = 30 \text{ V}; \\ I_D = 25 \text{ A}; R_G = 10 \Omega \end{array} \right\}$	25		ns
t_r		50		ns
$t_{d(off)}$		70		ns
t_f		40		ns
V_F	(diode) $I_F = 80 \text{ A}; V_{GS} = 0 \text{ V}$	0.9	1.4	V
t_{rr}	(diode) $I_F = 20 \text{ A}; -di/dt = 100 \text{ A}/\mu\text{s}; V_{DS} = 30 \text{ V}$	100		ns
R_{thJC}	with heat transfer paste		0.85	K/W
R_{thJH}		1.7		K/W

Applications

- AC drives
 - in automobiles
 - electric power steering
 - starter generator
 - in industrial vehicles
 - propulsion drives
 - fork lift drives
 - in battery supplied equipment

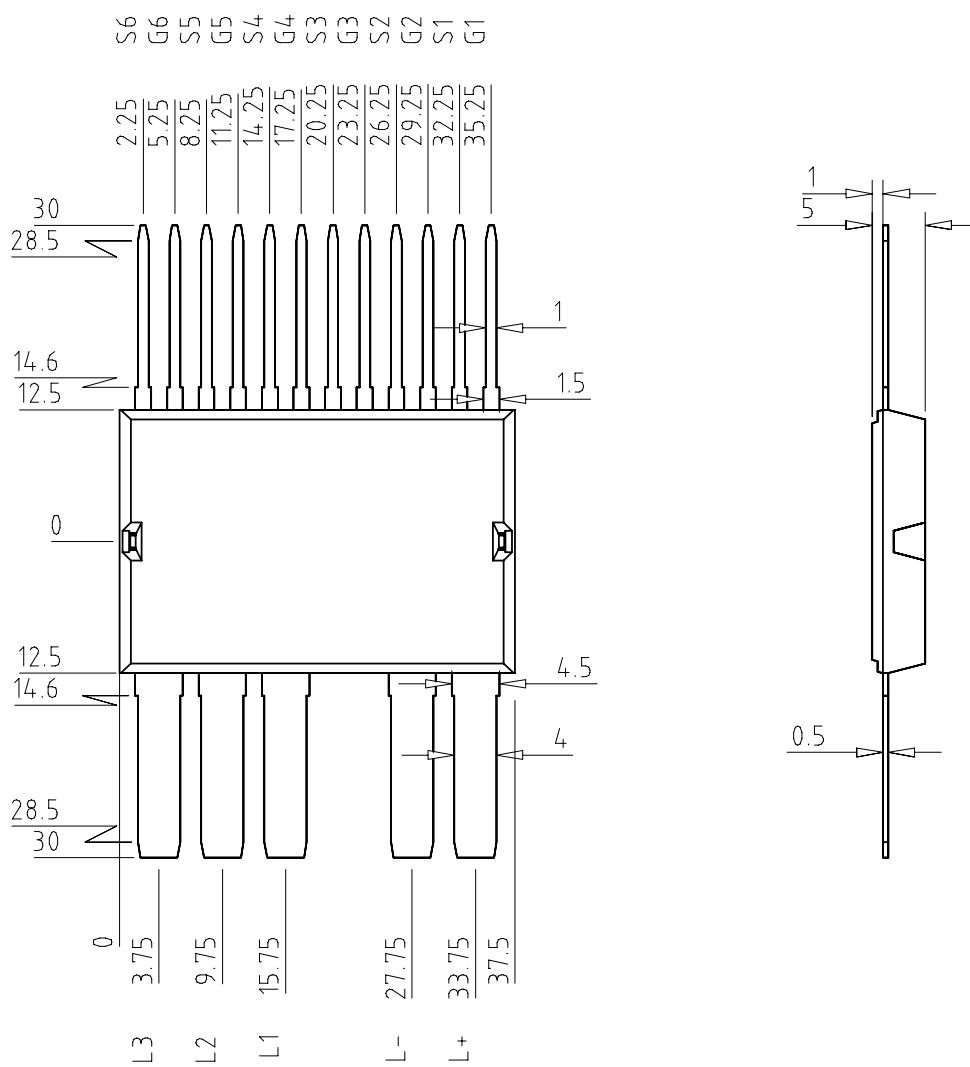
Features

- MOSFETs in trench technology:
 - low R_{DSon}
 - optimized intrinsic reverse diode
- package:
 - high level of integration
 - high current capability
 - auxiliary terminals for MOSFET control
 - terminals for soldering or welding connections
 - isolated DCB ceramic base plate with optimized heat transfer

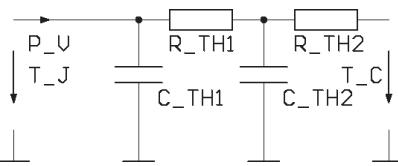
Component

Symbol	Conditions	Maximum Ratings	
I_{RMS}	per pin in main current paths (P+, N-, L ₁ , L ₂ , L ₃) may be additionally limited by external connections	300	A
T_{VJ}		-40...+175	°C
T_{stg}		-55...+125	°C
V_{ISOL}	$I_{ISOL} \leq 1$ mA; 50/60 Hz; t = 1 min	1000	V~
F_c	Mounting force with clip	50 - 250	N

Symbol	Conditions	Characteristic Values			
		$(T_{VJ} = 25^\circ\text{C}, \text{unless otherwise specified})$	min.	typ.	max.
$R_{\text{pin to chip}}$			0.6	mΩ	
C_p	coupling capacity between shorted pins and mounting tab in the case		160	pF	
Weight	typ.		25	g	

Dimensions in mm (1 mm = 0.0394")

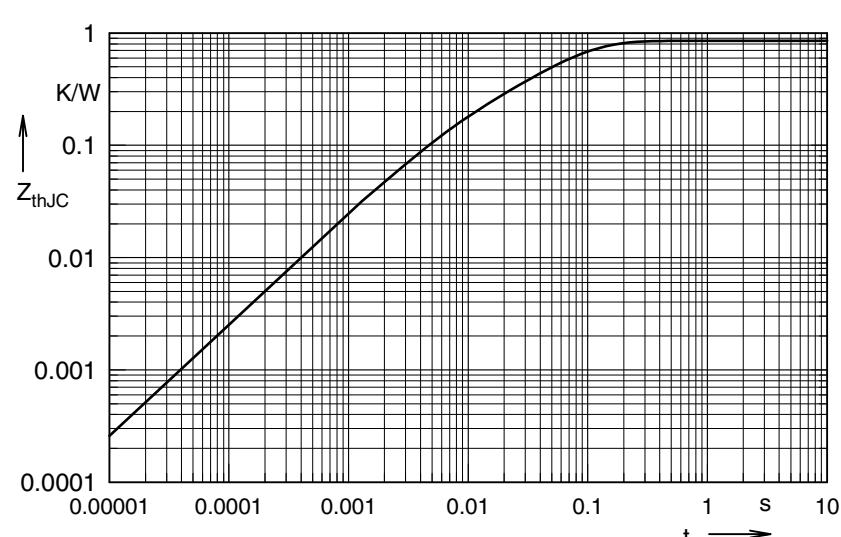
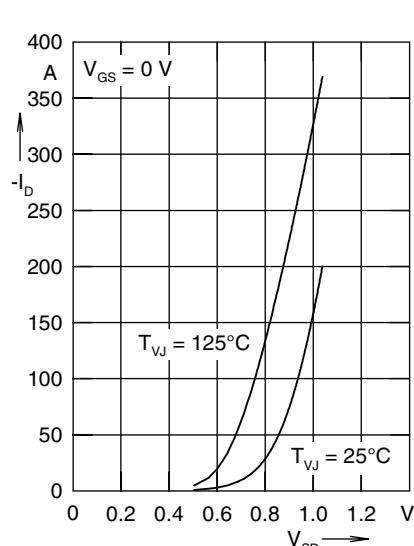
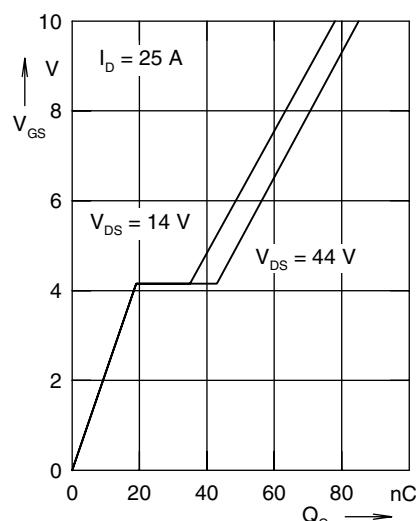
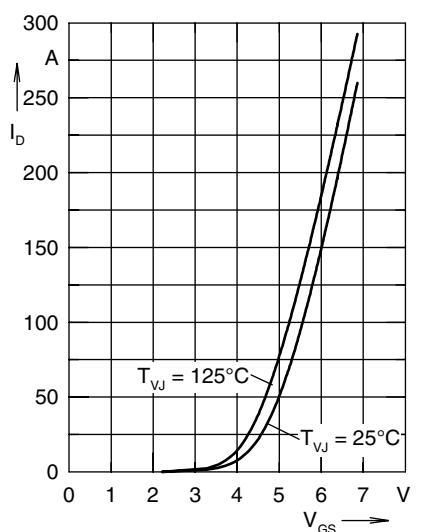
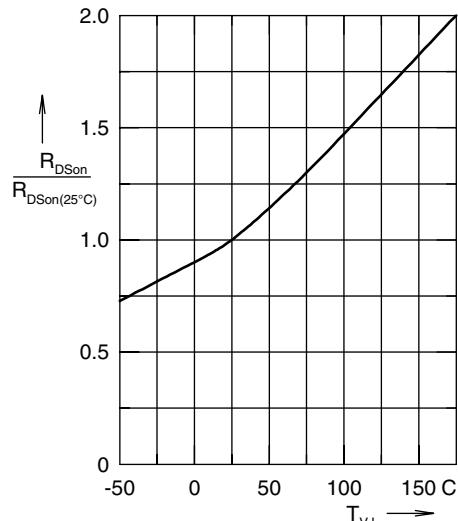
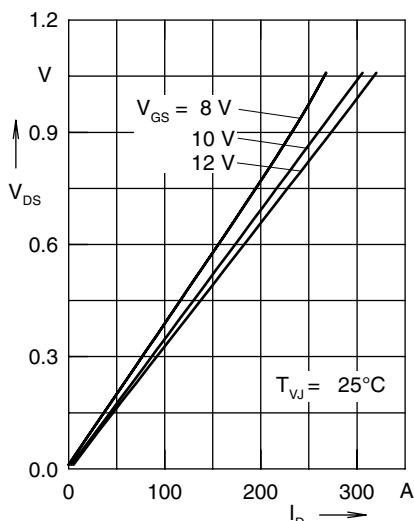
IXYS reserves the right to change limits, test conditions and dimensions.

Equivalent Circuits for Simulation**Thermal Response**

junction - case (typ.)

$$C_{th1} = 0.039 \text{ J/K}; R_{th1} = 0.28 \text{ K/W}$$

$$C_{th2} = 0.069 \text{ J/K}; R_{th2} = 0.57 \text{ K/W}$$



IXYS reserves the right to change limits, test conditions and dimensions.