TENTATIVE

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

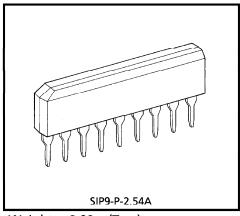
TC5081BP

PHASE COMPARATOR

The TC5081BP is phase comparator for PLL frequency synthesizer type, and consists of a digital phase comparator and an amplifier for active low pass filter.

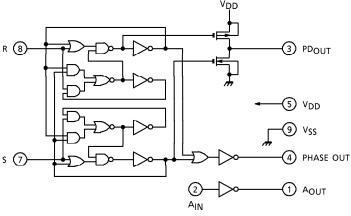
FEATURES

- The phase comparator detects two input pulse phase differences and outputs proportionate positive or negative pulses to PD_{OUT}. When the input pulse phases are the same, PD_{OUT} has high impedance.
- Because the IC is CMOS, the input impedance of the filter for the amp is extremely high and has excellent characteristics.
- TC5081BP comes in a SIP 9 PIN.

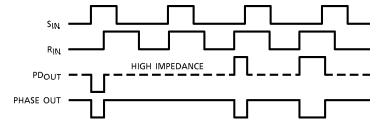


Weight: 0.92g (Typ.)

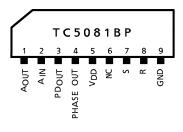
LOGIC DIAGRAM



PHASE COMPARATOR TIMING CHART



PIN CONNECTION (SIDE VIEW)



961001EBA2

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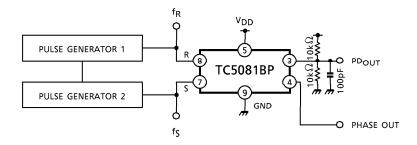
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD}	15	V
Input Voltage	v_{IN}	$-0.3 \sim V_{DD} + 0.3$	>
Operating Temperature	T _{opr}	- 30∼75	°C
Storage Temperature	T _{stg}	- 55∼125	°C

ELECTRICAL CHARACTERISTICS ($V_{DD} = 7.5V$, $T_0 = -30 \sim 75$ °C)

CHARACTE	RISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Operating Supp	ly Voltage	V_{DD}	_	_		4.5	_	12	٧
LOUTBUT VOITAGe ——	"H" Level	Voн		$V_{IH} = 6.6V$,	$I_{OH} = -50\mu A$	7.3	_	_	٧
	"L" Level	VOL		$V_{IL} = 1.6V$	$I_{OL} = 50 \mu A$	_	_	0.2	\
Quiescent Current		lDD	_	$V_{IH} = 7.5V, V_{IL} = 0V$		_	_	200	μΑ
13 3tate Leak	"H" Level	ITLH					-	500	nΑ
	"L" Level	lTLL		_		_	_	- 500	nΑ
Filter Amp. Voltage Gain		GV	3	$R_{\text{1}-\text{2}} = 1M\Omega$, $f_{\text{1N}} = 1kHz$ $R_{\text{g}} = 600\Omega$			30	_	dB

TEST CIRCUIT 1



The pulse generator-1 is synchronized with the pulse generator-2. Then, the phase of f_R and f_s is variable.

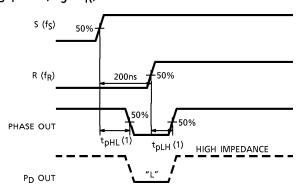
961001EBA2'

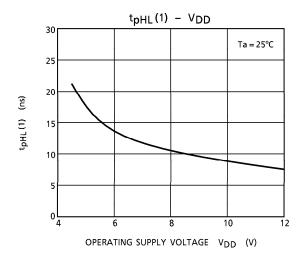
The products described in this document are subject to foreign exchange and foreign trade control laws.

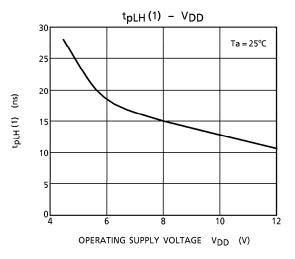
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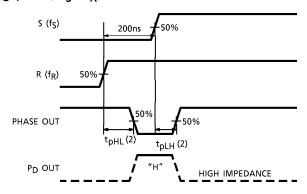
WAVE FORM 1 (The leading phase, $f_S > f_R$)

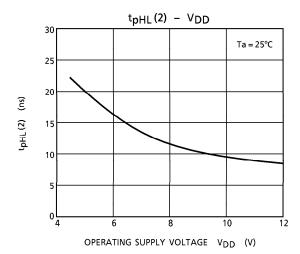


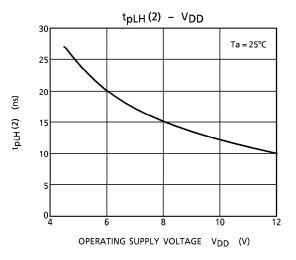




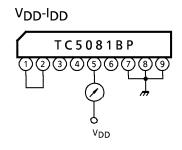
WAVE FORM 2 (The lagging phase, $f_S < f_R$)



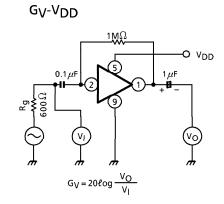


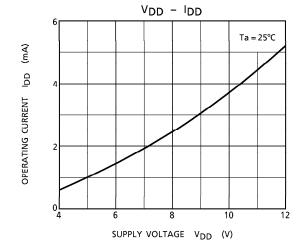


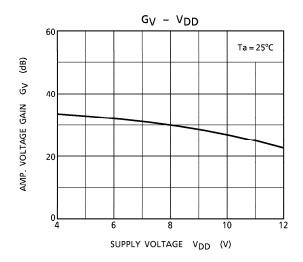
TEST CIRCUIT 2



TEST CIRCUIT 3

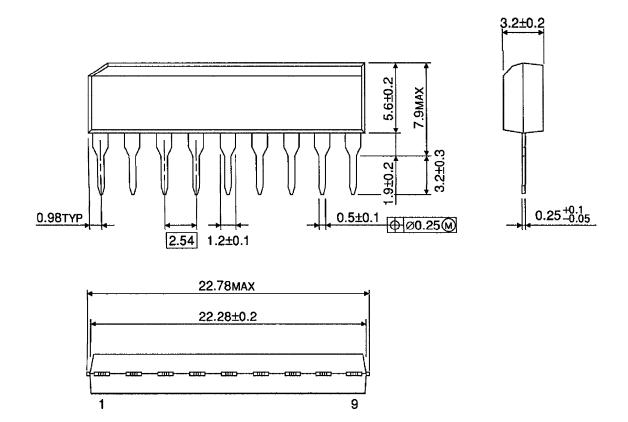






OUTLINE DRAWING

SIP9-P-2.54A Unit: mm



Weight: 0.92g (Typ.)