N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
30	0.009 @ V_{GS} = 10 V	11
	0.010 @ V_{GS} = 4.5 V	10
	0.014 @ V_{GS} = 2.5 V	8.8

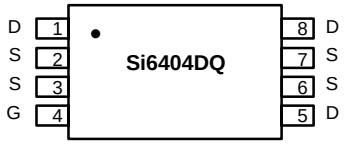
FEATURES

- TrenchFET® Power MOSFETS: 2.5-V Rated
- 30-V V_{DS}

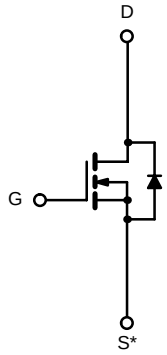
APPLICATIONS

- Battery Switch
- Charger Switch

TSSOP-8



Top View



N-Channel MOSFET

* Source Pins 2, 3, 6 and 7 must be tied common.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V_{DS}	30		V
Gate-Source Voltage		V_{GS}	±12		
Continuous Drain Current (T_J = 150°C)[a]	T_A = 25°C	I_D	11	8.6	A
	T_A = 70°C		8.9	6.9	
Pulsed Drain Current (10 μs Pulse Width)		I_{DM}	30		
Continuous Source Current (Diode Conduction)[a]		I_S	1.5	0.95	
Maximum Power Dissipation[a]	T_A = 25°C	P_D	1.75	1.08	W
	T_A = 70°C		1.14	0.69	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	–55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[a]	t ≤ 10 sec	R_{thJA}	55	70	°C/W
	Steady State		95	115	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	35	45	

Notes
a. Surface Mounted on 1" x 1" FR4 Board.

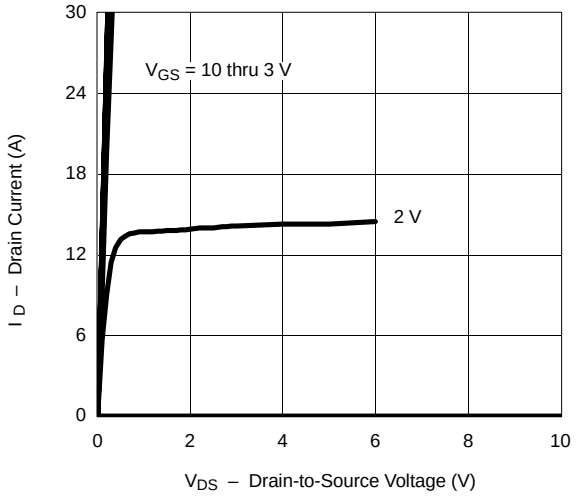
SPECIFICATIONS (T_J = 25°C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 μA	0.6			V
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 24 V, V_{GS} = 0 V			1	μA
		V_{DS} = 24 V, V_{GS} = 0 V, T_J = 70°C			10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} = 5 V, V_{GS} = 10 V	20			A
Drain-Source On-State Resistance[a]	$r_{DS(on)}$	V_{GS} = 10 V, I_D = 11 A		0.0073	0.009	Ω
		V_{GS} = 4.5 V, I_D = 10 A		0.0084	0.010	
		V_{GS} = 2.5 V, I_D = 8.8 A		0.0116	0.014	Ω
Forward Transconductance[a]	g_{fs}	V_{DS} = 10 V, I_D = 11 A		27		S
Diode Forward Voltage[a]	V_{SD}	I_S = 1.5 A, V_{GS} = 0 V		0.72	1.1	V
Dynamic[b]						
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 11 A		32	48	nC
Gate-Source Charge	Q_{gs}			8.1		
Gate-Drain Charge	Q_{gd}			10		
Gate Resistance	R_g			7.5		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 15 Ω I_D ≅ 1 A, V_{GEN} = 4.5 V, R_G = 6 Ω		35	55	ns
Rise Time	t_r			35	55	
Turn-Off Delay Time	$t_{d(off)}$			100	150	
Fall Time	t_f			50	75	
Source-Drain Reverse Recovery Time	t_{rr}	I_F = 1.5 A, di/dt = 100 A/μs		40	85	

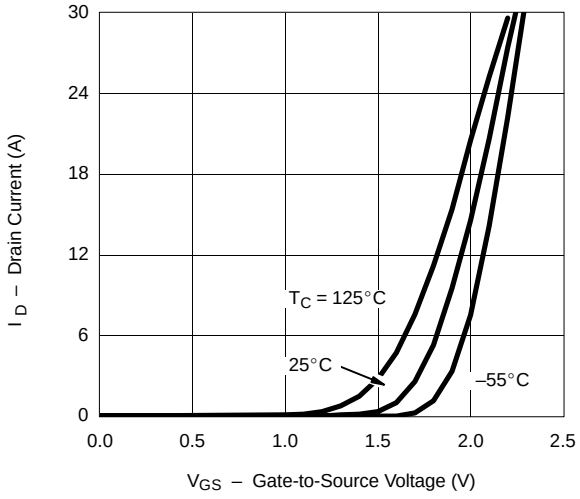
Notes
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

Output Characteristics

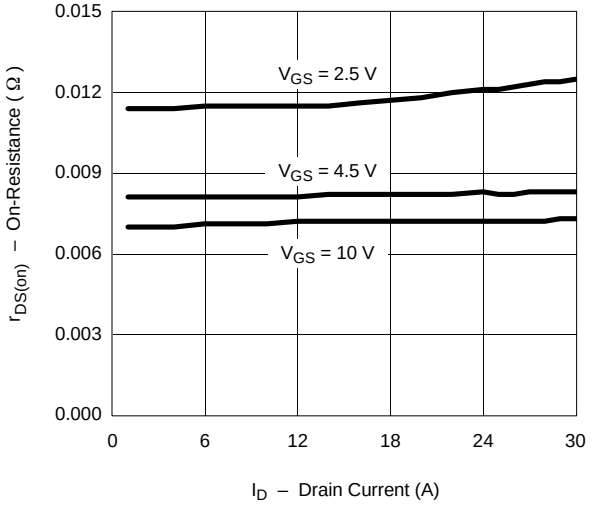


Transfer Characteristics

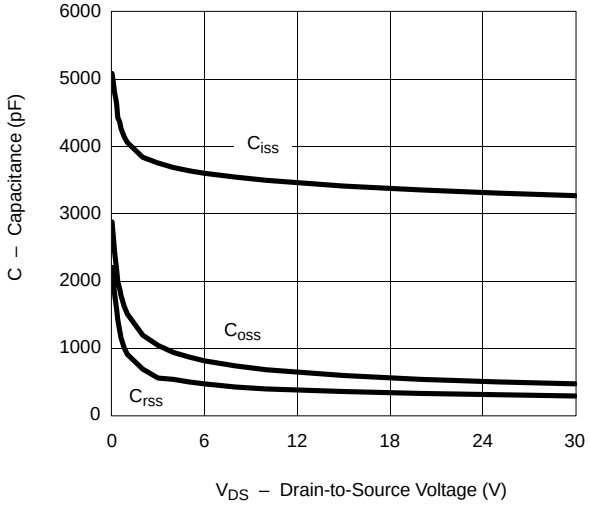


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

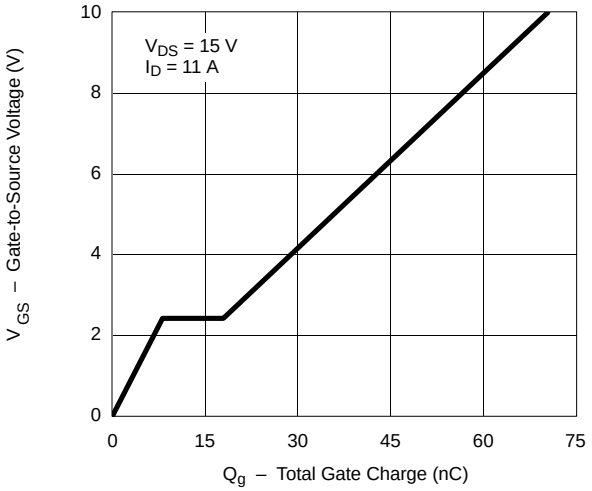
On-Resistance vs. Drain Current



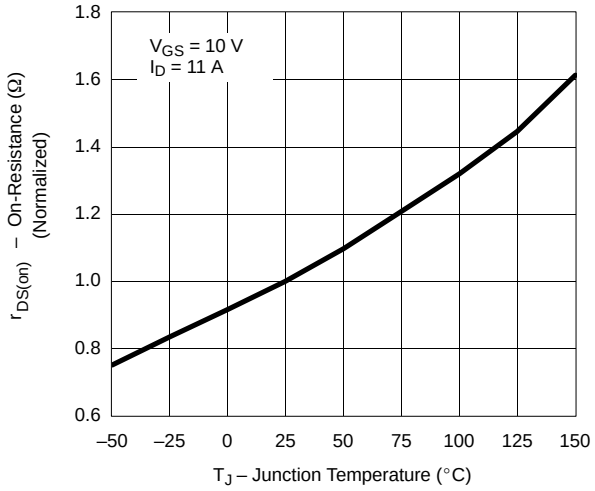
Capacitance



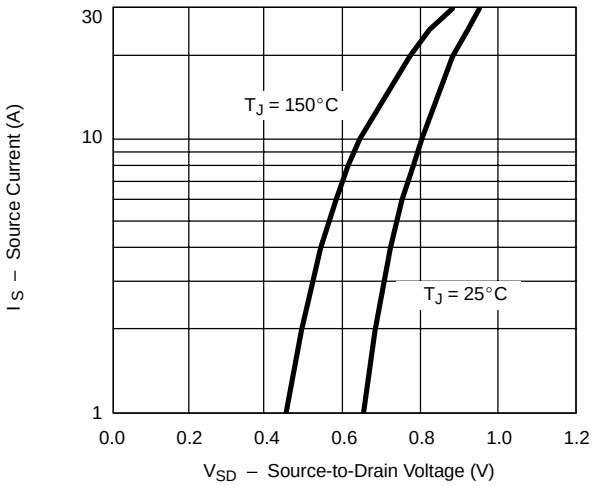
Gate Charge



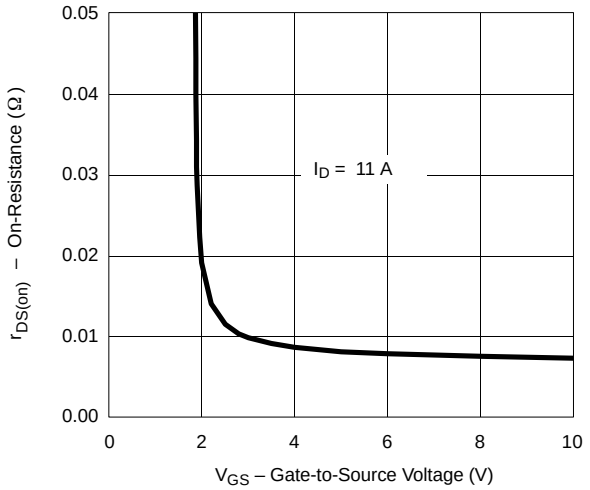
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

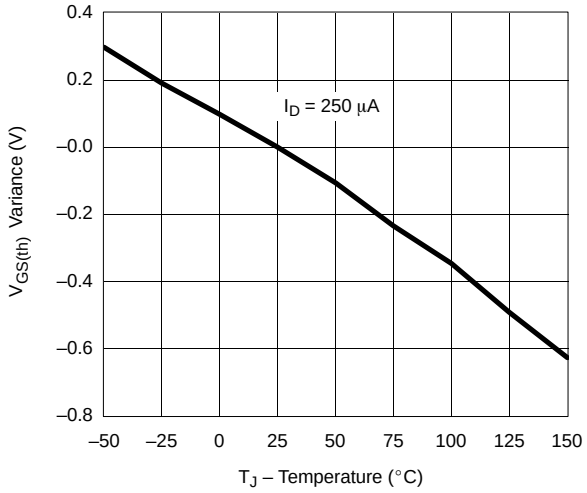


On-Resistance vs. Gate-to-Source Voltage

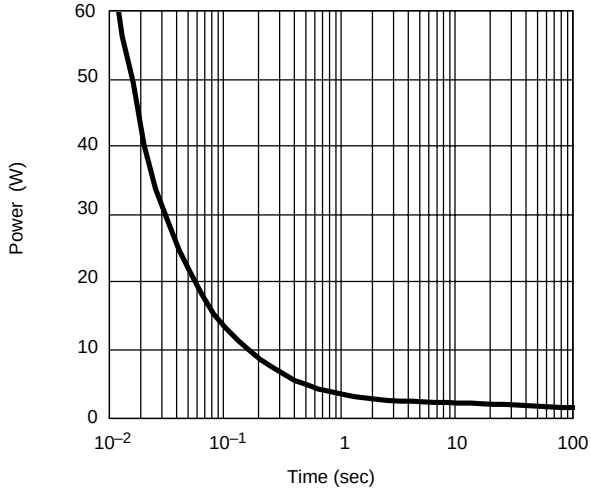


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

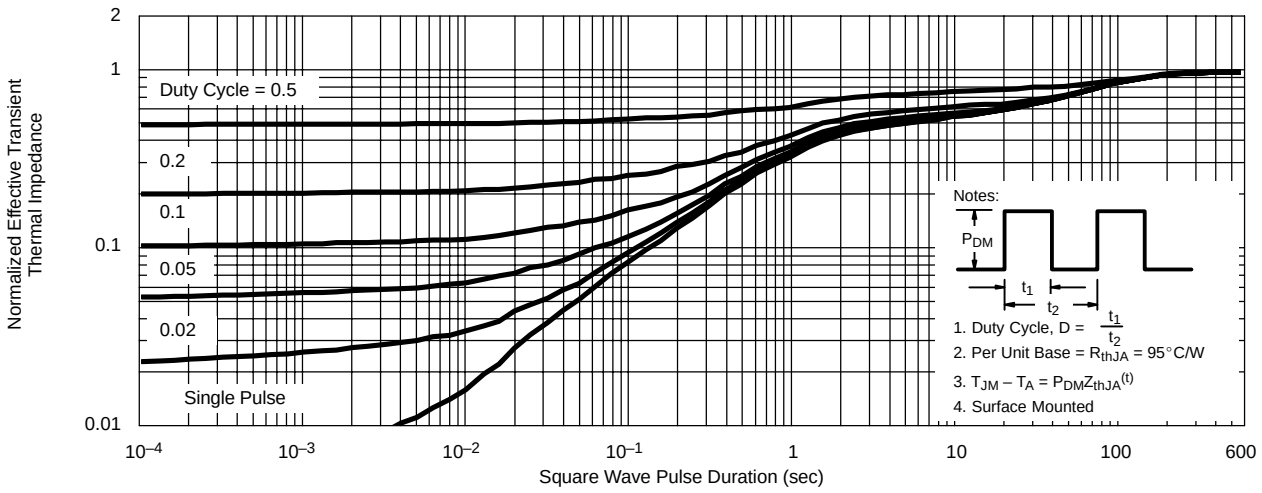
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot