

# UNISONIC TECHNOLOGIES CO., LTD

UT136N03 **Preliminary Power MOSFET** 

# N-CHANNEL **ENHANCEMENT MODE**

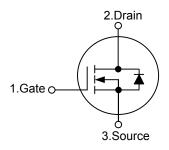
#### **DESCRIPTION**

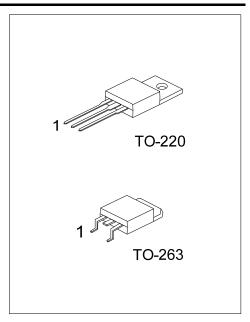
The UT136N03 uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

# **FEATURES**

- \* V<sub>DS</sub>(V)= 30 V
- \* I<sub>D</sub>=136 A
- \*  $R_{DS(ON)}$ = 4.5m $\Omega$ @ $V_{GS}$ =10 V
- \*  $R_{DS(ON)} = 5.6 \text{m}\Omega @V_{GS} = 4.5 \text{ V}$

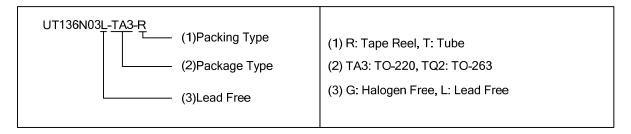
#### **SYMBOL**





#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT136N03L-TA3-R	UT136N03G-TA3-T	TO-220	G	D	S	Tube	
UT136N03L-TQ2-T	UT136N03G-TQ2-T	TO-263	G	D	S	Tube	
UT136N03L-TQ2-R	UT136N03G-TQ2-R	TO-263	G	D	S	Tape Reel	



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# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	136	Α
Pulsed Drain Current (Note1)	I <sub>DM</sub>	400	Α
Single Pulsed Avalanche Energy (Note4)	E <sub>AS</sub>	875	mJ
Power Dissipation	$P_{D}$	100	W
Junction Temperature	$T_J$	+175	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ <b>+</b> 175	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	62.5	°C/W	
Junction to Case	$\theta_{JC}$	1.4	°C/W	

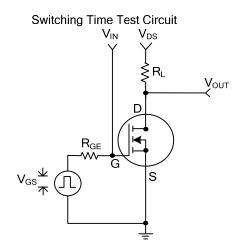
# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise noted)

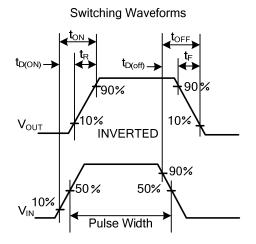
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
Drain-Source Leakage Current	$I_{DSS}$	V <sub>DS</sub> =30 V,V <sub>GS</sub> =0 V			1	μΑ	
Gate-Source Leakage Current	$I_{GSS}$	V <sub>DS</sub> =0 V, V <sub>GS</sub> = ±20 V			±100	nA	
ON CHARACTERISTICS (Note2)							
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1		3	V	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =40 A		3.6	4.5	mΩ	
		V <sub>GS</sub> =4.5 V, I <sub>D</sub> =40 A			5.6	11177	
DYNAMIC PARAMETERS (Note3)							
Input Capacitance	$C_{ISS}$			3800		pF	
Output Capacitance	Coss	$V_{DS}$ =15V, $V_{GS}$ =0V, f=1.0MHz		800			
Reverse Transfer Capacitance	$C_{RSS}$			600			
SWITCHING PARAMETERS (Note3)	_		=	-	-		
Total Gate Charge	$Q_{G}$			60	72	nC	
Gate Source Charge	$Q_GS$	$V_{DS}$ =15V, $V_{GS}$ =5V, $I_{D}$ =16A		20.8			
Gate Drain Charge	$Q_GD$			22			
Turn-ON Delay Time	$t_{D(ON)}$			25.7	50	ns	
Turn-ON Rise Time	$t_R$	$V_{DD}$ =15V, $I_D$ =1A, $R_{GEN}$ =6 $\Omega$		10	20		
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	V <sub>GS</sub> =10 V		128	200		
Turn-OFF Fall-Time	$t_{F}$			34	70		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Drain-Source Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =20 A,V <sub>GS</sub> =0 V			1.5	V	
Drain-Source Diode Forward Current	Is				90	Α	

Note: 1. Pulse width limited by maximum junction temperature

- 2. Pulse Test: Pulse Width < 300µs, Duty Cycle < 2%.
- 3. Guaranteed by design, not subject to production testing.
- 4. L = 0.5mH,  $I_{AS}$  = 35A,  $V_{DD}$  = 25V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C.

#### ■ TEST CIRCUIT AND WAVEFORM





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