

UNISONIC TECHNOLOGIES CO., LTD

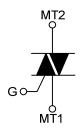
UT136E Preliminary TRIAC

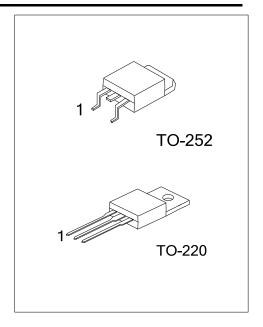
TRIAC

■ DESCRIPTION

Passivated, sensitive gate triacs in a plastic envelope, suitable for surface mounting, intended for use in general purpose bidirectional switching and phase control applications, where high sensitivity is required in all four quadrants.

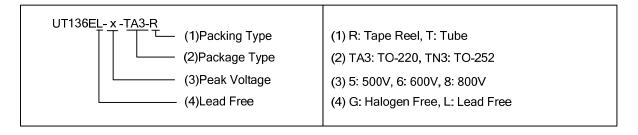
■ SYMBOL





ORDERING INFORMATION

Ordering	Doolsogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing
UT136EL-x-TA3-T	UT136EG-x-TA3-T	TO-220	MT1	MT2	G	Tube
UT136EL-x-TN3-R	UT136EG-x-TN3-R	TO-252	MT1	MT2	G	Tape Reel
UT136EL-x-TN3-T	UT136EG-x-TN3-T	TO-252	MT1	MT2	G	Tube



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
		UT136E-5		500 (Note 2)	V
Repetitive Peak Off-State Voltages		UT136E-6	V_{DRM}	600 (Note 2)	V
		UT136E-8		800	V
RMS On-State Current	(full sine wave, T	_{MB} ≤107°C)	I _{T(RMS)}	4	Α
Non-Repetitive Peak On-State Current t =20ms			I _{TSM}	25	Α
(Full sine wave; T _J =25°C prior to surge) t =16.7ms				27	Α
I ² t for fusing (t =10ms)			l ² t	3.1	A^2s
Departitive Date of Disc	I _{TM} =6A, I _G =0.2A, dI _G /dt=0.2A/μs	T2+ G+	dl _⊤ /dt	50	A/μs
		T2+ G-		50	A/µs
		T2- G-		50	A/μs
Arter ringgering	ui _G /uι=υ.ΖΑ/μS	T2- G+		10	A/μs
Peak Gate Voltage		V_{GM}	5	V	
Peak Gate Current			I_{GM}	2	Α
Peak Gate Power			P_{GM}	5	W
Average Gate Power (over any 20 ms period)		$P_{G(AV)}$	0.5	W	
Junction Temperature			T_J	125	°C
Storage Temperature			T _{STG}	-40 ~ +150	°C

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. Although not recommended, off-state voltages up to 800V may be applied without damage, but the traic may switch to the on-state. The rate of rise of current should not exceed 3A/µs.

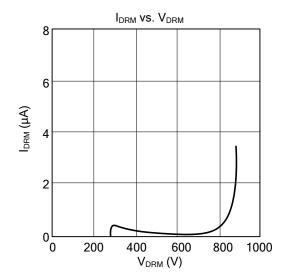
■ THERMAL DATA

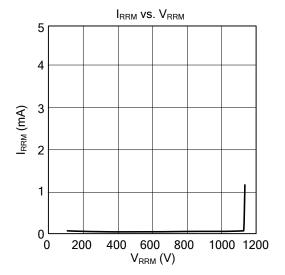
PARAMETER			SYMBOL	RATINGS	UNIT	
Junction to Ambient	Pcb Mounted	TO-220	θ_{JA}	60	IZ/\A/	
		TO-252		75	K/W	
Lucation to Mounting Dans		Full Cycle	0	3.0	IZ /\ A /	
Junction to Mounting B	ase	Half Cycle	θ_{JB}	3.7	K/W	

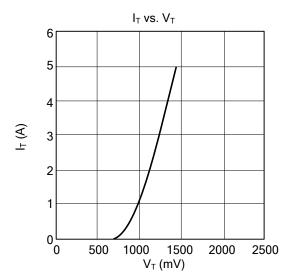
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT		
STATIC									
Gate Trigger Current	I _{GT}		T2+ G+		2.5	10	mA		
		V _D =12V, I _T =0.1A	T2+ G-		4.0	10	mA		
			T2- G-		5.0	10	mA		
			T2- G+		11	25	mA		
Latching Current	IL		T2+ G+		3.0	15	mA		
		\/ -40\/ L -0.4A	T2+ G-		10	20	mA		
		V _D =12V, I _{GT} =0.1A	T2- G-		2.5	15	mA		
			T2- G+		4.0	20	mA		
Holding Current	I _H	V _D =12V, I _{GT} =0.1A			2.2	15	mA		
On-State Voltage	V_{T}	I _T =5A			1.4	1.7	٧		
Out Time William	V _{GT}	$V_D=12V, I_T=0.1A$			0.7	1.5	V		
Gate Trigger Voltage		V _D =400V, I _T =0.1A, T _J =125°C		0.25	0.4		V		
Off-State Leakage Current	I _D	V _D =V _{DRM(MAX)} , T _J =125°C			0.1	0.5	mA		
DYNAMIC									
Critical Rate of Rise of Off-State Voltage	dV _D /dt	V _{DM} =67%V _{DRM(max)} , T _J =125°C, exponential waveform; gate open circuit			50		V/µs		
Gate Controlled Turn-On Time	t _{GT}	I _{TM} =6A, V _D =V _{DRM(MAX)} , I _G =0.1A, dI _G /dt=5A/μs			2		μs		

■ TYPICAL CHARACTERISTICS







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