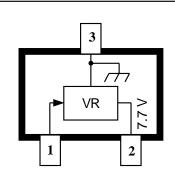
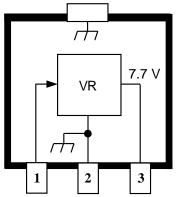
# 8178

#### **PRODUCT PREVIEW**

(Subject to change without notice)
April 5, 1996





Dwg. PD-0xx

#### **ABSOLUTE MAXIMUM RATINGS**

| Input Voltage, V <sub>I</sub>            | 30 V |
|--|------|
| Reverse Input Voltage, V <sub>RI</sub> 1 | 20 V |
| Output Current, I <sub>O</sub>           | TBD* |
| Operating Temperature Range,             |      |
| Output Current, I <sub>O</sub>           |      |

 $T_S$  ..... -40°C to +170°C

\* Output current rating is limited by input voltage, duty cycle, and ambient temperature. Under any set of conditions, do not exceed a junction temperature of +150°C. See next page.

## LOW-DROPOUT, 7.7 V PREREGULATOR

Designed specifically to meet the stringent requirements of automotive applications, the A8178LLR and A8178LLT provide an output voltage of 7.7 V  $\pm$ 15% for supply voltages greater than 6.9 V. They also provide a low-dropout tracking output for supply voltages down to 2.5 V. These regulators are intended to be used with sensitive automotive electronics systems that do not have the capability of low dropout operation and cannot withstand the high-voltage transients typical of automotive applications. These 7.7 V preregulators will withstand all ISO pulses (ignition coil disconnect requires an external diode) and will continue to operate during positive power supply transients (noise).

These devices are supplied in small-outline plastic transistor packages for surface-mount applications. The A8178LLR is supplied in the minimum footprint SOT-23/TO-236AB; the A8178LLT is furnished in the SOT-89/TO-243AA for increased allowable package power dissipation.

This document contains information on a product under development. Allegro MicroSystems, Inc. reserves the right to change or discontinue this product without notice.

Always order by complete part number:

**A8178LLT** 



### **ELECTRICAL CHARACTERISTICS** over operating temperature range.

|                           |                           |   | Limits               |      |      |       |
|---------------------------|---------------------------|---|----------------------|------|------|-------|
| Characteristic            | Symbol                    | Test Conditions   | Min.                 | Тур. | Max. | Units |
| Output Voltage            | V <sub>o</sub>            | 6.9 V ≤ V <sub>I</sub> ≤ 24 V                                   | 6.5                  | 7.7  | 8.8  | V     |
|                           |                           | 2.5 V ≤ V <sub>I</sub> ≤ 6.9 V                                  | V <sub>I</sub> - 0.4 | _    | _    | V     |
|                           |                           | V <sub>I</sub> = 120 V, pulse test                              | _                    | _    | 11   | V     |
| Output Volt. Temp. Coeff. | $\alpha_{ m VO}$          |   | _                    | _    | TBD  | mV/°C |
| Line Regulation           | $\Delta V_{O(\Delta VI)}$ |   | _                    | _    | TBD  | mV    |
| Load Regulation           | $\Delta V_{O(\Delta IO)}$ |   | _                    | _    | TBD  | mV    |
| Quiescent Current         | I <sub>Q</sub>            | $V_1 = 13.5 \text{ V}, I_0 = 0$                                 | _                    | _    | 2.5  | mA    |
|                           |                           | $2.5 \text{ V} \le \text{V}_1 \le 8 \text{ V}, \text{ I}_0 = 0$ | _                    | _    | 5.6  | mA    |
| Power Dissipation         | P <sub>d</sub>            | V <sub>I</sub> = 13.5 V, I <sub>O</sub> = 15 mA                 | _                    | _    | 130  | mW    |
| Thermal Resistance*       | $R_{\theta JA}$           | A8178LLR  | _                    | 575  | _    | °C/W  |
|                           |                           | A8178LLT  | _                    | 200  | _    | °C/W  |

Typical values are at  $T_A$  = +25°C and are given for circuit design information only.

#### **INTERNAL PROTECTIVE FEATURES**

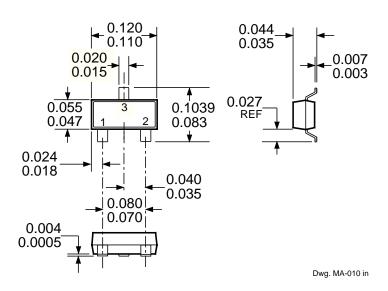
| ISO<br>Pulse No. | Test   | Test Conditions (at $T_A = +25^{\circ}C$ )   |
|------------------|--|--|
| i dise ito.      | 1631   | Test conditions (at 1 A = +23 G)   |
| 1                | Inductive Turn Off (Negative)                      | $V_S$ = -100 V, $R_S$ = 10 $\Omega$ , $t_r$ = 1 $\mu s$ , $t_d$ = 2 ms                     |
| 2                | Inductive Turn Off (Positive)                      | $V_S$ = 100 V, $R_S$ = 10 $\Omega$ , $t_r$ = 1 $\mu$ s, $t_d$ = 50 $\mu$ s, non-operating  |
| 3a               | Capacitive/Inductive Coupling (Neg)                | $V_S$ = -150 V, $R_S$ = 50 $\Omega$ , $t_r$ = 50 ns, $t_d$ = 100 ns                        |
| 3b               | Capacitive/Inductive Coupling (Pos)                | $V_S = 100 \text{ V}, R_S = 50 \Omega, t_r = 50 \text{ ns}, t_d = 100 \text{ ns}$          |
| 4                | Reverse Battery                                    | $V_S = -14 \text{ V}, t_d = 20 \text{ s}, \text{ non-operating}$                           |
| 5                | Load Dump  | $V_{S} = 86.5 \text{ V}, R_{S} = 0.5 \Omega, t_{r} = 5 \text{ ms}, t_{d} = 400 \text{ ms}$ |
| 6                | Ignition Coil Disconnect EXTERNAL PROTECTION REQ'D | $V_S$ = -300 V, $R_S$ = 30 $\Omega$ , $t_r$ = 60 $\mu s$ , $t_d$ = 300 $\mu s$             |
| 7                | Field Decay (Negative)                             | $V_S$ = -80 V, $R_S$ = 10 $\Omega$ , $t_r$ = 5 ms, $t_d$ = 100 ms                          |



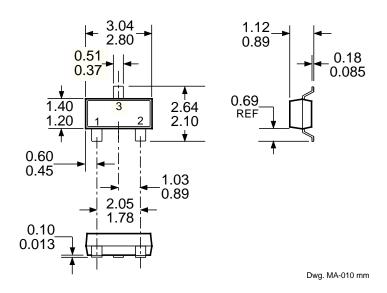
<sup>\*</sup>Mounted on 2.24" x 2.24" solder-coated copper-clad board in still air.

#### **A8178LLR**

Dimensions in Inches (for reference only)



# Dimensions in Millimeters (controlling dimensions)

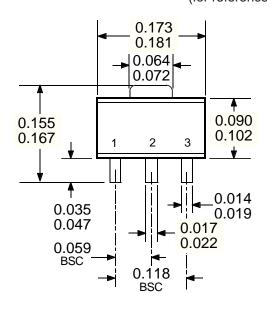


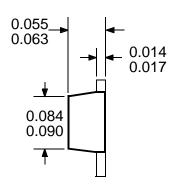
NOTES: 1. Lead spacing tolerance is non-cumulative.

2. Exact body and lead configuration at vendor's option within limits shown.

### 8178 LOW-DROPOUT, 7.7 V PREREGULATOR

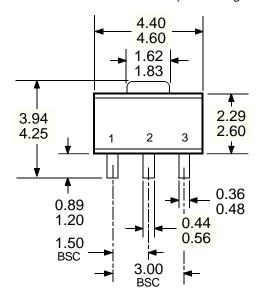
**A8178LLT**Dimensions in Inches (for reference only)

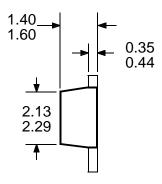




Dwg. MA-009-3 in

# Dimensions in Millimeters (controlling dimensions)





Dwg. MA-009-3 mm

NOTES: 1. Lead spacing tolerance is non-cumulative.

2. Exact body and lead configuration at vendor's option within limits shown.

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