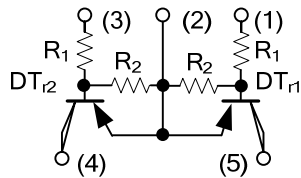


**UA11J**

Preliminary

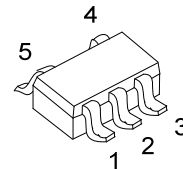
DUAL TRANSISTOR**EMITTER COMMON
(DUAL DIGITAL TRANSISTORS)**■ **FEATURES**

- * Two DTA143Z Chips in a SOT-353 Package.
- * Mounting Cost and Area can be Cut in Half.
- * Epitaxial Planar Type
- * PNP Silicon Transistor(Built-In Resistor Type)

■ **EQUIVALENT CIRCUIT**

$$R_1=4.7k\Omega$$

$$R_2=47k\Omega$$



SOT-353

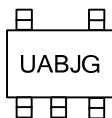
■ **ORDERING INFORMATION**

Ordering Number	Package	Pin Assignment					Packing
		1	2	3	4	5	
UA11JG-AL5-R	SOT-353	B1	E1,E2	B2	C2	C1	Tape Reel

UA11JG-AL5-R

- (1) Packing Type
- (2) Package Type
- (3) Halogen Free

- (1) R: Tape Reel
- (2) AL5: SOT-353
- (3) G: Halogen Free

■ **MARKING**

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{CC}	-50	V
Input Voltage	V_{IN}	-30 ~ +5	V
Output Current	I_{OUT}	-100	mA
Collector Current	$I_{C(MAX)}$	-100	mA
Total Power Dissipation (Note1)	P_D	150	mW
Junction Temperature	T_J	+150	°C
Storage Temperature	T_{STG}	-40 ~ +150	°C

Note 1. 120mW per element must not be exceeded.

2. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	$V_{IN(OFF)}$	$V_{CC}=-5V, I_{OUT}=-100\mu A$			-0.5	V
	$V_{IN(ON)}$	$V_{OUT}=-0.3V, I_{OUT}=-5mA$	-3			
Output Voltage	$V_{OUT(ON)}$	$I_{OUT}/I_{IN}=-5mA/-0.25mA$		-0.1	-0.3	V
Input Current	I_{IN}	$V_{IN}=-5V$			-1.8	mA
Output Current	$I_{OUT(OFF)}$	$V_{CC}=-50V, V_{IN}=0V$			-0.5	μA
DC Current Gain	h_{FE}	$V_{OUT}=-5V, I_{OUT}=-10mA$	80			
Transition frequency of the device	f_T	$V_{CE}=-10V, I_E=-5mA, f=100MHz$		250		MHz
Input Resistance	R_1		3.29	4.7	6.11	K Ω
Resistance Ratio	R_2/R_1		8	10	12	

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