

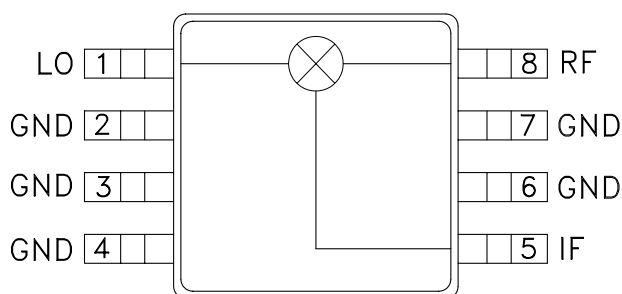
GaAs MMIC SMT DOUBLE-BALANCED MIXER, 0.7 - 2.0 GHz

Typical Applications

The HMC208MS8 is ideal for:

- Base Stations
- PCMCIA Transceivers
- Cable Modems
- Portable Wireless

Functional Diagram



Features

Ultra Small Package: MSOP8

Conversion Loss: 9 dB

LO / RF Isolation: 24 dB

IP3 (Input): +17 dBm

General Description

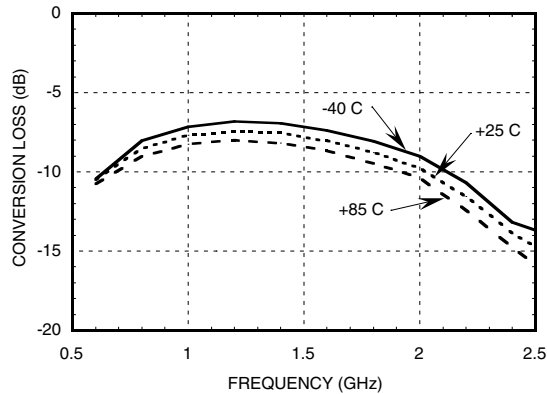
The HMC208MS8 is an ultra miniature double-balanced mixer in an 8 lead plastic surface mount package (MSOP). This passive MMIC mixer is constructed of GaAs Schottky diodes and novel planar transformer baluns on the chip. The device can be used as an upconverter, downconverter, biphas (de)modulator, or phase comparator. The consistent MMIC performance will improve system operation and assure regulatory compliance.

Electrical Specifications, $T_A = +25^\circ \text{C}$, As a Function of LO Drive

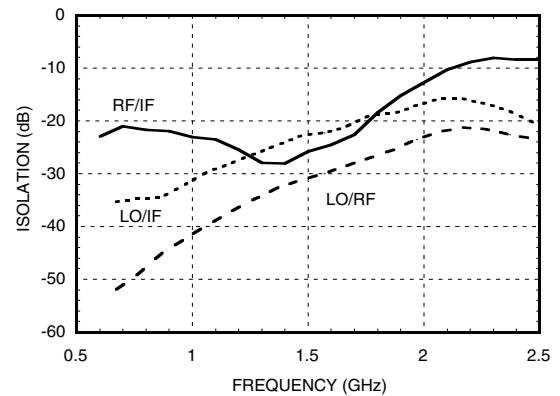
Parameter	LO = +13 dBm IF = 70 MHz			LO = +10 dBm IF = 70 MHz			Units
	Min.	Typ.	Max.	Min.	Typ.	Max.	
Frequency Range, RF & LO	0.7 - 2.0			0.8 - 1.2			GHz
Frequency Range, IF	DC - 0.5			DC - 0.5			GHz
Conversion Loss		9	10.5		8.5	10.5	dB
Noise Figure (SSB)		9	10.5		8.5	10.5	dB
LO to RF Isolation	20	24		32	40		dB
LO to IF Isolation	13	17		22	30		dB
RF to IF Isolation	10	14		17	22		dB
IP3 (Input)	13	17		12	16		dBm
1 dB Gain Compression (Input)	7	10		5	8		dBm

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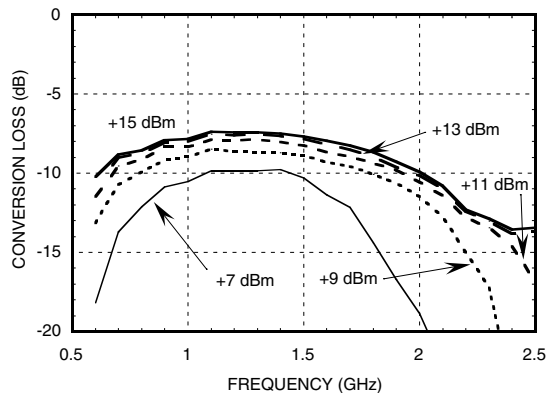
Conversion Loss vs Temperature @ LO = +13 dBm



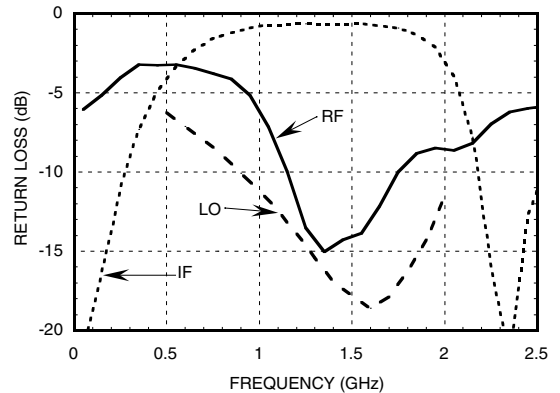
Isolation @ LO = +13 dBm



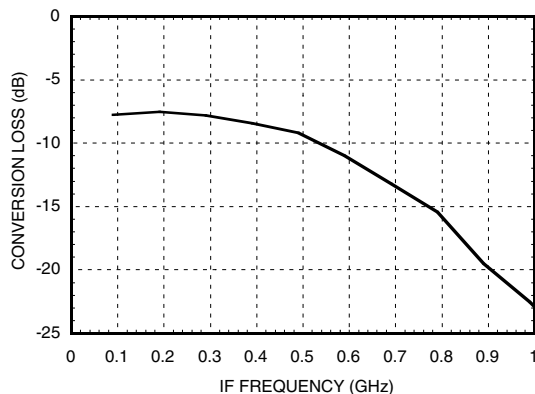
Conversion Loss vs. LO Drive



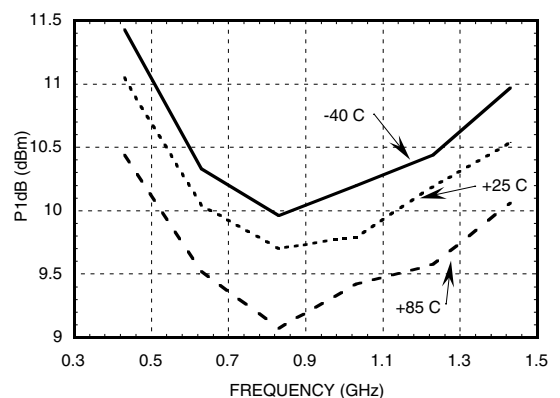
Return Loss @ LO = +13 dBm



IF Bandwidth @ LO = +13 dBm

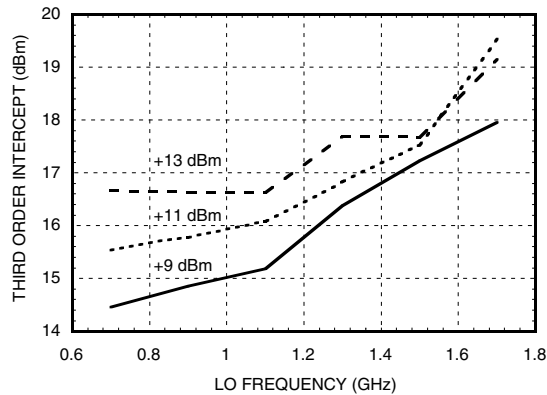


P1dB vs. Temperature @ LO = +13 dBm

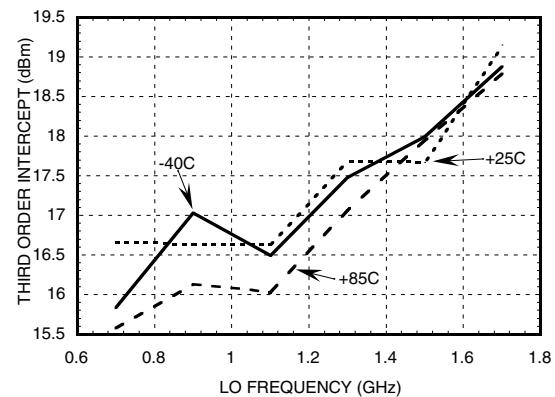


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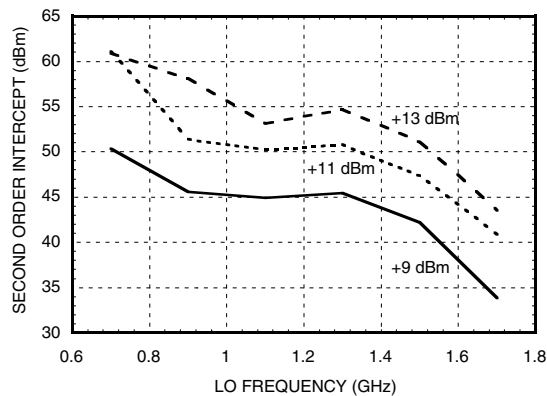
Input IP3 vs. LO Drive



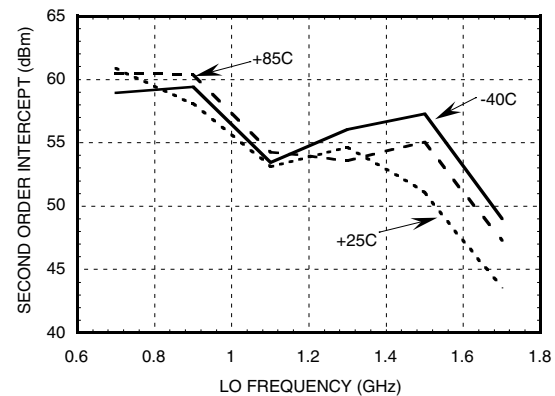
Input IP3 vs. Temperature @ LO = +13 dBm



Input IP2 vs. LO Drive



Input IP2 vs. Temperature @ LO = +13 dBm



MxN Spurious Outputs

	nLO				
mRF	0	1	2	3	4
0	xx	2	25	17	45
1	16	0	52	38	27
2	60	55	72	62	77
3	65	96	88	64	91
4	>105	>105	>105	100	>105

RF = 0.9 GHz @ -10 dBm
LO = 0.97 GHz @ +13 dBm
All values in dBc relative to the IF

Harmonics of LO

LO Freq. (GHz)	nLO Spur at RF Port			
	1	2	3	4
0.7	50	40	47	61
0.9	46	54	56	71
1.1	39	54	51	72
1.3	34	53	51	90
1.5	31	50	65	87
1.7	28	50	72	82

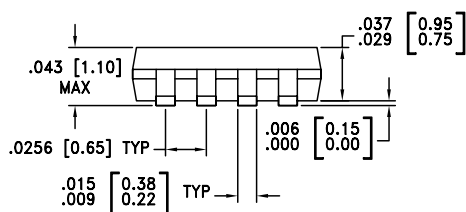
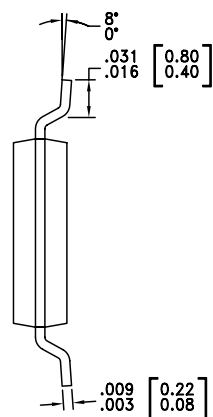
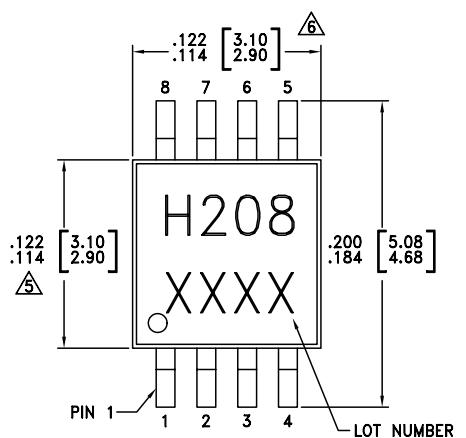
LO = +13 dBm
Values in dBc below input LO level measured at RF Port.

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Absolute Maximum Ratings

RF / Input	+13 dBm
LO Drive	+27 dBm
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

Outline Drawing

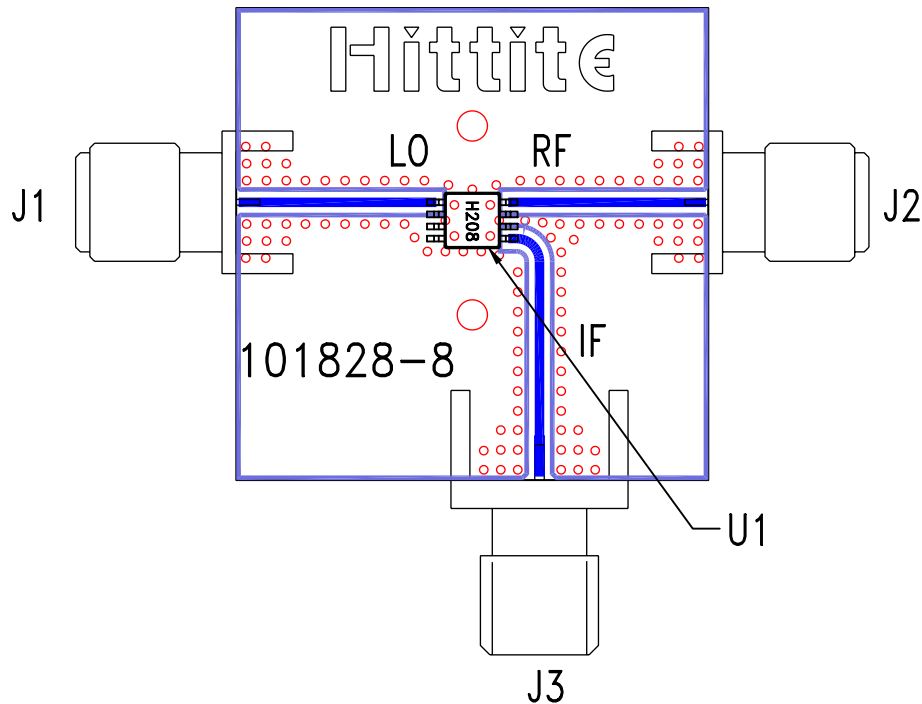


NOTES:

1. PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.
2. LEADFRAME MATERIAL: COPPER ALLOY
3. LEADFRAME PLATING: Sn/Pb SOLDER
4. DIMENSIONS ARE IN INCHES [MILLIMETERS].
5. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
6. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
7. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

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Evaluation PCB



List of Material

Item	Description
J1 - J3	PC Mount SMA RF Connector
U1	HMC208MS8 Mixer
PCB*	101828 Evaluation Board
* Circuit Board Material: Rogers 4350	

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads should be connected directly to the ground plane similar to that shown. A sufficient number of VIA holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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Notes: