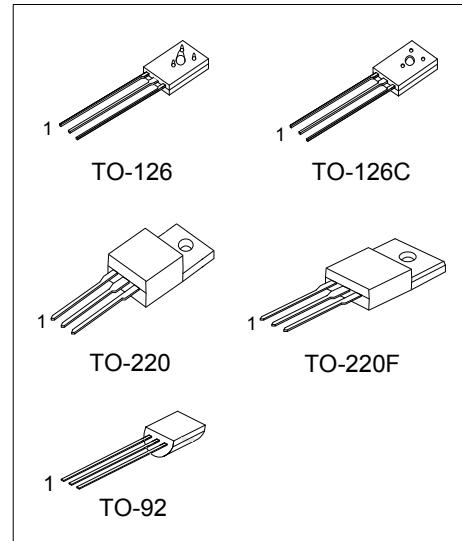


## 78MXX

## LINEAR INTEGRATED CIRCUIT

## 3-Terminal 0.5A POSITIVE VOLTAGE REGULATOR



## ■ DESCRIPTION

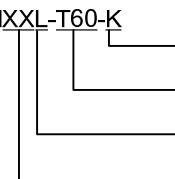
The UTC **78MXX** family is monolithic fixed voltage regulator integrated circuit. They are suitable for applications that required supply current up to 0.5A.

## ■ FEATURES

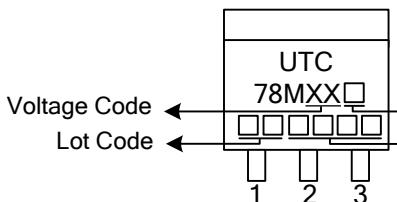
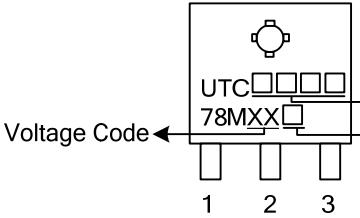
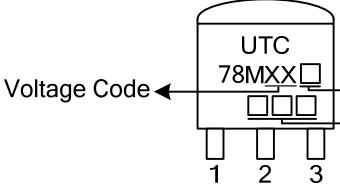
- \* Output Current up to 0.5A
- \* Fixed Output Voltage of 5V, 6V, 8V, 9V, 12V, 15V and 18V Available
- \* Thermal Overload Shutdown Protection
- \* Short Circuit Current Limiting
- \* Output Transistor SOA Protection

## ■ ORDERING INFORMATION

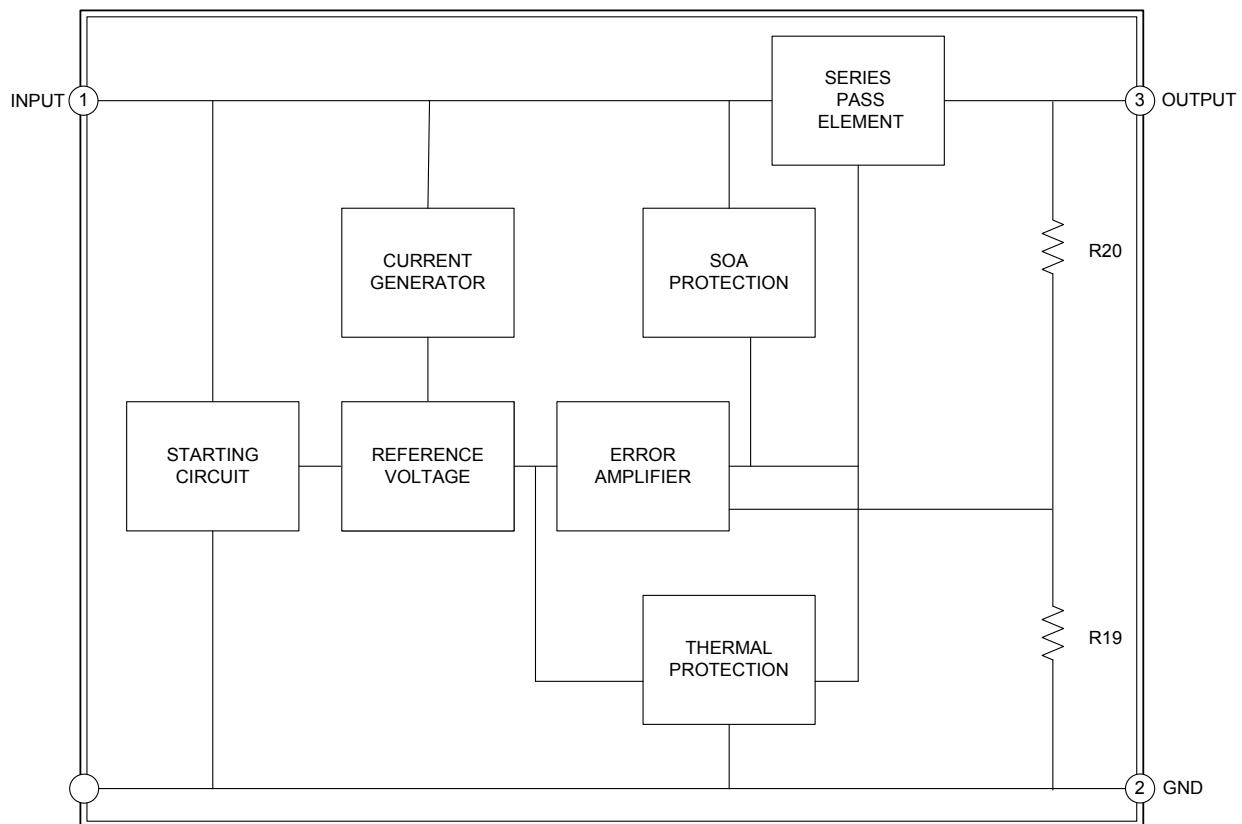
Ordering Number			Package	Pin Assignment			Packing
Normal	Lead Free	Halogen Free		1	2	3	
78MXX-T60-K	78MXXL-T60-K	78MXXG-T60-K	TO-126	I	G	O	Bulk
78MXX-T6C-K	78MXXL-T6C-K	78MXXG-T6C-K	TO-126C	I	G	O	Bulk
78MXX-T92-B	78MXXL-T92-B	78MXXG-T92-B	TO-92	O	G	I	Tape Box
78MXX-T92-K	78MXXL-T92-K	78MXXG-T92-K	TO-92	O	G	I	Bulk
78MXX-TA3-T	78MXXL-TA3-T	78MXXG-TA3-T	TO-220	I	G	O	Tube
78MXX-TF3-T	78MXXL-TF3-T	78MXXG-TF3-T	TO-220F	I	G	O	Tube

 78MXXL-T60-K	(1) B: Tape Box, K: Bulk, T: Tube
	(2) T60: TO-126, T6C: TO-126C, T92: TO-92,
	TA3: TO-220, TF3: TO-220F
	(3) G: Halogen Free, L: Lead Free, Blank: Pb/Sn
(4) xx: refer to Marking Information	

## ■ MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
TO-220 TO-220F		 <p>Voltage Code ← Lot Code ← Date Code →</p> <p>L: Lead Free G: Halogen Free</p>
TO-126 TO-126C	05: 5V 06: 6V 08: 8V 09: 9V 12: 12V 15: 15V 18: 18V	 <p>Voltage Code ← Date Code → L: Lead Free G: Halogen Free</p>
TO-92		 <p>Voltage Code ← Date Code → L: Lead Free G: Halogen Free</p>

## ■ BLOCK DIAGRAM



## ■ ABSOLUTE MAXIMUM RATINGS

(Operating temperature range applies unless otherwise specified)

PARAMETER	SYMBOL	RATING	UNIT
Input Voltage	V <sub>IN</sub>	35	V
Output Current	I <sub>OUT</sub>	0.5	A
Power Dissipation	P <sub>D</sub>	Internally Limited	W
Operating Junction Temperature	T <sub>OPR</sub>	-40 ~ +85	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ ELECTRICAL CHARACTERISTICS

### FOR UTC78M05

(Refer to the test circuits, T<sub>MIN</sub>≤T<sub>J</sub>≤125°C, I<sub>OUT</sub>=350mA, V<sub>IN</sub>=10V, unless otherwise specified, Ci=0.33μF, Co=0.1μF)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V <sub>OUT</sub>	T <sub>J</sub> =25°C	4.80	5.0	5.20	V
		I <sub>OUT</sub> =5mA ~ 350mA V <sub>IN</sub> =7 ~ 20V	4.75		5.25	V
Load Regulation	ΔV <sub>OUT</sub>	T <sub>J</sub> =25°C, I <sub>OUT</sub> =5mA~0.5A			100	mV
		T <sub>J</sub> =25°C, I <sub>OUT</sub> =5mA~200mA			50	mV
Line regulation	ΔV <sub>OUT</sub>	V <sub>IN</sub> =7 ~ 25V, T <sub>J</sub> =25°C, I <sub>OUT</sub> =200mA			100	mV
		V <sub>IN</sub> =8 ~ 25V, T <sub>J</sub> =25°C, I <sub>OUT</sub> =200mA			50	mV
Quiescent Current	I <sub>Q</sub>	T <sub>J</sub> =25°C		4.0	6.0	mA
Quiescent Current Change	ΔI <sub>Q</sub>	V <sub>IN</sub> =8 ~ 25V, I <sub>OUT</sub> =200mA			0.8	mA
		I <sub>OUT</sub> =5mA~350mA			0.5	mA
Output Noise Voltage	e <sub>N</sub>	10Hz≤f≤100KHz		40		μV
Ripple Rejection	RR	V <sub>IN</sub> =8 ~ 18V, f=120Hz I <sub>OUT</sub> =300mA	62			dB
Peak Output Current	I <sub>PEAK</sub>	T <sub>J</sub> =25°C		700		mA
Short-Circuit Current	I <sub>SC</sub>	V <sub>IN</sub> =35V, T <sub>J</sub> =25°C		300		mA
Dropout Voltage	V <sub>D</sub>	T <sub>J</sub> =25°C, I <sub>OUT</sub> =500mA		2.0		V

### FOR UTC78M06

(Refer to the test circuits, T<sub>MIN</sub>≤T<sub>J</sub>≤125°C, I<sub>OUT</sub>=350mA, V<sub>IN</sub>=11V, unless otherwise specified, Ci=0.33μF, Co=0.1μF)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V <sub>OUT</sub>	T <sub>J</sub> =25°C, I <sub>OUT</sub> =5~350mA	5.76	6.0	6.24	V
		V <sub>IN</sub> =8 ~ 21V, I <sub>OUT</sub> =5mA~350mA	5.70		6.30	V
Load Regulation	ΔV <sub>OUT</sub>	T <sub>J</sub> =25°C, I <sub>OUT</sub> =5mA~0.5A			120	mV
		T <sub>J</sub> =25°C, I <sub>OUT</sub> =5mA~200mA			60	mV
Line regulation	ΔV <sub>OUT</sub>	V <sub>IN</sub> =8V ~ 25V, T <sub>J</sub> =25°C, I <sub>OUT</sub> =200mA			100	mV
		V <sub>IN</sub> =9V ~ 25V, T <sub>J</sub> =25°C, I <sub>OUT</sub> =200mA			50	mV
Quiescent Current	I <sub>Q</sub>	T <sub>J</sub> =25°C		4.0	6	mA
Quiescent Current Change	ΔI <sub>Q</sub>	I <sub>OUT</sub> =5mA ~ 350mA			0.5	mA
		V <sub>IN</sub> =9V ~ 25V, I <sub>OUT</sub> =200mA			0.8	mA
Output Noise Voltage	e <sub>N</sub>	10Hz≤f≤100kHz		45		μV
Ripple Rejection	RR	V <sub>IN</sub> =9V ~ 19V, f=120Hz, T <sub>J</sub> =25°C I <sub>OUT</sub> =300mA	59			dB
Peak Output Current	I <sub>PEAK</sub>	T <sub>J</sub> =25°C		700		mA
Short-Circuit Current	I <sub>SC</sub>	V <sub>IN</sub> =35V, T <sub>J</sub> =25°C		300		mA
Dropout Voltage	V <sub>D</sub>	T <sub>J</sub> =25°C		2.0		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

**FOR UTC78M08**

(Refer to the test circuits,  $T_{MIN} \leq T_J \leq 125^\circ\text{C}$ ,  $I_{OUT}=350\text{mA}$ ,  $V_{IN}=14\text{V}$ , unless otherwise specified,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$T_J = 25^\circ\text{C}$	7.68	8.0	8.32	V
		$V_{IN} = 10.5\text{V} \sim 23\text{V}$ , $I_{OUT}=5\text{mA} \sim 350\text{mA}$	7.60		8.40	V
Load Regulation	$\Delta V_{OUT}$	$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA} \sim 0.5\text{A}$			160	mV
		$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA} \sim 200\text{mA}$			80	mV
Line regulation	$\Delta V_{OUT}$	$V_{IN} = 10.5\text{V} \sim 25\text{V}$			100	mV
		$T_J = 25^\circ\text{C}$ , $I_{OUT}=200\text{mA}$			50	mV
Quiescent Current	$I_Q$	$T_J = 25^\circ\text{C}$		4.0	6	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN} = 10.5\text{V} \sim 25\text{V}$ , $I_{OUT}=200\text{mA}$			0.8	mA
		$I_{OUT}=5\text{mA} \sim 350\text{mA}$			0.5	mA
Output Noise Voltage	$e_N$	$10\text{Hz} \leq f \leq 100\text{KHz}$		52		$\mu\text{V}$
Ripple Rejection	$RR$	$V_{IN} = 11.5\text{V} \sim 21.5\text{V}$ , $f=120\text{Hz}$ , $I_{OUT}=300\text{mA}$	56			dB
Peak Output Current	$I_{PEAK}$	$T_J = 25^\circ\text{C}$		700		mA
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}$ , $T_J = 25^\circ\text{C}$		300		mA
Dropout Voltage	$V_D$	$T_J = 25^\circ\text{C}$ , $I_{OUT}=500\text{mA}$		2.0		V

**FOR UTC78M09**

(Refer to the test circuits,  $T_{MIN} \leq T_J \leq 125^\circ\text{C}$ ,  $I_{OUT}=350\text{mA}$ ,  $V_{IN}=17\text{V}$ , unless otherwise specified,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$T_J = 25^\circ\text{C}$	8.64	9	9.36	V
		$V_{IN} = 12.5\text{V} \sim 25\text{V}$ , $I_{OUT}=5\text{mA} \sim 350\text{mA}$	8.55		9.45	V
Load Regulation	$\Delta V_{OUT}$	$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA} \sim 0.5\text{A}$			200	mV
		$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA} \sim 200\text{mA}$			100	mV
Line regulation	$\Delta V_{OUT}$	$V_{IN} = 12.5\text{V} \sim 25\text{V}$			100	mV
		$T_J = 25^\circ\text{C}$ , $I_{OUT}=200\text{mA}$			50	mV
Quiescent Current	$I_Q$	$T_J = 25^\circ\text{C}$		4.1	6	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN} = 12.5\text{V} \sim 25\text{V}$ , $I_{OUT}=200\text{mA}$			0.8	mA
		$I_{OUT}=5\text{mA} \sim 350\text{mA}$			0.5	mA
Output Noise Voltage	$e_N$	$10\text{Hz} \leq f \leq 100\text{kHz}$		65		$\mu\text{V}$
Ripple Rejection	$RR$	$V_{IN} = 13\text{V} \sim 23\text{V}$ , $f=120\text{Hz}$ $I_{OUT}=300\text{mA}$	55			dB
Peak Output Current	$I_{PEAK}$	$T_J = 25^\circ\text{C}$		700		mA
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}$ , $T_J = 25^\circ\text{C}$		300		mA
Dropout Voltage	$V_D$	$T_J = 25^\circ\text{C}$ , $I_{OUT}=500\text{mA}$		2.0		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

**FOR UTC78M12**

(Refer to the test circuits,  $T_{MIN} \leq T_J \leq 125^\circ\text{C}$ ,  $I_{OUT}=350\text{mA}$ ,  $V_{IN}=19\text{V}$ , unless otherwise specified,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$T_J = 25^\circ\text{C}$	11.52	12.0	12.48	V
		$V_{IN} = 14.5\text{V to } 27\text{V}$ , $I_{OUT}=5\text{mA}\sim 350\text{mA}$	11.40		12.60	V
Load Regulation	$\Delta V_{OUT}$	$T_J = 25^\circ\text{C}, I_{OUT}=5\text{mA}\sim 0.5\text{A}$			240	mV
		$T_J = 25^\circ\text{C}, I_{OUT}=5\text{mA}\sim 2\text{A}$			120	mV
Line regulation	$\Delta V_{OUT}$	$V_{IN} = 14.5\text{V to } 30\text{V}$ $T_J = 25^\circ\text{C}, I_{OUT}=200\text{mA}$			100	mV
		$V_{IN} = 16\text{V to } 30\text{V}$ $T_J = 25^\circ\text{C}, I_{OUT}=200\text{mA}$			50	mV
Quiescent Current	$I_Q$	$T_J = 25^\circ\text{C}$		4.1	6.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN} = 14.5\text{V to } 30\text{V}, I_{OUT}=200\text{mA}$			0.8	mA
		$I_{OUT}=5\text{mA}\sim 350\text{mA}$			0.5	mA
Output Noise Voltage	$e_N$	$10\text{Hz} \leq f \leq 100\text{kHz}$		75		$\mu\text{V}$
Ripple Rejection	$RR$	$V_{IN} = 15\text{V to } 25\text{V}, f=120\text{Hz}$ $I_{OUT}=300\text{mA}$	55			dB
Peak Output Current	$I_{PEAK}$	$T_J = 25^\circ\text{C}$		700		mA
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}, T_J = 25^\circ\text{C}$		300		mA
Dropout Voltage	$V_D$	$T_J = 25^\circ\text{C}, I_{OUT}=500\text{mA}$		2.0		V

**FOR UTC78M15**

(Refer to the test circuits,  $T_{MIN} \leq T_J \leq 125^\circ\text{C}$ ,  $I_{OUT}=350\text{mA}$ ,  $V_{IN}=23\text{V}$ , unless otherwise specified,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$T_J = 25^\circ\text{C}$	14.40	15.0	15.60	V
		$V_{IN} = 17.5\text{V} \sim 30\text{V}$ , $I_{OUT}=5\text{mA}\sim 350\text{mA}$	14.25		15.75	V
Load Regulation	$\Delta V_{OUT}$	$T_J = 25^\circ\text{C}, I_{OUT}=5\text{mA}\sim 0.5\text{A}$			300	mV
		$T_J = 25^\circ\text{C}, I_{OUT}=5\text{mA}\sim 200\text{mA}$			150	mV
Line regulation	$\Delta V_{OUT}$	$V_{IN} = 17.5\text{V} \sim 30\text{V}$ $T_J = 25^\circ\text{C}, I_{OUT}=200\text{mA}$			100	mV
		$V_{IN} = 20\text{V} \sim 30\text{V}$ $T_J = 25^\circ\text{C}, I_{OUT}=200\text{mA}$			50	mV
Quiescent Current	$I_Q$	$T_J=25^\circ\text{C}$		4.1	6.0	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN} = 17.5\text{V} \sim 30\text{V}, I_{OUT}=200\text{mA}$			0.8	mA
		$I_{OUT}=5\text{mA}\sim 350\text{mA}$			0.5	mA
Output Noise Voltage	$e_N$	$10\text{Hz} \leq f \leq 100\text{KHz}$		90		$\mu\text{V}$
Ripple Rejection	$RR$	$V_{IN} = 18.5\text{V} \sim 28.5\text{V}$ $f=120\text{Hz}, I_{OUT}=300\text{mA}$	54			dB
Peak Output Current	$I_{PEAK}$	$T_J = 25^\circ\text{C}$		700		mA
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}, T_J = 25^\circ\text{C}$		300		mA
Dropout Voltage	$V_D$	$T_J = 25^\circ\text{C}, I_{OUT}=500\text{mA}$		2.0		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

**FOR 78M18**

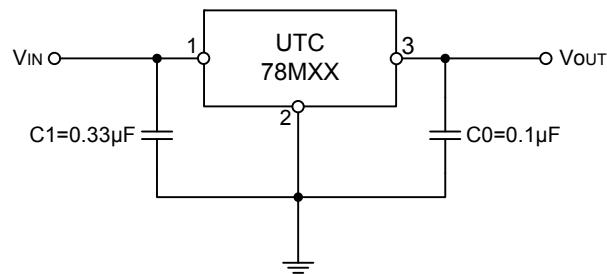
(Refer to the test circuits,  $T_{MIN} \leq T_J \leq 125^\circ\text{C}$ ,  $I_{OUT}=350\text{mA}$ ,  $V_{IN}=26\text{V}$ , unless otherwise specified,  $C_i=0.33\mu\text{F}$ ,  $C_o=0.1\mu\text{F}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_{OUT}$	$T_J = 25^\circ\text{C}$	17.28	18.0	18.72	V
		$V_{IN} = 20.5\text{V}$ to $33\text{V}$ $I_{OUT}=5\text{mA}$ ~ $350\text{mA}$	17.10		18.90	V
Load Regulation	$\Delta V_{OUT}$	$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA}$ ~ $0.5\text{A}$			360	mV
		$T_J = 25^\circ\text{C}$ , $I_{OUT}=5\text{mA}$ ~ $200\text{mA}$			180	mV
Line regulation	$\Delta V_{OUT}$	$V_{IN}=21\text{V}$ to $33\text{V}$ $T_J = 25^\circ\text{C}$ , $I_{OUT} = 200\text{mA}$			100	mV
		$V_{IN} = 24\text{V}$ to $33\text{V}$ , $T_J = 25^\circ\text{C}$ , $I_{OUT} = 200\text{mA}$			50	mV
Quiescent Current	$I_Q$	$T_J = 25^\circ\text{C}$		4.2	6	mA
Quiescent Current Change	$\Delta I_Q$	$V_{IN} = 21\text{V}$ to $33\text{V}$ , $I_{OUT} = 200\text{mA}$			0.8	mA
		$I_{OUT}=5\text{mA}$ ~ $350\text{mA}$			0.5	mA
Output Noise Voltage	$e_N$	$10\text{Hz} \leq f \leq 100\text{KHz}$		100		$\mu\text{V}$
Ripple Rejection	RR	$V_{IN}=22\text{V}$ to $32\text{V}$ , $f=120\text{Hz}$ $I_{OUT} = 300\text{mA}$	53			dB
Peak Output Current	$I_{PEAK}$	$T_J = 25^\circ\text{C}$		700		mA
Short-Circuit Current	$I_{SC}$	$V_{IN}=35\text{V}$ , $T_J = 25^\circ\text{C}$		300		mA
Dropout Voltage	$V_D$	$T_J = 25^\circ\text{C}$		2.0		V

Notes 1. The Maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB. The data above represents pulse test conditions with junction temperatures specified at the initiation of test.

2. Power dissipation < 0.5W

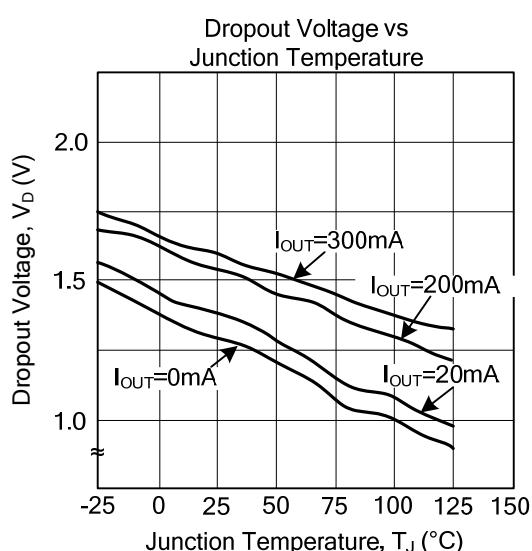
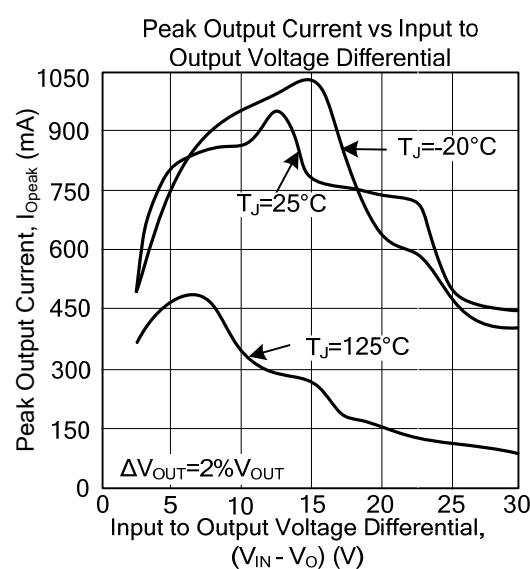
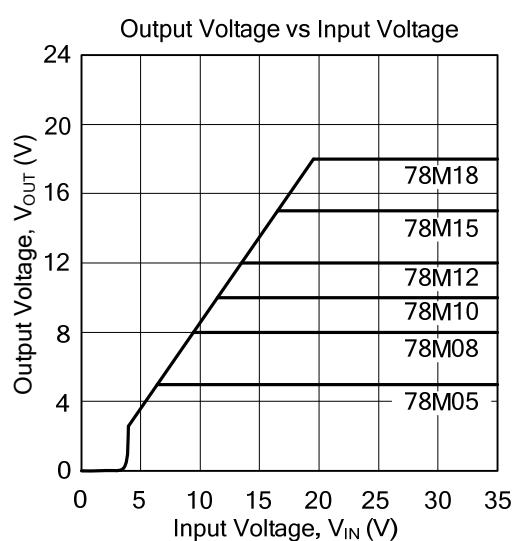
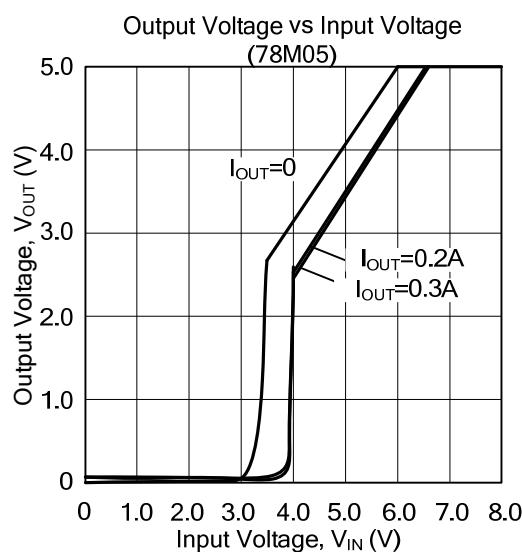
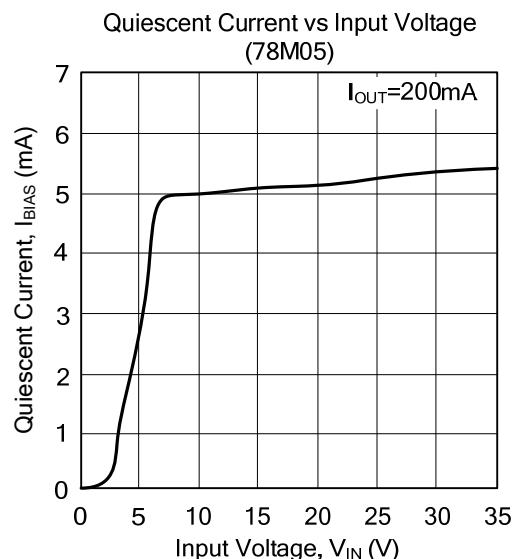
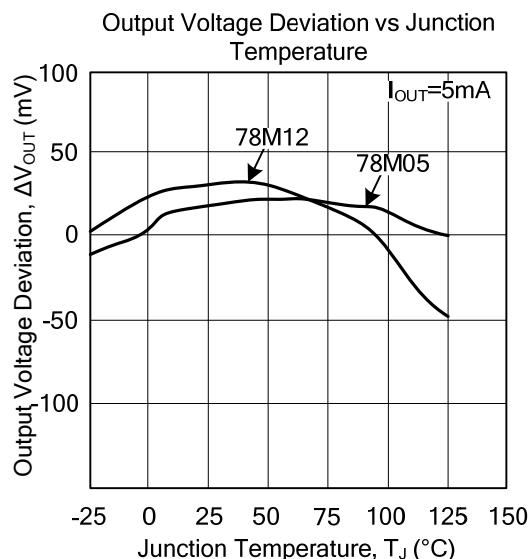
### ■ APPLICATION CIRCUIT



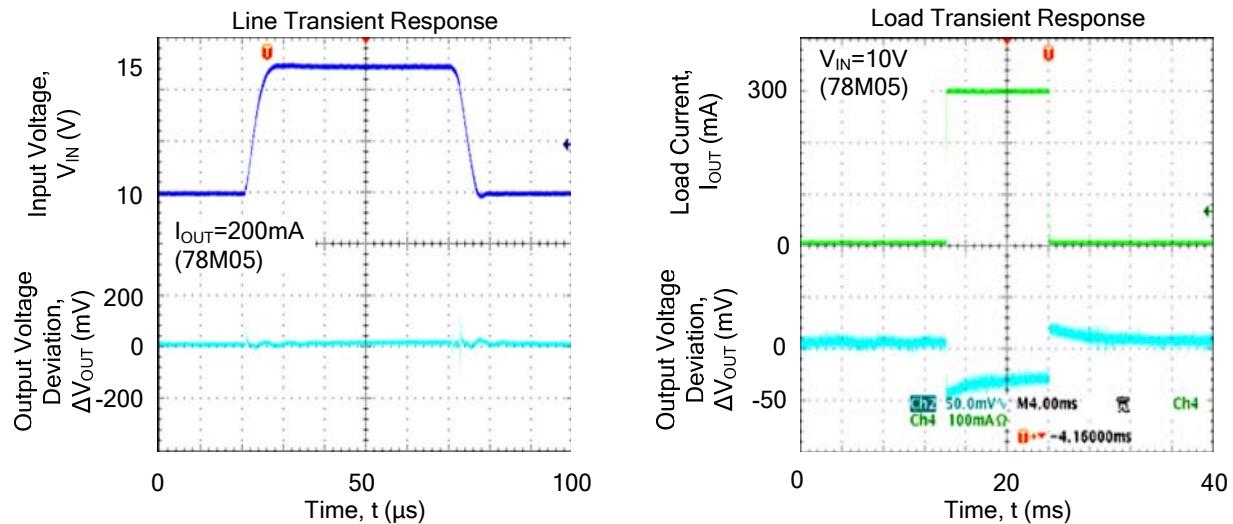
Notes:

1. To specify an output voltage, substitute voltage value for "MXX".
2. Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS(Cont.)



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