



Dual P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY

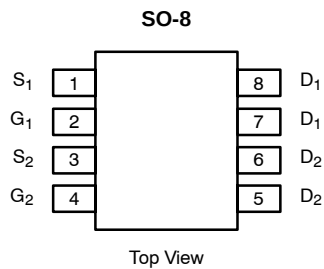
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-20	0.019 @ $V_{GS} = -10$ V	-8.4
	0.031 @ $V_{GS} = -4.5$ V	-6.7

FEATURES

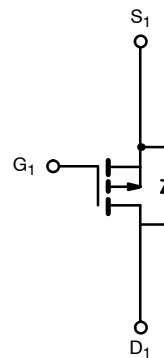
- TrenchFET® Power MOSFET
- 100% R_g Tested

APPLICATIONS

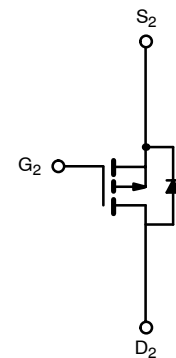
- Load Switching
 - Computer
 - Game Systems
- Battery Switching
 - 2-Cell Li-Ion



Ordering Information: Si4943BDY—E3
Si4943BDY-T1—E3 (with Tape and Reel)



P-Channel MOSFET



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	−20		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	I _D	−8.4	−6.3	A
	T _A = 70°C		−6.7	−5.1	
Pulsed Drain Current		I _{DM}	−30		
Continuous Source Current (Diode Conduction) ^a		I _S	−1.7	−0.9	
Maximum Power Dissipation ^a	T _A = 25°C	P _D	2.0	1.1	W
	T _A = 70°C		1.3	0.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	−55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	46	62.5	$^\circ\text{C/W}$
	Steady State		85	110	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	26	35	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

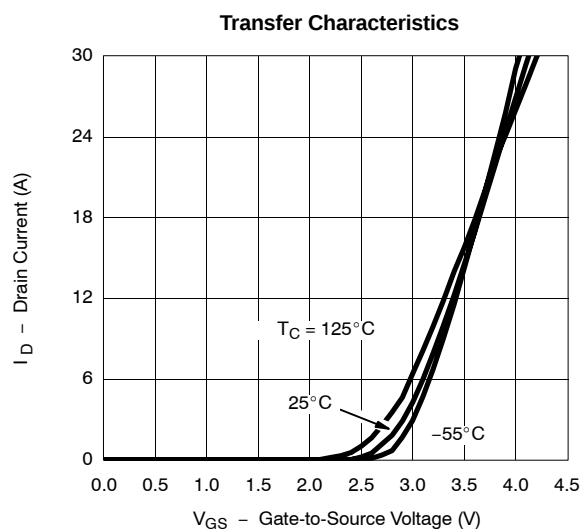
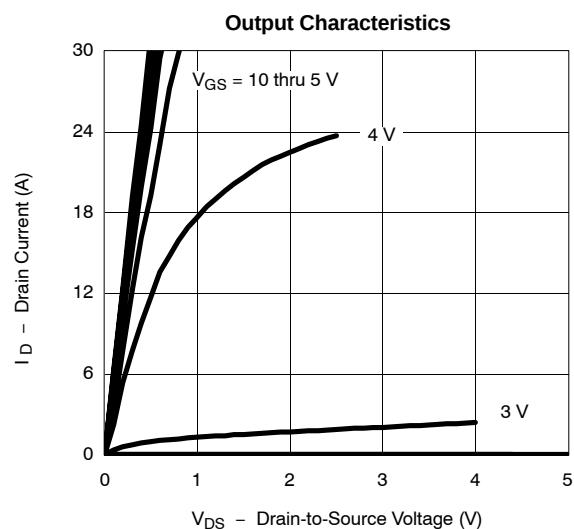
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$	-1		-3	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}, V_{GS} = \pm 20\ \text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\ \text{V}, V_{GS} = 0\ \text{V}$			-1	μA
		$V_{DS} = -20\ \text{V}, V_{GS} = 0\ \text{V}, T_J = 55^\circ\text{C}$			-5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\ \text{V}, V_{GS} = -10\ \text{V}$	-30			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = -10\ \text{V}, I_D = -8.4\ \text{A}$		0.016	0.019	Ω
		$V_{GS} = -4.5\ \text{V}, I_D = -6.7\ \text{A}$		0.026	0.031	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\ \text{V}, I_D = -8.4\ \text{A}$		20		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7\ \text{A}, V_{GS} = 0\ \text{V}$		-0.75	-1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\ \text{V}, V_{GS} = -5\ \text{V}, I_D = -8.4\ \text{A}$		17	25	nC
Gate-Source Charge	Q_{gs}			5		
Gate-Drain Charge	Q_{gd}			6.7		
Gate Resistance	R_g	$f = 1\ \text{MHz}$	6	12	18	Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\ \text{V}, R_L = 10\ \Omega$ $I_D \cong -1\ \text{A}, V_{GEN} = -10\ \text{V}, R_g = 6\ \Omega$		11	17	ns
Rise Time	t_r			10	15	
Turn-Off Delay Time	$t_{d(off)}$			94	140	
Fall Time	t_f			60	90	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -1.7\ \text{A}, di/dt = 100\ \text{A}/\mu\text{s}$		55	80	

Notes

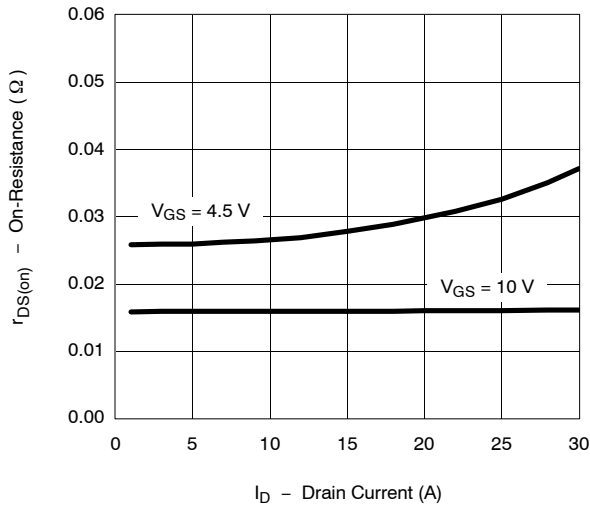
a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

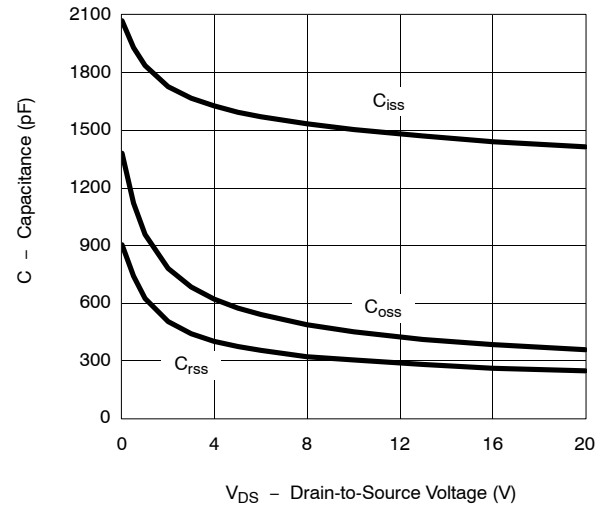
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

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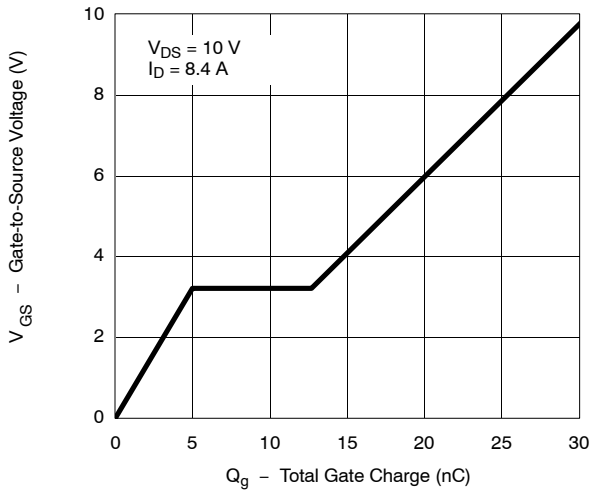
On-Resistance vs. Drain Current



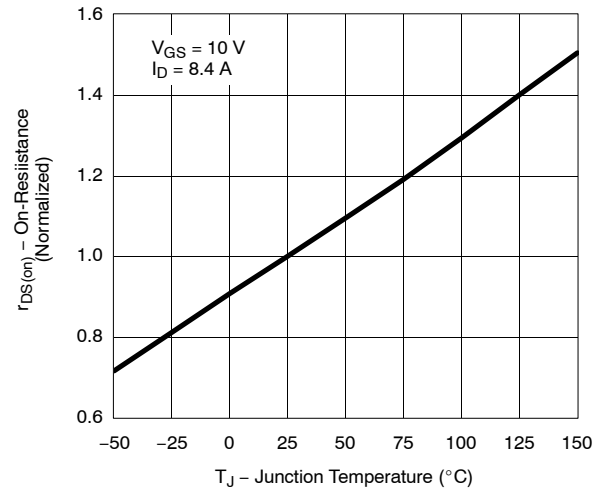
Capacitance



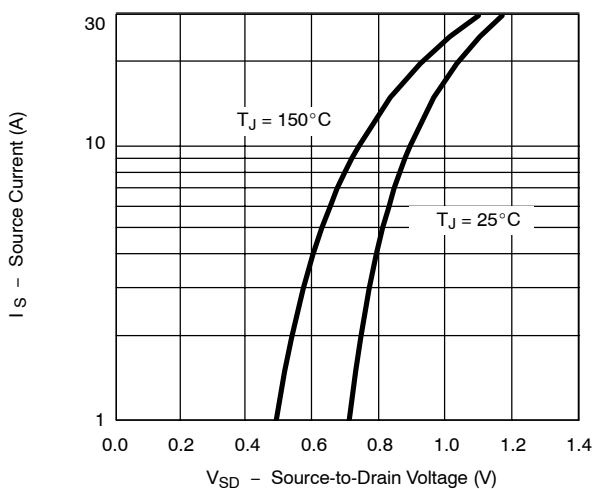
Gate Charge



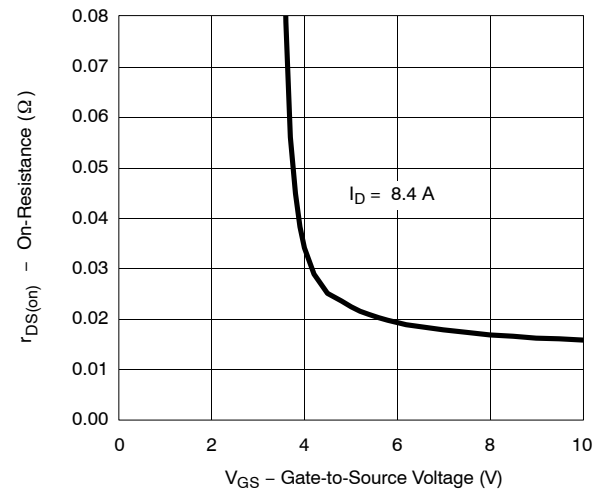
On-Resistance vs. Junction Temperature

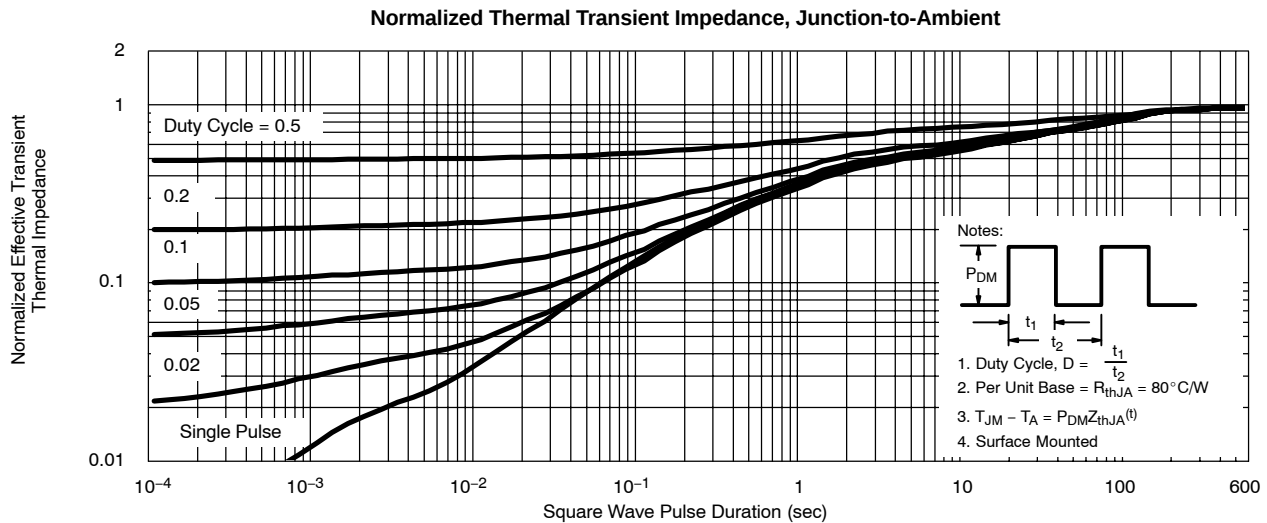
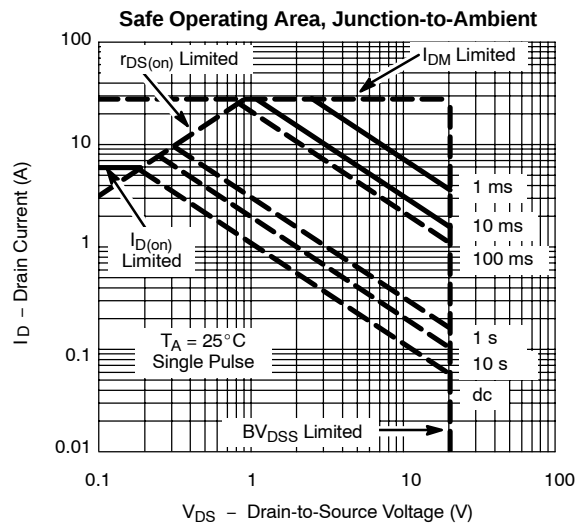
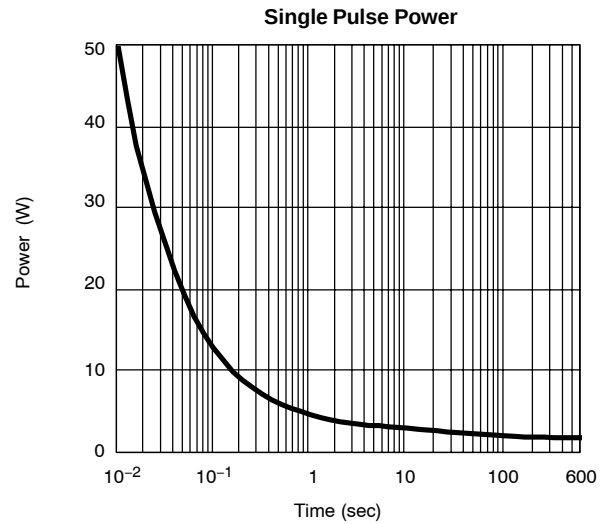
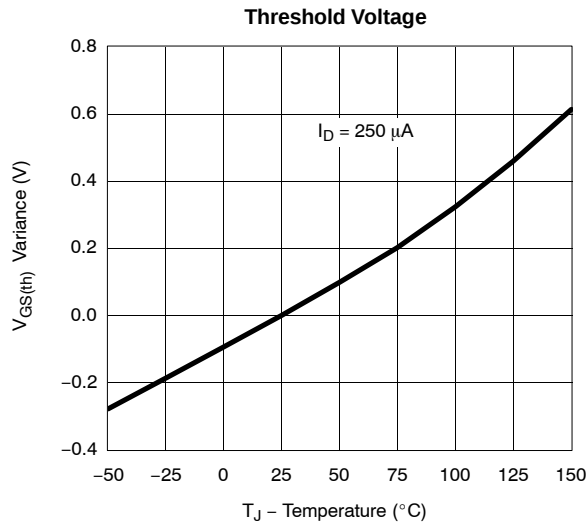


Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)




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