## 2-W MONO AUDIO POWER AMPLIFIER WITH HEADPHONE DRIVE

SLOS277A - JANUARY 2000 - REVISED MARCH 2000

- Ideal for Notebook Computers, PDAs, and **Other Small Portable Audio Devices**
- 2 W Into 4- $\Omega$  From 5-V Supply
- 0.6 W Into 4- $\Omega$  From 3-V Supply
- **Stereo Head Phone Drive**
- Separate Inputs for the Mono (BTL) Signal and Stereo (SE) Left/Right Signals
- **Wide Power Supply Compatibility** 3 V to 5 V
- Meets PC99 Desktop Specs (Target)
- **Low Supply Current** 
  - 11 mA Typical at 5 V
  - 10 mA Typical at 3 V
- Shutdown Control . . . 1 µA Typical
- Shutdown Pin is TTL Compatible
- -40°C to 85°C Operating Temperature Range
- Space-Saving, Thermally-Enhanced MSOP **Packaging**

#### **DGQ PACKAGE** (TOP VIEW) 10 LO/MO MONO-IN □ SHUTDOWN I LIN $V_{DD} \square$ □ GND BYPASS □ ☐ SR/MN RIN 🗆 □ RO/MO

### description

The TPA0223 is a 2-W mono bridge-tied-load (BTL) amplifier designed to drive speakers with as low as 4- $\Omega$ impedance. The amplifier can be reconfigured on-the-fly to drive two stereo single-ended (SE) signals into head phones. This makes the device ideal for use in small notebook computers, PDAs, Digital Personal Audio players, anyplace a mono speaker and stereo head phones are required. From a 5-V supply, the TPA0223 can delivery 2-W of power into a 4- $\Omega$  speaker.

The gain of the input stage is set by the user-selected input resistor and a 50-k $\Omega$  internal feedback resistor  $(A_V = -R_F/R_I)$ . The power stage is internally configured with a gain of -1.25 V/V in SE mode, and -2.5 V/V in BTL mode. Thus, the overall gain of the amplifier is 62.5 k $\Omega$ / R<sub>I</sub> in SE mode and 125 k $\Omega$ / R<sub>I</sub> in BTL mode. The input terminals are high-impedance CMOS inputs, and can be used as summing nodes.

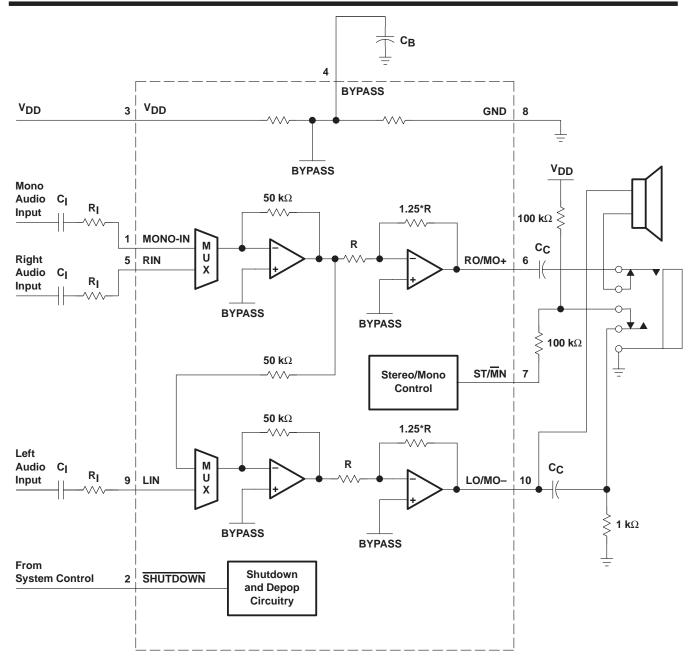
The TPA0223 is available in the 10-pin thermally-enhanced MSOP package (DGQ) and operates over an ambient temperature range of -40°C to 85°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PowerPAD is a trademark of Texas Instruments Incorporated.





### **AVAILABLE OPTIONS**

	PACKAGED DEVICES	MSOP
TA	MSOP† (DGQ)	SYMBOLIZATION
-40°C to 85°C	TPA0223DGQ	AEI

<sup>†</sup>The DGQ package are available taped and reeled. To order a taped and reeled part, add the suffix R to the part number (e.g., TPA0223DGQR).



SLOS277A – JANUARY 2000 – REVISED MARCH 2000

### **Terminal Functions**

TERMINA	\L		DECORPORTION
NAME	NO.	1/0	DESCRIPTION
MONO-IN	1	I	Mono input terminal
SHUTDOWN	2	I	SHUTDOWN places the entire device in shutdown mode when held low. TTL compatible input.
$V_{DD}$	3	I	V <sub>DD</sub> is the supply voltage terminal.
BYPASS	4	I	BYPASS is the tap to the voltage divider for internal mid-supply bias. This terminal should be connected to a $0.1$ - $\mu$ F to $1$ - $\mu$ F capacitor.
RIN	5	I	Right-channel input terminal
RO/MO	6	0	Right-output in SE mode and mono positive output in BTL mode
SR/MN	7	I	Selects between stereo and mono mode. When held high, the amplifier is in SE stereo mode, while held low, the amplifier is in BTL mono mode.
GND	8		Ground terminal
LIN	9	I	Left-channel input terminal
LO/MO	10	0	Left-output in SE mode and mono negative output in BTL mode.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)§

Supply voltage, V <sub>DD</sub>	6 V
Input voltage, V <sub>I</sub>	0.3 V to V <sub>DD</sub> +0.3 V
Continuous total power dissipation	internally limited (see Dissipation Rating Table)
Operating free-air temperature range, T <sub>A</sub> (see Table 3)	–40°C to 85°C
Operating junction temperature range, T <sub>J</sub>	–40°C to 150°C
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 secon	nds 260°C

<sup>§</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### **DISSIPATION RATING TABLE**

PACKAGE	$T_{\mbox{A}} \le 25^{\circ} \mbox{C}$	DERATING FACTOR	T <sub>A</sub> = 70°C	T <sub>A</sub> = 85°C
DGQ	2.14 W¶	17.1 mW/°C	1.37 W	1.11 W

<sup>¶</sup> Please see the Texas Instruments document, PowerPAD Thermally Enhanced Package Application Report (literature number SLMA002), for more information on the PowerPAD package. The thermal data was measured on a PCB layout based on the information in the section entitled Texas Instruments Recommended Board for PowerPAD on page 33 of the before mentioned document.

### recommended operating conditions

			MIN	MAX	UNIT
Supply voltage, V <sub>DD</sub>	Supply voltage, V <sub>DD</sub>			5.5	V
	OT (10)	V <sub>DD</sub> = 3 V	2.7		
High-level input voltage, V <sub>IH</sub>	ST/MN	V <sub>DD</sub> = 5 V	4.5		V
	SHUTDOWN		2		
	ST/MN	V <sub>DD</sub> = 3 V		1.65	
Low-level input voltage, V <sub>IL</sub>		V <sub>DD</sub> = 5 V		2.75	V
	SHUTDOWN			0.8	
Operating free-air temperature, T <sub>A</sub>			-40	85	°C



SLOS277A – JANUARY 2000 – REVISED MARCH 2000

# electrical characteristics at specified free-air temperature, $V_{DD}$ = 3 V, $T_A$ = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
IVool	Output offset voltage (measured differentially)				30	mV
I <sub>DD</sub>	Supply current			10	13	mA
I <sub>DD(SD)</sub>	Supply current, shutdown mode			1	10	μΑ

### operating characteristics, V<sub>DD</sub> = 3 V, T<sub>A</sub> = 25°C, R<sub>L</sub> = 4 $\Omega$

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
PO Output power, see Note 1	THD = 1%,	BTL mode			660		mW	
	Output power, see Note 1	THD = 0.1%,	SE mode,	$R_L = 32 \Omega$		33		mv
THD + N	Total harmonic distortion plus noise	$P_0 = 500 \text{ mW},$	f = 20 Hz to 20 kHz			0.3%		
ВОМ	Maximum output power bandwidth	Gain = 2,	THD = 2%			20		kHz

NOTE 1: Output power is measured at the output terminals of the device at f = 1 kHz.

## electrical characteristics at specified free-air temperature, $V_{DD}$ = 5 V, $T_A$ = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
IVool	Output offset voltage (measured differentially)				30	mV
IDD	Supply current			11	15	mA
I <sub>DD(SD)</sub>	Supply current, shutdown mode			1	10	μΑ

## operating characteristics, V<sub>DD</sub> = 5 V, T<sub>A</sub> = 25°C, R<sub>L</sub> = 4 $\Omega$

	PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
PO	Output power, see Note 1	THD = 1%,	BTL mode			2		W
		THD = 0.1%,	SE mode,	$R_L = 32 \Omega$		95		mW
THD + N	Total harmonic distortion plus noise	P <sub>O</sub> = 1 W,	f = 20 Hz to 20 kHz			0.2%		
ВОМ	Maximum output power bandwidth	Gain = 2.5,	THD = 2%			20		kHz

NOTE 1: Output power is measured at the output terminals of the device at f = 1 kHz.

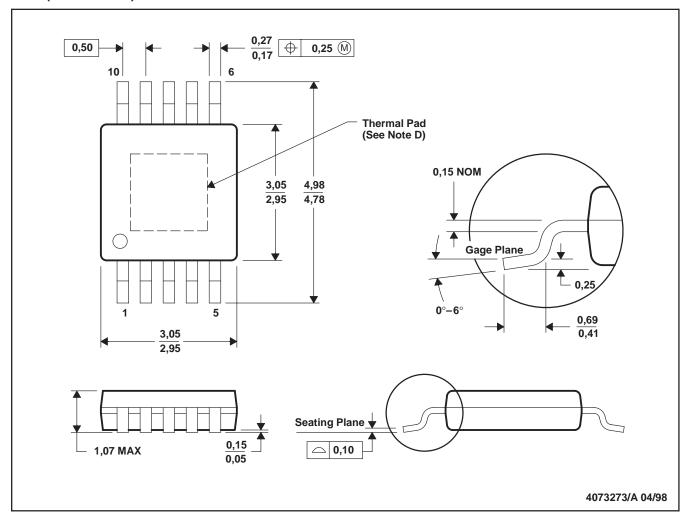


SLOS277A – JANUARY 2000 – REVISED MARCH 2000

### **MECHANICAL DATA**

### DGQ (S-PDSO-G10)

### PowerPAD™ PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. The package thermal performance may be enhanced by bonding the thermal pad to an external thermal plane. This pad is electrically and thermally connected to the backside of the die and possibly selected leads.

PowerPAD is a trademark of Texas Instruments Incorporated.



#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 2000, Texas Instruments Incorporated