

Dual Input, High Speed, Dual Channel Power MOSFET Driver

Features

- Logic AND/NAND input
- 3V and 5V Input compatible
- Clocking speeds up to 10 MHz
- 20 ns Switching/delay time
- 2A Peak drive
- Isolated drains
- Low output impedance
- Low quiescent current
- Wide operating voltage— 4.5V-16V

Applications

- Short circuit protected switching
- Under-voltage shut-down circuits
- Switch-mode power supplies
- Motor controls
- Power MOSFET switching
- Switching capacitive loads
- Shoot-thru protection
- Latching drivers

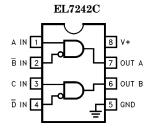
Ordering Information

Part No.	Temp. Range	Pkg.	Outline #
EL7242CN	-40°C to +85°C	8-Pin P-DIP	MDP0031
EL7242CS	-40°C to +85°C	8-Pin SOIC	MDP0027
EL7252CN	-40°C to +85°C	8-Pin P-DIP	MDP0031
EL7252CS	-40°C to +85°C	8-Pin SOIC	MDP0027

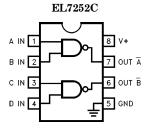
General Description

The EL7242C/EL7252C dual input, 2-channel drivers achieve the same excellent switching performance of the EL7212 family while providing added flexibility. The 2-input logic and configuration is applicable to numerous power MOSFET drive circuits. As with other Elantec drivers, the EL7242C/EL7252C are excellent for driving large capacitive loads with minimal delay and switching times. "Shoot-thru" protection and latching circuits can be implemented by simply "cross-coupling" the 2-channels.

Connection Diagrams



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Manufactured under U.S. Patent Nos. 5,334,883, #5,341,047

TD is 3.1in

EL7242C/EL7252C

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Absolute Maximum Ratings

Supply (V+ to Gnd) 16.5V Operating Junction Temperature 125°C

Input Pins -0.3V to +0.3V above V^+ Power Dissipation

Combined Peak Output Current 4A SOIC 570 mW Storage Temperature Range -65° C to $+150^{\circ}$ C PDIP 1050 mW

Ambient Operating Temperature -40°C to $+85^{\circ}\text{C}$

Important Note:

All parameters having Min/Max specifications are guaranteed. The Test Level column indicates the specific device testing actually performed during production and Quality inspection. Elantec performs most electrical tests using modern high-speed automatic test equipment, specifically the LTX77 Series system. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level Test Procedure

 $\label{eq:local_symmetric} \begin{array}{ll} I & 100\% \text{ production tested and QA sample tested per QA test plan QCX0002.} \\ II & 100\% \text{ production tested at $T_A = 25^\circ$C$ and QA sample tested at $T_A = 25^\circ$C$,} \end{array}$

T_{MAX} and T_{MIN} per QA test plan QCX0002.

III QA sample tested per QA test plan QCX0002.

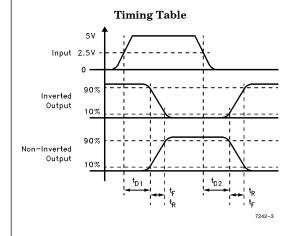
 $\begin{array}{ll} IV & \text{Parameter is guaranteed (but not tested) by Design and Characterization Data.} \\ V & \text{Parameter is typical value at } T_A = 25^{\circ}C \text{ for information purposes only.} \\ \end{array}$

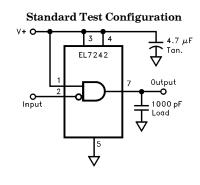
DC Electrical Characteristics $T_A = 25^{\circ}C$, V = 15V unless otherwise specified

Parameter	Description	Test Conditions	Min	Тур	Max	Test Level	Units
Input							
V_{IH}	Logic "1" Input Voltage		2.4			I	v
I _{IH}	Logic "1" Input Current	@V+		0.1	10	I	μΑ
V_{IL}	Logic "0" Input Voltage				0.8	I	v
I _{IL}	Logic "0" Input Current	@0V		0.1	10	I	μΑ
V _{HVS}	Input Hysteresis			0.3		v	v
Output			•				
R _{OH}	Pull-Up Resistance	$I_{OUT} = -100 \text{ mA}$		3	6	I	Ω
R _{OL}	Pull-Down Resistance	$I_{OUT} = +100 \text{ mA}$		4	6	I	Ω
I_{PK}	Peak Output Current	Source Sink		2 2		IV	A
I _{DC}	Continuous Output Current	Source/Sink	100			I	mA
Power Supply	•		•				
I _S	Power Supply Current	Inputs High		1	2.5	I	mA
V _S	Operating Voltage		4.5		16	I	v

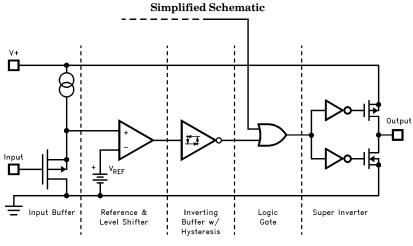
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AC Electrical Characteristics $T_A = 25$ °C, $V = 15$ V unless otherwise specified							
Parameter	Description	Test Conditions	Min	Тур	Max	Test Level	Units
Switching Chara	cteristics						
t _R	Rise Time	$egin{aligned} C_{ m L} &= 500 \ m pF \ C_{ m L} &= 1000 \ m pF \end{aligned}$			10 20	IV	ns
$t_{\mathbf{F}}$	Fall Time	$C_{L} = 500 \text{ pF}$ $C_{L} = 1000 \text{ pF}$			10 20	IV	ns
t _{D-ON}	Turn-On Delay Time			20	25	IV	ns
t _{D-OFF}	Turn-Off Delay Time			20	25	IV	ns

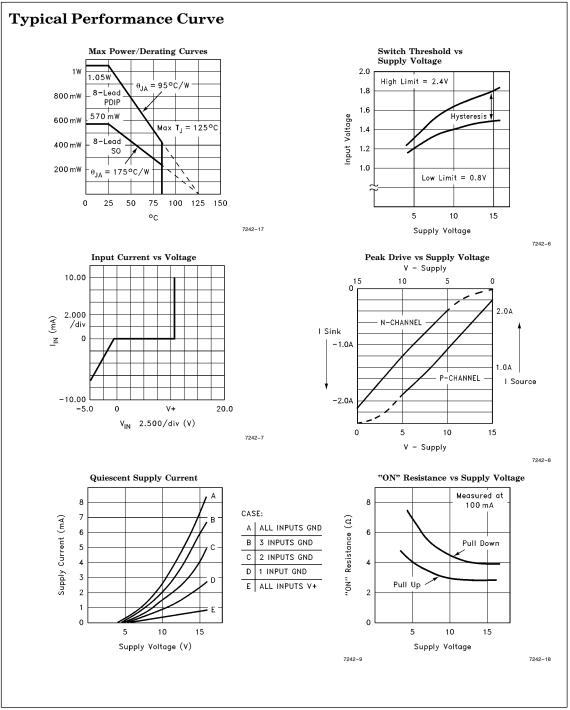




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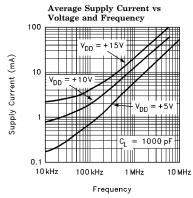


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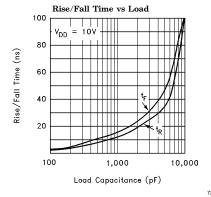


Dual Input, High Speed, Dual Channel Power MOSFET Driver

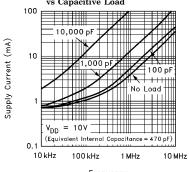
${\bf Typical\ Performance\ Curve-Contd}.$



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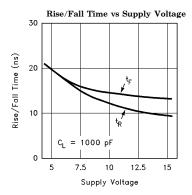


Average Supply Current vs Capacitive Load



Frequency

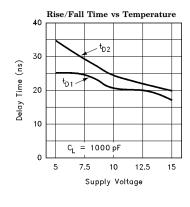
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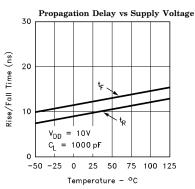


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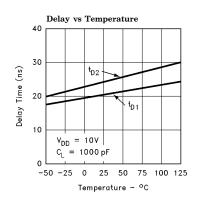
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Typical Performance Curve — Contd.





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General Disclaimer

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