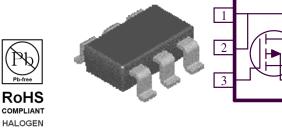
P-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

•	Low $r_{DS(on)}$ provides higher efficiency and
	extends battery life

- Low thermal impedance copper leadframe TSOP-6 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY					
$V_{DS}(V)$	$r_{DS(on)}(\Omega)$	$I_{D}(A)$			
	$0.038 @ V_{GS} = -4.5V$	-5.6			
-20	$0.054 @ V_{GS} = -2.5V$	-4.8			
	$0.072 @ V_{GS} = -1.8V$	-4.0			



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter			Maximum	Units		
Drain-Source Voltage			-20	V		
Gate-Source Voltage	V_{GS}	±8	V			
Continuous Drain Current ^a	T _A =25°C	Τ_	-5.6			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	П	-4.5	A		
Pulsed Drain Current ^b	I_{DM}	±20				
Continuous Source Current (Diode Conduction) ^a	I_S	-1.7	A			
Decree Disciplify and	$T_A=25^{\circ}C$	D_	2.0	\mathbf{w}		
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	Ir D	1.3	**		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

FREE

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient ^a	t <= 5 sec	R_{THJA}	62.5	°C/W		

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

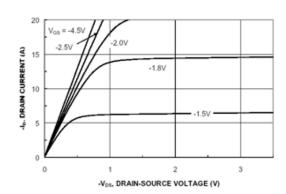
SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)							
D	Ch a l	T C 122	Limits			T T •4	
Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \text{ uA}$	-20			V	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	-0.45			· ·	
Gate-Body Leakage	Igss	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			±100	nA	
Zana Cata Valta aa Duain Cumant	т	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-5		
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-20			A	
		$V_{GS} = -4.5 \text{ V}, I_{D} = -5.5 \text{ A}$		24	38		
Drain-Source On-Resistance ^A	rDS(on)	$V_{GS} = -2.5 \text{ V}, I_D = -4.8 \text{ A}$		30	54	mΩ	
		$V_{GS} = -1.8 \text{ V}, I_D = -4.0 \text{ A}$		43	72		
Forward Tranconductance ^A	g _{fs}	$V_{DS} = -5 \text{ V}, I_{D} = -3.5 \text{ A}$		23		S	
Diode Forward Voltage	V_{SD}	$I_S = 1.3 \text{ A}, V_{GS} = 0 \text{ V}$		-0.70	-1.20	V	
Dynamic ^b							
Total Gate Charge	Qg	V 10 V V 25 V		19.0	31		
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V},$ $I_{D} = -3.5 \text{ A}$		4.20		nC	
Gate-Drain Charge	Qgd			7.60		†	
Switching	-						
Turn-On Delay Time	t _{d(on)}			13	24		
Rise Time	t _r	$V^{}_{\rm DD} = -10$ V, $R^{}_{\rm L} = 6~\Omega$, $I^{}_{\rm D} = -1$ A,		12	19		
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}$		91	140	ns	
Fall-Time	tf			44	73	Ī	

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics (P-Channel)



BOWN 2.5

OBANAL CENT (A)

PROBANT CENT (A)

OBANAL CENT (A)

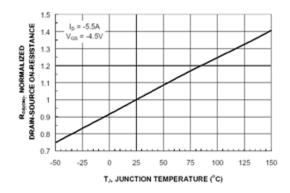
OBANAL CENT (A)

OBANAL CENT (A)

OBANAL CENT (A)

Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.



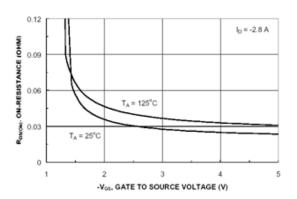
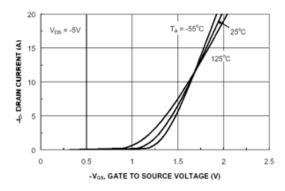


Figure 3. On-Resistance Variation withTemperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



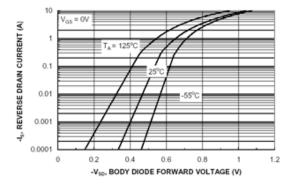


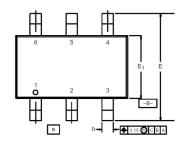
Figure 5. Transfer Characteristics.

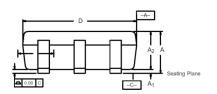
Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

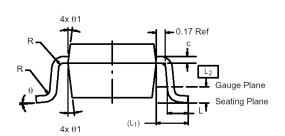
Typical Electrical Characteristics (P-Channel) 3500 Vas. GATE-SOURCE VOLTAGE (V) f = 1MHz I_D = -5.5A -10V 2500 2000 1500 1000 Coss 500 0 20 Q₀, GATE CHARGE (nC) -VDS. DRAIN TO SOURCE VOLTAGE (V) Figure 7. Gate Charge Characteristics. Figure 8. Capacitance Characteristics. 100 SINGLE PULSE R_{s,M} = 156°C/W 46. DRAIN CURRENT (A) T_A = 25°C 2 POWER (W) 0.1 SINGLE PULSE R_{83A} = 156°C/W $T_A = 25^{\circ}C$ 0.01 0.1 10 0.1 -Vos. DRAIN-SOURCE VOLTAGE (V) SINGLE PULSE TIME (SEC) Figure 9. Maximum Safe Operating Area. Figure 10. Single Pulse Maximum Power Dissipation. NORMALIZED EFFECTIVE TRANSIENT THERMAL RESISTANCE R_{0JA} = 156 °C/W 0.01 0.001 0.0001 100 1000 t₁, TIME (sec) Figure 11. Transient Thermal Response Curve.

Package Information

TSOP-6: 6LEAD







	MIL	LIMET	ERS	ı	;	
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.91	-	1.10	0.036	-	0.043
Α1	0.01	_	0.10	0.0004	-	0.004
A ₂	0.84	_	1.00	0.033	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
С	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E ₁	1.55	1.65	1.70	0.061	0.065	0.067
е	1.00 BSC			(0.0394 BSC	;
┙	0.35	_	0.50	0.014	-	0.020
L ₁	0.60 Ref				0.024 Ref	
L ₂	0.25 BSC				0.010 BSC	
R	0.10	_	_	0.004	-	_
θ	0°	4°	8°	0°	4°	8°
θ_1	7° Nom 7° No			7° Nom		