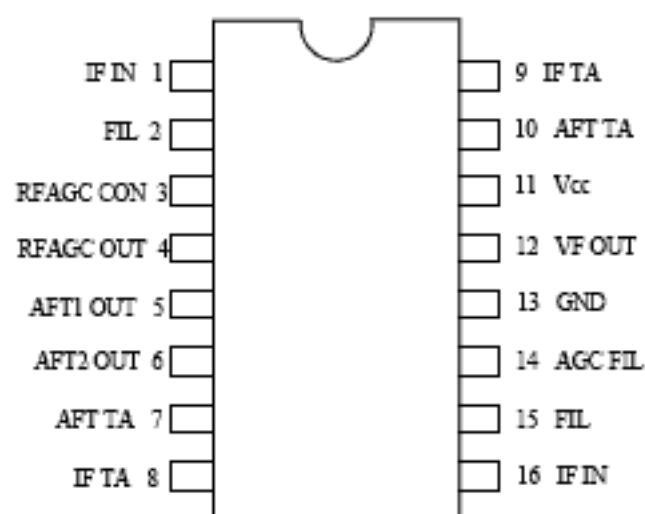
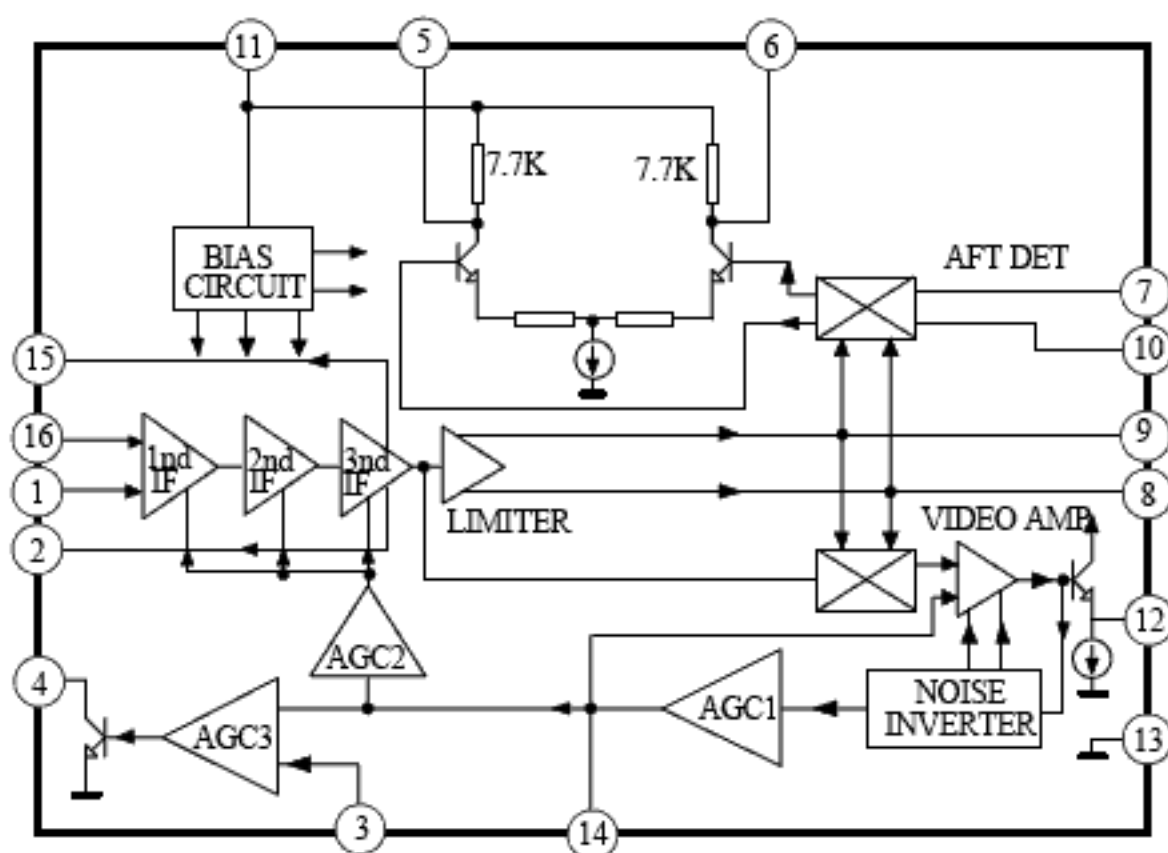


PIN CONNECTION



BLOCK DIAGRAM



MAXIMUM RATINGS (Ta=25℃)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	15	V
Open Loop Voltage	V4	15	V
Video DC Output Current	I12	6	mA
Power Dissipation (note)	Pd	1.4	W
Ambient Temperature	Ta	-20~65	℃
Storage Temperature	Tstg	-55~150	℃

(Note) Derated above Ta=25℃ in the proportion of 11.2mW/℃

ELECTRICAL CHARACTERISTICS (Ta=25℃)

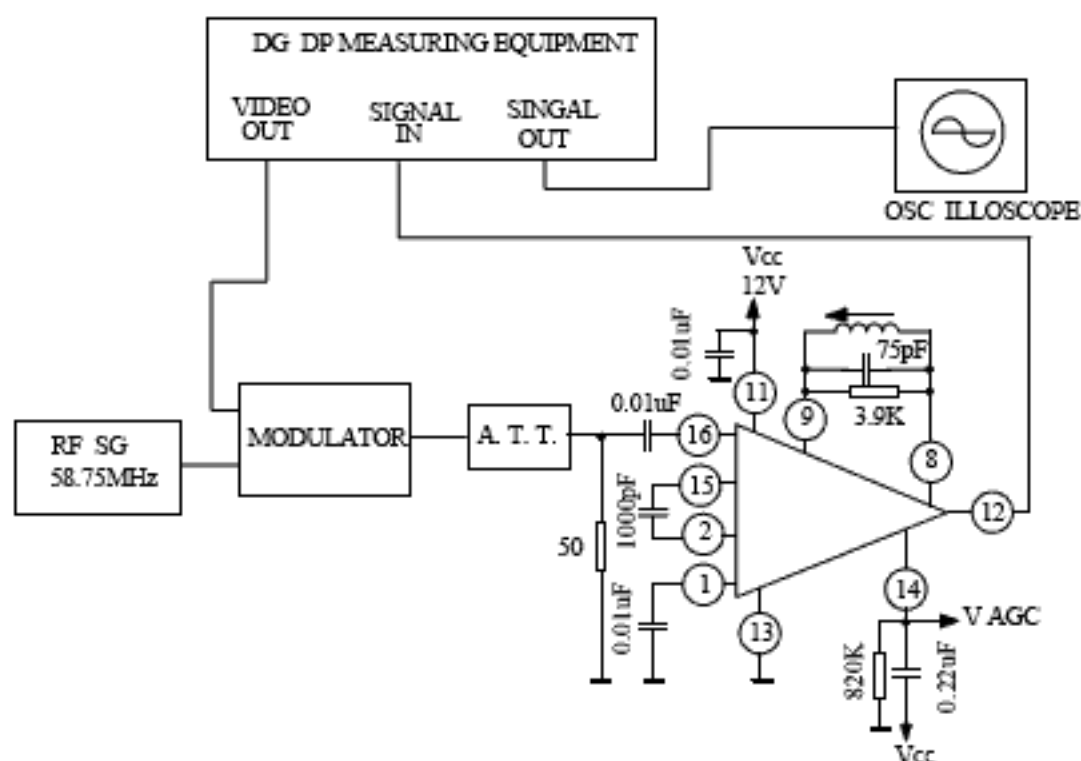
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Recommended Supply Voltage	Vcc (V11)	-	-	10.8	12.0	13.2	V
Supply Current	Icc (I11)	1	Vcc=12V	42	51	63	mA
VIDEO DC Output Voltage	V12	1	Vcc=12V	5.2	5.5	5.8	V
AFT DC Output Voltage	V5	1	Vcc=12V SW1:ON, SW2:ON	5.3	6.8	8.3	V
AFT DC Output Voltage	V6	1	Vcc=12V SW1:ON, SW2:ON	5.3	6.8	8.3	V
AFT Output Offset Voltage	V5-V6	1	Vcc=12V SW1:ON, SW2:ON	-1.5	0	1.5	V
RF AGC Residual Output Voltage	V4 SAT	1	Vcc=12V, SW3:2 SW4:1—D7607 SW4:2—D7611	-	-	0.5	V
RF AGC Leak Current	I4 LEAK		Vcc=12V, SW3:1 SW4:1—D7611 SW4:2—D7607	-	-	1	μA
Video Sensitivity	Vi Pin1-16	2	Vcc=12V, fp=58.75MHz V12=0.8Vp-p, AM:30%	100	200	300	μVrms
AGC Range	ΔA (IF)	2	Vcc=12V, fp=58.75MHz V14=11.5V→4.0V	60	64	-	dB
Sync Tip Level Voltage	VSYNC (V12)	2	Vcc=12V, fp=58.75MHz	2.3	2.5	2.7	V
Maximum IF Input Voltage	VIN MAX PIF	2	Vcc=12V, fp=58.75MHz	100	120	-	mVrms
White Noise Threshold	Vw TH (V12)	2	Vcc=12V, fp=58.75MHz	5.8	6.2	6.6	V
White Noise Clamp Level	Vw CL (V12)	2	Vcc=12V, fp=58.75MHz	3.7	4.1	4.5	V

ELECTRICAL CHARACTERISTICS

CONTINUE

CHARACTERISTIC	SYM-BOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Black Noise Threshold	V _B TH (V12)	2	V _{CC} =12V, f _p =58.75MHz	1.4	1.6	1.8	V
Black Noise Clamp Level	V _B CL (V12)	2	V _{CC} =12V, f _p =58.75MHz	2.9	3.3	3.7	V
Video Frequency Response	f _{BW}	2	Input 58.75MHz +Sweep Generator	4.5	5.5	-	MHz
Suppression of carrier		4	SG1=100mV _{rms} SG2, SG3 OFF	40	50	-	dB
Suppression of 2 nd carrier	I2nd	4	SG1=100mV _{rms} SG2, SG3 OFF	40	50	-	dB
Suppression of Sound Carrier Color Subcarrier	I920	4	SG1=100mV _{rms} SG2=32mV _{rms} SG3=32mV _{rms}	33	38	-	dB
Differential Phase	DP	3	-	-	3.5	5	deg
Differential Gain	DG	3	(Note 10)	-	7	10	%
Input Impedance	R _{IN}	-	f=58.75MHz Between Pin16-1	3.0	4.5	6.0	kΩ
	C _{IN}			-	2.0	5.0	pF
AFT Sensitivity	$\Delta F / \Delta V_{5-6}$	2	f=58.75MHz	-	16	-	kHz/V
AFT Output Upper Voltage	V ₅ , V ₆ U	2	f=58.75MHz	11.7	11.9	12.0	V
AFT Output Lower Voltage	V ₅ , V ₆ L	2	f=58.75MHz	1.8	2.3	2.8	V
Max Available Current	I ₄ MAX	-	D7607	0.3	-	-	mA
			D7611	7	-	-	mA
RF AGC Delay Point Range	V _{IN} Delay	-	f=58.75MHz	-	-	-	--

3.DG DP TEST CIRCUIT

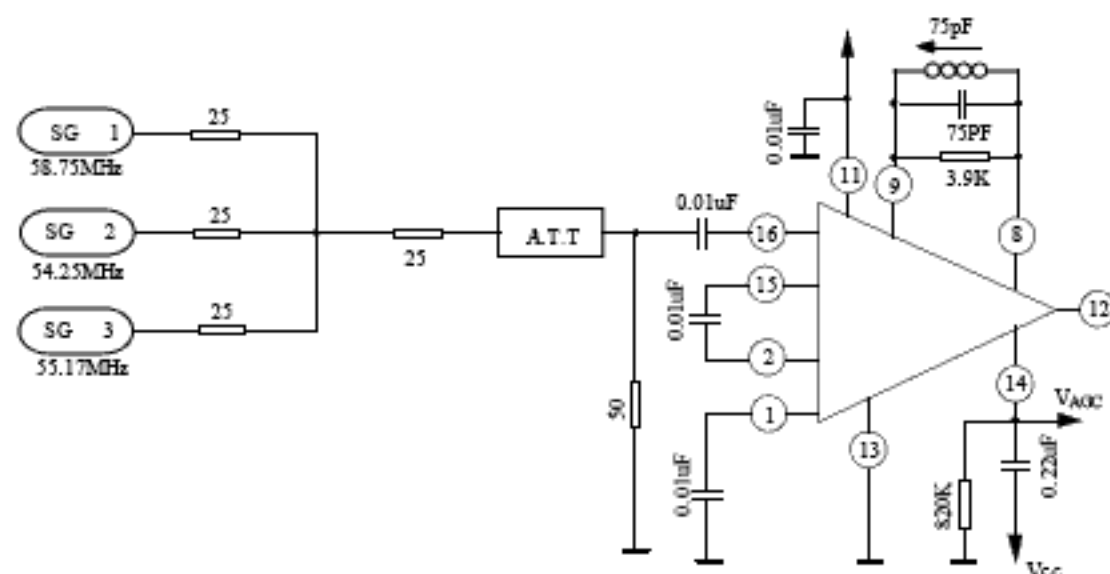


V AGC : GAIN REDUCTION 40Db ADJUST

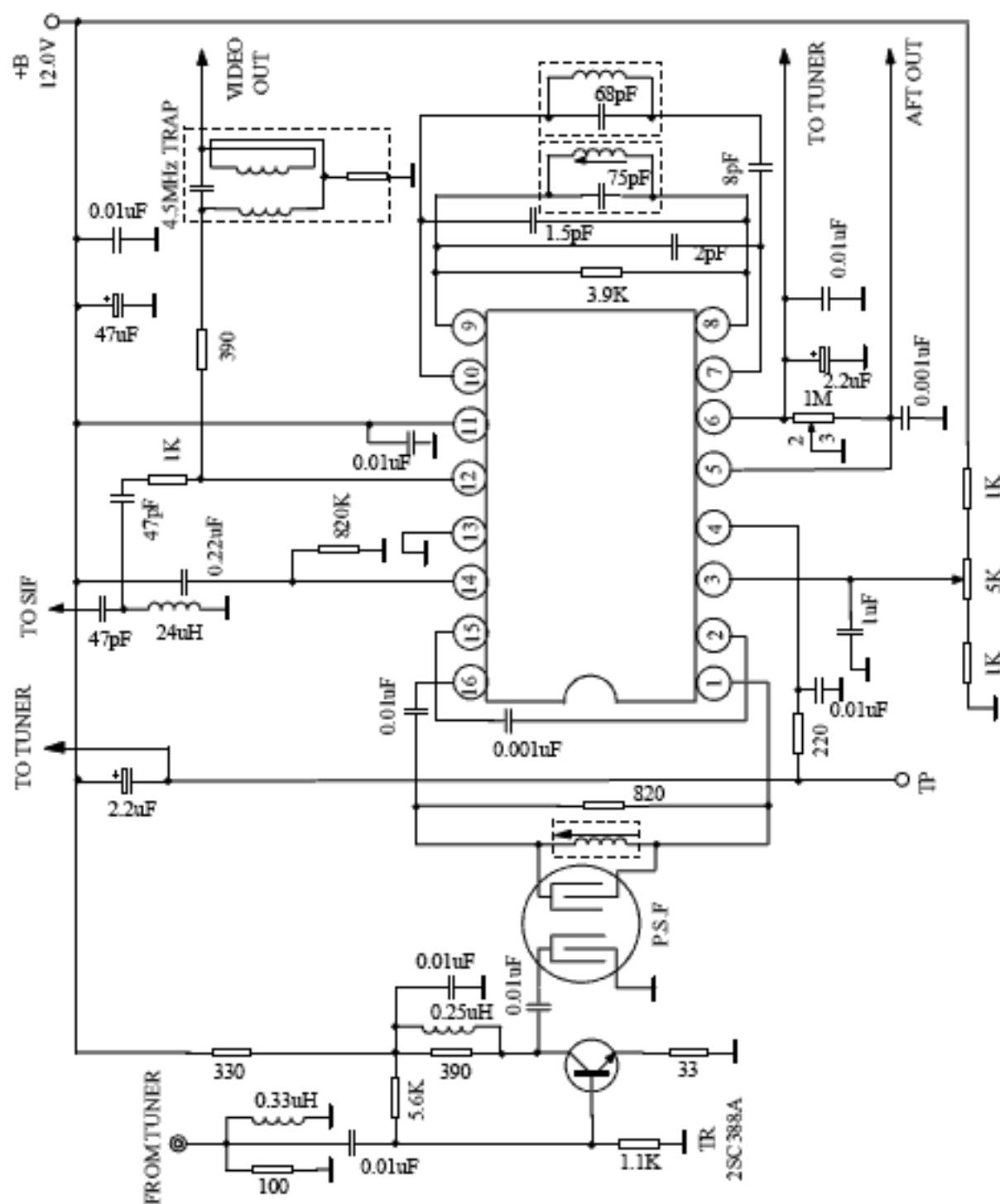
A. T. T. : SYNC TIP LEVEL DC 2.5V ADJUST

A. P. L : 50%

4.INTER MODULATION TEST CIRCUIT



APPLICATION CIRCUIT



CHARACTERISTIC CURVES

AFT OUTPUT VOLTAGE AS A FUNCTION OF FREQUENCY FOR THE CD7607CP/CD7611CP

