TOSHIBA TA8030S

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8030S

WATCHDOG TIMER

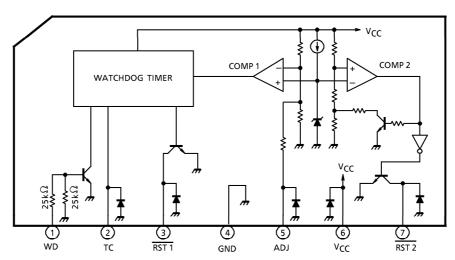
The TA8030S is a system reset IC supplied with 5V power. It is specially designed for microcomputer systems. It incorporates a watchdog timer for monitoring microcomputer operation and has many reset functions, including a reset timer output which will be given at power-on and another reset output which will be given when the supply voltage drops. With these functions, it helps build up a reliable system.

Weight: 0.7g (Typ.)

FEATURES

- Watchdog timer
- Power-on reset timer
- **Dual-reset output**
- Small SIP-7 pin

BLOCK DIAGRAM AND PIN LAYOUT



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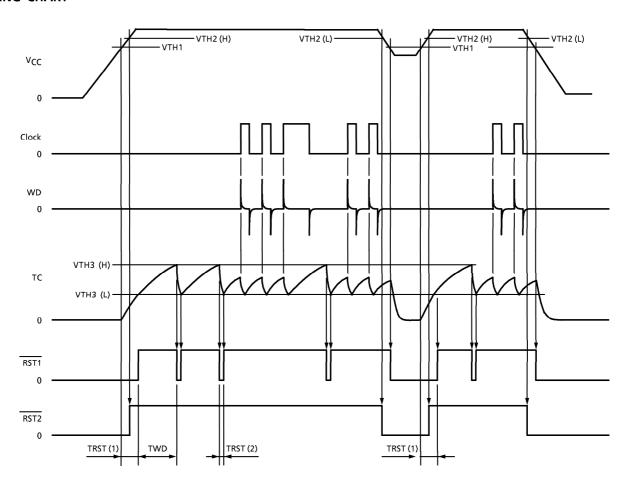
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PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION
1	WD	Clock input pin for watchdog timer. If this IC is only used as a power-on reset timer, this pin is connected to RST1.
2	TC	Time setting pin for the reset and watchdog timers. R_1 leads to V_{CC} , and C_1 leads to GND.
3	RST1	 Supplies an NPN transistor open-collector output. Generates a reset signal determined by the CR combination connected to the TC pin. Supplies reset pulses intermittently if no clock is given to the WD pin.
4	GND	Grounded
5	ADJ	V _{CC} detect voltage (1) adjusting pin. The detection voltage is 4.6V when this pin is grounded; it is 3.5V when this pin is directly connected to V _{CC} .
6	V _{CC}	Power supply pin for internal circuit. The output voltage can also be detected at this pin.
7	RST2	Supplies an NPN transistor open-collector output. It is the output pin for V _{CC} detect voltage (2). The detect voltage has a hysteresis of 0.17V.

TIMING CHART



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	17	V
Input Voltage	V _{IN}	-7 to 7	V
Output Voltage	Vout	7	V
Output Current	IOUT	10	mA
Power Dissipation	PD	300	mW
Operating Temperature	T _{opr}	-40 to 85	°C
Storage Temperature	T _{stg}	- 55 to 150	°C
Lead Temperature-time	T _{sol}	260 (10s)	°C

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5V$, $T_{a} = -40$ to $85^{\circ}C$) (1) DC CHARACTERISTICS

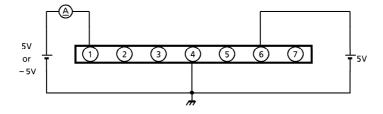
CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
land Command	ΊΗ	\A/D	1	V _{IN} = 5V	0.1	0.17	0.35	A	
Input Current	Iμ	WD	1	V _{IN} = -5V	- 0.06	- 0.1	-0.2	mA	
Innest Valtage	VIH	\ # / F	2	_	2.2		_	.,	
Input Voltage	V_{IL}	WD	2	_	_	_	0.6	V	
Input Current	IN	TC	4	V _{IN} = 1.5V	-2		2	μΑ	
Output Current	IOUT	TC	4	V _{OUT} = 4.2V	2.4	4	7.7	mA	
Watchdog Timer	V _{TH3} (H)	TC	3	_	3.5	4	4.5	V/	
Threshold Voltage	V _{TH3} (L)	TC	3	_	1.75	2	2.25	V	
Output Voltage	VOL	RST1	5	I _{OUT} = 2mA	_		0.5	V	
Output Leakage Current	^I LEAK	RST2	6	V _{OUT} = 7V	_		5	μΑ	
	V _{TH1}		_	_	4.0	4.25	4.5		
V _{CC} Detect Voltage (1)	V _{TH1} (H)	Vcc	3	ADJ = GND	4.3	4.6	4.9	V	
	V _{TH1} (L)		3	ADJ = V _{CC}	3.25	3.5	3.75		
V - Detect Veltere (2)	V _{TH2} (H)	V	3	_	4.4	4.65	4.9	V	
V _{CC} Detect Voltage (2)	∆V _{TH2}	Vcc	3	_	_	0.17	0.3]	
Current Consumption	lcc	Vcc	7	_	_	2.5	4.5	mA	

(2) AC CHARACTERISTICS

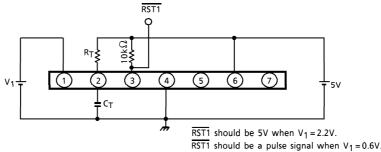
CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Watchdog Timer	T_{WD}	RST1	3	_	$0.9 \times C_1R_1$	$1.1 \times C_1R_1$	$1.3 \times C_1R_1$	S
Reset Timer (1)	T _{RST} (1)	RST1	3	_	$0.4 \times C_1R_1$	$0.5 \times C_1R_1$	$0.6 \times C_1R_1$	S
Reset Timer (2)	T _{RST} (2)	RST1	3	_	350 × C ₁	750 × C ₁	1500 × C ₁	S
Input Pulse Width	TW	WD	3	_	3	_	_	μs
Transfer Delay Time	^t d1	RST1	3	t_{dHL} (C ₁ = 0 μ F)	_	3	10	μs
Transier Delay Time	^t d2	RST2	3	^t dHL ^{, t} dLH	_	3	10	μs

TEST CIRCUIT

1. I_{IH} | I_{IL} (WD)

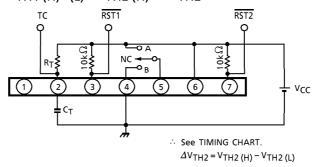


2. V_{IH}、V_{IL} (WD)

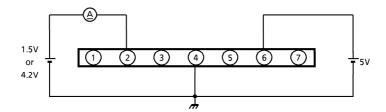


 $\overline{RST1}$ should be a pulse signal when $V_1 = 0.6V$.

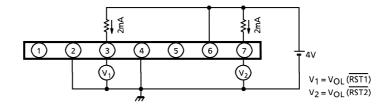
3. V_{TH3} (H), (L) (TC), V_{TH1} , V_{TH1} (H), (L), V_{TH2} (H), ΔV_{TH2} , AC CHARACTERISTICS



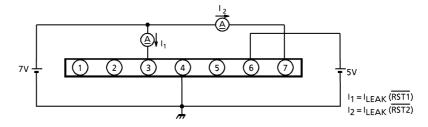
4. I_{IN}, I_{OUT} (TC)



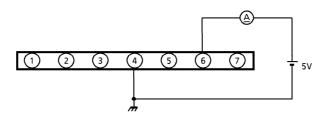
5. V_{OL} (RST1) (RST2)



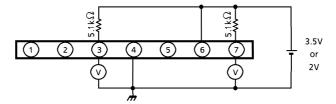
6. V_{LEAK} (RST1) (RST2)



7. I_{CC}

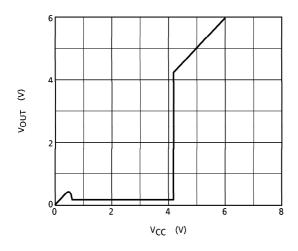


8. V_{OL (1)}, (2) (RST1) (RST2)

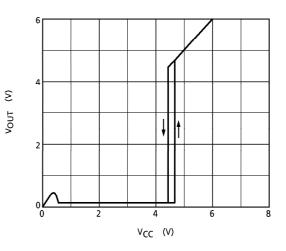


RESET OUTPUT STANDARD CHARACTERISTICS

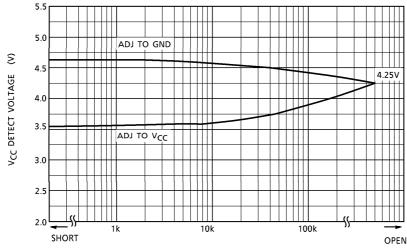
1. **RST1**



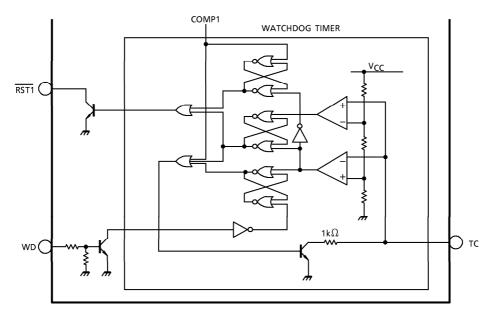
 $2. \ \overline{RST2}$



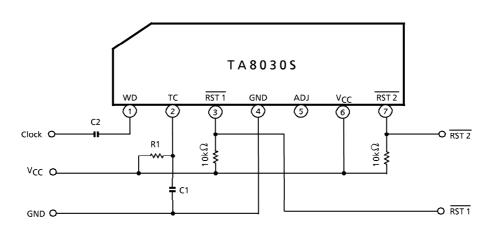
ADJ PIN PULL-UP / DOWN RESISTANCE VS $V_{\mbox{\footnotesize{CC}}}$ DETECT VOLTAGE



EQUIVALENT CIRCUIT DIAGRAM (WATCHDOG TIMER)



EXAMPLE OF APPLICATION CIRCUIT



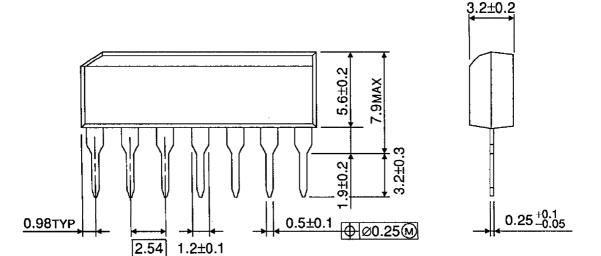
RECOMMENDED CONDITIONS

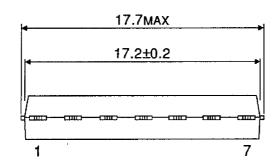
PART NAME	MIN.	TYP.	MAX.	UNIT
C ₁	0.01	_	100	μ F
R ₁	10	_	100	kΩ
C ₂	_	2200	_	pF

Unit: mm

OUTLINE DRAWING

SIP7-P-2.54A





Weight: 0.7g (Typ.)