TOSHIBA TA8052S

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8052S

0.3A MOTOR DRIVER WITH BRAKE FUNCTION

The TA8052S is a full-bridge driver which directly drives a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, the IC can be directly controlled from a CPU or other control system. The IC also has various protective functions.

Weight: 0.7g (Typ.)

FEATURES

Output current: 300mA (max.)

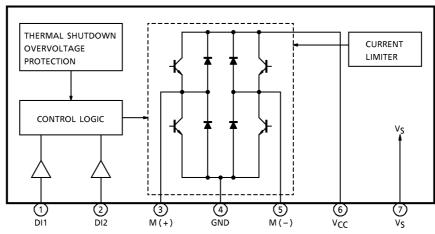
Four modes : Forward, reverse, stop, and brake

Multiple protective functions

: Thermal shutdown, current limiter, and overvoltage shut down.

- Bulit-in diode for counteracting counter electromotive force
- Small SIP-7 pin

BLOCK DIAGRAM AND PIN LAYOUT



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PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION
1	DI1	Output status control pin.
2	DI2	Connects to a PNP-type voltage comparator.
3	M (+)	Connects to the DC motor. Diodes for absorbing counter electromotive force are contained on the V_{CC} and GND sides.
4	GND	Grounded
5	M (–)	Connects to the DC motor together with pin 3 and has the same function as pin 3. This pin is controlled by the inputs from pins 1 and 2.
6	Vcc	Power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 30V, thus protecting the IC and the load.
7	VS	Power supply pin for the control section. This pin is completely separated from the V _{CC} pin.

TRUTH TABLE

Inp	out	Out	put	Output Mada		
DI1	DI2	M(+)	M (-)	Output Mode		
Н	Н	L	L	BRAKE		
L	Н	L	Н	REVERSE		
Н	L	Н	L	FORWARD		
L	L	O (high im	FF pedance)	STOP		

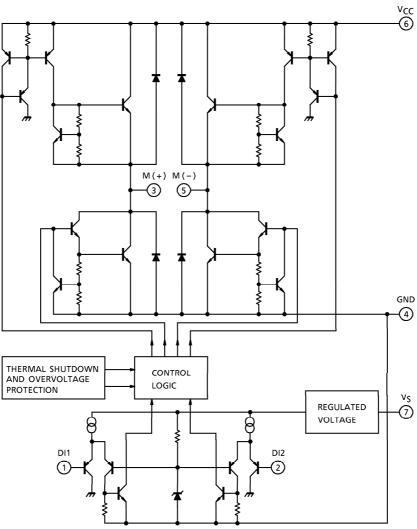
MAXIMUM RATINGS (Ta = 25° C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	VCC	50 (1s)	V
Input Voltage	V _{IN}	-0.3~V _{CC} +0.3	\
Output Current	lout	300	mA
Power Dissipation	PD	0.92	W
Operating Temperature	T _{opr}	- 40∼85	°C
Storage Temperature	T _{stg}	- 55~150	°C
Lead Temperature-time	T _{sol}	260 (10s)	°C

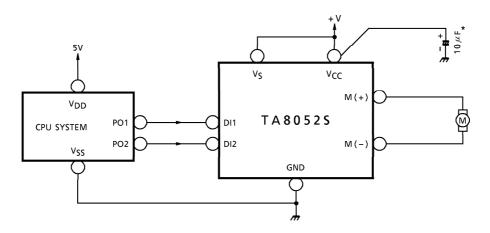
ELECTRICAL CHARACTERISTICS (V_S , $V_{CC} = 8$ to 16V, $T_0 = -40$ to 85°C)

	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Current Consumption	I _{S1}		_	Stop	_	2.5	5	mA	
Current Consumption (I)	I _{S2}	Vs	_	Forward / Reverse	_	4	8		
(1)	I _{S3}		1	Brake	_	4	8		
Current Consumption	l _{CC1}	Vcc		Stop	_		1		
(II)	lcc2			Forward / Reverse	_	7.5	15	mA	
(11)	I _{CC3}		-	Brake	_		1		
Input Voltage	V _{IL}	DI1			_		0.8	V	
input voitage	٧ _{IH}	/ DI2		_	2.0		_		
Innut Current	VIL	DI1	_	V _{IN} = 0.4V	_	_	- 20		
Input Current	VIH	/ DI2	_	V _{IN} = V _{CC}	_	_	10	μ A	
Output Saturation Voltage	V _{sat} (total)	M (+) /M(-)		I _O = 200mA	_	1.8	2.5	٧	
Output Leekees Coment	ILEAK-U	M(+)	_	V _O = 0V	_		- 100	μΑ	
Output Leakage Current	ILEAK-L	/M(-)	_	VO = VCC	_		100		
Diada Famurand Valtage	V _{F-U}	M(+)	_	I _F = 200mA	_	1.1	_	V	
Diode Forward Voltage	V _{F-L}	/M(-)	_	I _F = 200mA	_	1.1	_	, v	
Output Limit Current	Isc	_	_	Ta = 25°C	0.3	0.55	_	Α	
Shutdown Tomporatura	T _{SD-H}	_	_	ON→OFF —		150	_	°C	
Shutdown Temperature	T _{SD-L}	_	_	OFF→ON	_	130	_	ر	
Overvoltage Detection	V _{SD}	_	_	-	27	30	33	٧	
Transfer Dolay Time	t _{pLH}	_	_	_	_	1	10		
Transfer Delay Time	t _{pHL}			_	_	1	10	μ s	

I/O EQUIVALENT CIRCUIT



EXAMPLE OF APPLICATION CIRCUIT

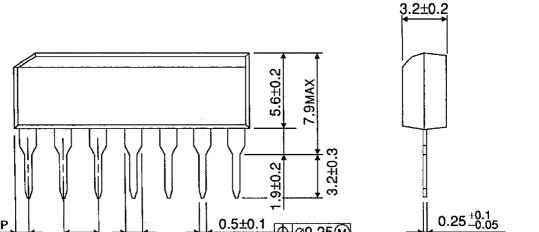


* Connect this capacitor as close to the IC as Possible.

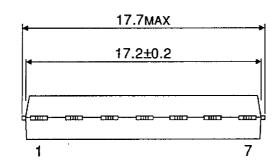
Unit: mm

OUTLINE DRAWING

SIP7-P-2.54A



0.5±0.1 ⊕ Ø0.25 €



2.54 1.2±0.1

Weight: 0.7g (Typ.)

0.98TYP