

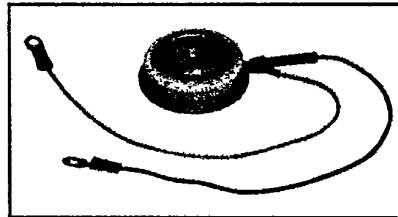
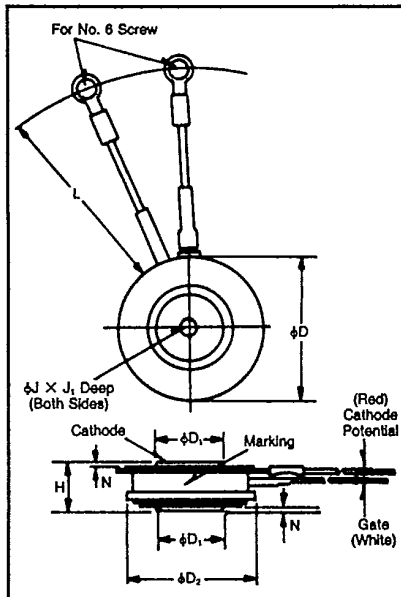
POWEREX

Powerex, Inc. Hillis Street, Youngwood, Pennsylvania 15697 (412) 925-7272
 Powerex Europe, S.A., 428 Ave. G. Durand, BP107, 72003 LeMans, France (43) 72.75.15

T620/T630

Phase Control SCR

150-300 Amperes Avg
 100-1600 Volts



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T62 Outline Drawing

Dimensions	Inches		Millimeters	
	Min.	Max.	Min.	Max.
φD	1.610	1.650	40.89	41.91
φD ₁	.745	.755	18.92	19.18
φD ₂	1.420	1.460	36.07	37.08
H	.500	.560	12.70	14.22
φJ	.135	.145	3.43	3.68
J ₁	.072	.082	1.83	2.08
L	7.75	8.50	196.85	215.90
N	.030	—	.76	—

Creep Distance—.34 in. min. (8.64 mm)
 Strike Distance—.26 in. min. (6.60 mm).
 (In accordance with NEMA standards.)
 Finish—Nickel Plate.
 Approx. Weight—2.3 oz. (66 g).
 1. Dimension "H" is clamped dimension.

Description

Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak (Pow-R-Disc) devices employing the field-proven amplifying (di/namic) gate.

Features:

- ☐ Low On-State Voltage
- ☐ High di/dt
- ☐ High dv/dt
- ☐ Hermetic Packaging
- ☐ Excellent Surge and I²t Ratings

Applications:

- ☐ Power Supplies
- ☐ Battery Chargers
- ☐ Motor Control
- ☐ Light Dimmers
- ☐ VAR Generators

Ordering Information

Example: Select the complete eight digit part number you desire from the table – i.e. T6200820 is a 800 Volt, 200 Ampere Phase Control SCR.

Type	Voltage		Current	
	V _{ORM} V _{RRM}	Code	I _r (avg)	Code
T630	100	01	150	15
	200	02	200	20
	300	03	300	30
	400	04		
	500	05		
	600	06		
T620	700	07		
	800	08		
	900	09		
	1000	10		
	1100	11		
	1200	12		
	1300	13		
	1400	14		
	1500	15		
	1600	16		



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Absolute Maximum Ratings

	Symbol	T620 -- 15 T630 -- 15	T620 -- 20 T630 -- 20	T620 -- 30 T630 -- 30	Units
RMS On-State Current	$I_{T(RMS)}$	235	315	470	Amperes
Average On-State Current	$I_{T(av)}$	150	200	300	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (60Hz) ^①	I_{TSM}	3300	4000	5500	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (50Hz) ^①	I_{TSM}	3000	3650	5000	Amperes
Critical Rate-of-Rise of On-State Current (Non-Repetitive) ^{① ② ③}	di/dt	800	800	800	Amperes/ μs
Critical Rate-of-Rise of On-State Current (Repetitive)	di/dt	150	150	150	Amperes/ μs
I^2t (for Fusing), 8.3 milliseconds	I^2t	45,000	64,400	120,000	A ² sec
Peak Gate Power Dissipation	P_{GM}	16	16	16	Watts
Average Gate Power Dissipation	$P_{G(av)}$	3	3	3	Watts
Storage Temperature	T_{STG}	-40 to 150	-40 to 150	-40 to 150	°C
Operating Temperature	T_J	-40 to 125	-40 to 125	-40 to 125	°C
Mounting Force ^④		1000 to 1400	1000 to 1400	1000 to 1400	lb.
Mounting Force ^⑤		450 to 635	450 to 635	450 to 635	kg

① Consult recommended mounting procedures.

② Applies for zero or negative gate bias.

③ Per JEDEC RS-397, 5.2.2.1.

④ With recommended gate drive.

⑤ Higher dv/dt ratings available, consult factory.

⑥ Per JEDEC standard RS-397, 5.2.2.6.



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Electrical and Thermal Characteristics

Characteristics	Symbol	Test Conditions	T620 _ _ 15 T630 _ _ 15	T620 _ _ 20 T630 _ _ 20	T620 _ _ 30 T630 _ _ 30	Units
Current—Conducting State Maximums						
Peak On-State Voltage	V _{TM}	I _{TM} = 625A, T _J = 25°C	2.6	2.05	1.55	Volts
T620/T630						
Voltage—Blocking State Maxlimums^⓪						
Forward Leakage, Peak	I _{DRM}	T _J = 125°C, V _{DRM} = rated		25		mA
Reverse Leakage, Peak	I _{RRM}	T _J = 125°C, V _{RRM} = rated		25		mA
Switching						
Typical Turn-Off Time	t _q	I _T = 150A, T _J = 125°C, di _R /dt = 12.5A/μsec, reapplied dv/dt = 20V/μsec linear to 0.8V _{DRM}		100		μsec
Typical Turn-On Time ^⓪	t _{on}	I _T = 100A, V _D = 100V		5		μsec
Min. Critical dv/dt exponential to V _{DRM} ^⓪ ⓪	dv/dt	T _J = 125°C		300		V/μsec
Thermal						
Maximum Thermal Resistance, ^⓪ double sided cooling						
Junction to Case	R _{θJC}			.08		°C/Watt
Case to Sink, Lubricated	R _{θCS}			.02		°C/Watt
Gate—Maximum Parameters						
Gate Current to Trigger	I _{GT}	T _J = 25°C, V _D = 12V		150		mA
Gate Voltage to Trigger	V _{GT}	T _J = 25°C, V _D = 12V		3		Volts
Non-Triggering Gate Voltage	V _{GDM}	T _J = 125°C, rated V _{DRM}		.15		Volts
Peak Forward Gate Current	I _{GTM}			4		Amperes
Peak Reverse Gate Voltage	V _{GRM}			5		Volts

① Consult recommended mounting procedures.

② Applies for zero or negative gate bias.

③ Per JEDEC RS-397, 5.2.2.1.

④ With recommended gate drive.

⑤ Higher dv/dt ratings available, consult factory.

⑥ Per JEDEC standard RS-397, 5.2.2.6.



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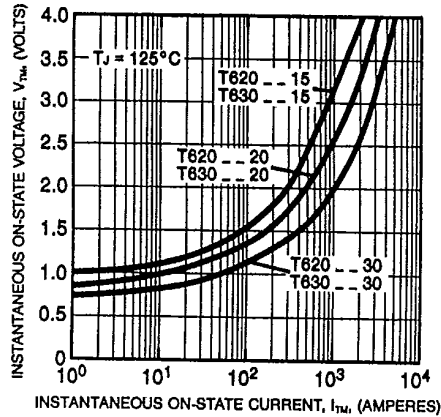
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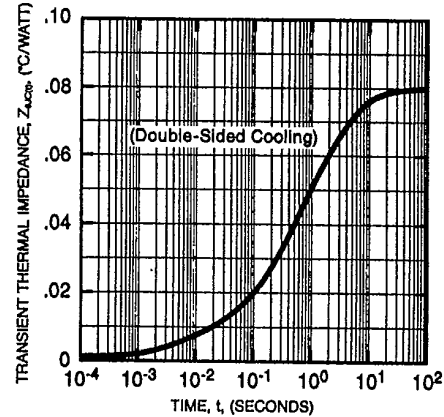
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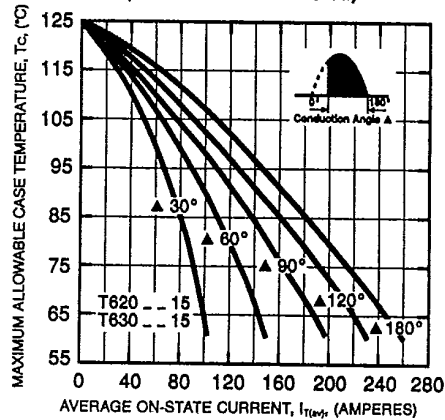
MAXIMUM ON-STATE CHARACTERISTICS



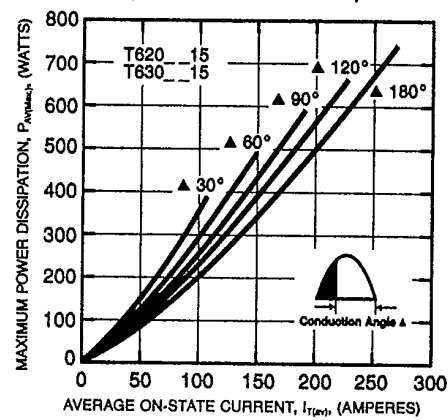
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (JUNCTION TO CASE)



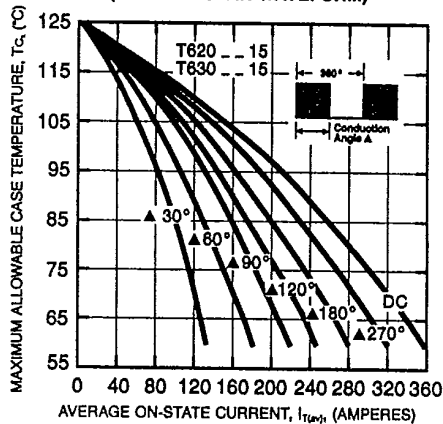
MAXIMUM ALLOWABLE CASE TEMPERATURE (SINUSOIDAL WAVEFORM)



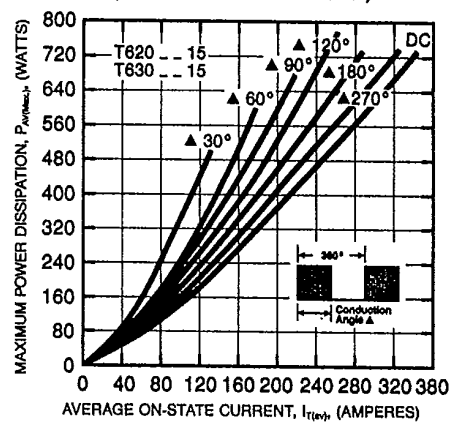
MAXIMUM ON-STATE POWER DISSIPATION (SINUSOIDAL WAVEFORM)



MAXIMUM ALLOWABLE CASE TEMPERATURE (RECTANGULAR WAVEFORM)



MAXIMUM ON-STATE POWER DISSIPATION (RECTANGULAR WAVEFORM)





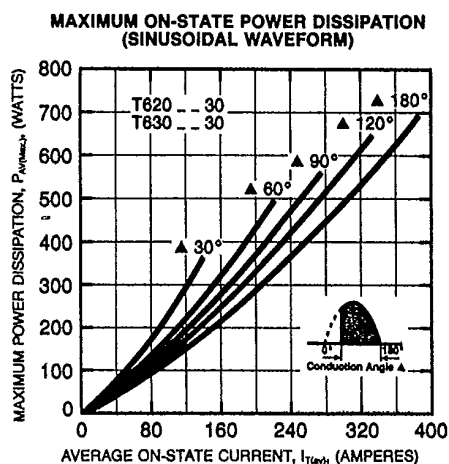
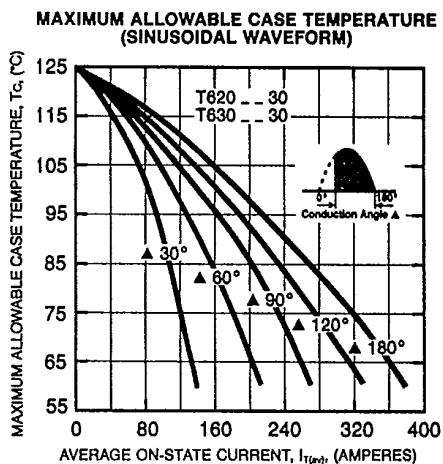
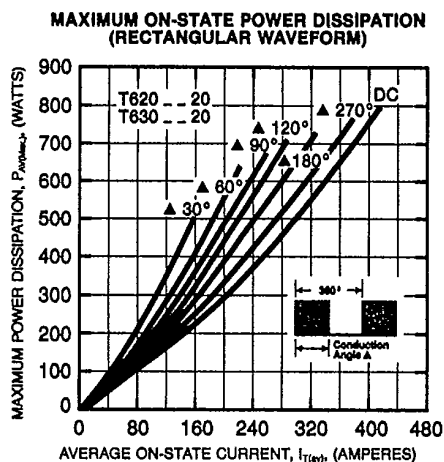
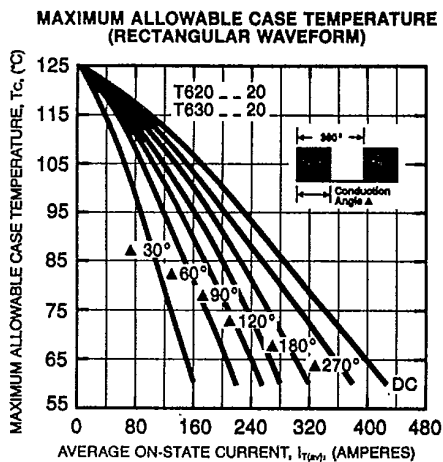
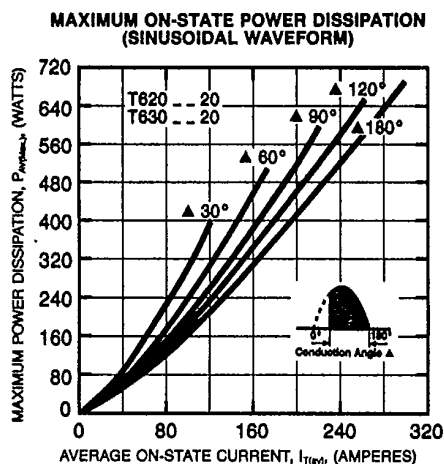
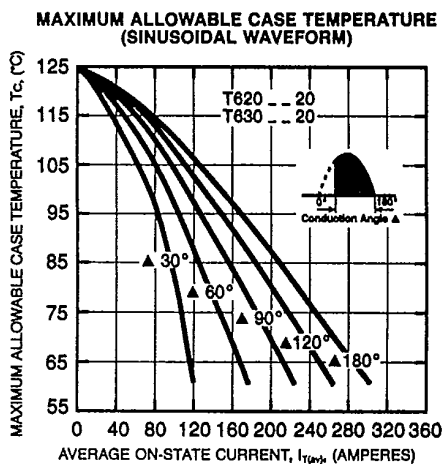
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