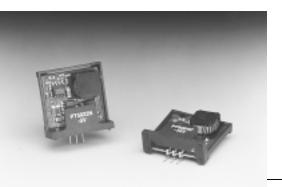
Positive Input/Negative Output Integrated Switching Regulator

(Revised 12/19/2001)



Features

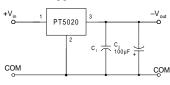
- Negative Output
- Input Voltage Range: +4.75 to +7 Volts
- Laser-Trimmed
- Small Footprint
- Soft Start
- 5-Pin Mount Option (Suffixes L & M)

Description

The PT5020 series of integrated switching regulators (ISRs) convert a positive input voltage, typically +5V, to a negative output voltage for a wide range of analog and datacom applications.

These Plus to Minus ISRs incorporate a "Buck-Boost" topology and are packaged in the 3-pin, single in-line pin (SIP) package configuration.

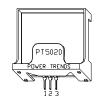
Standard Application



 C_1 = Optional ceramic (1-5 μ F) C₂ = Required Electrolytic (100μF)

Pin-Out Information

Pin	Function
1	V_{in}
2	GND
3	V _{out}



Ordering Information

PT5021 □ = -3.3 Volt
PT5022 □ = -5 Volts
PT5023 □ = -9 Volts
PT5024 □ = -12 Volts
PT5025 □ = -15 Volts
PT5026 □ = -5.2 Volt

PT5027 □ = -8.0 Volts **PT5028** □ = -6.5 Volts **PT5029** □ = -5.5 Volts

PT5030 □ = -6.0 Volts **PT5031** □ = -1.7 Volts

PT Series Suffix (PT1234x)

Case/Pin Configuration	Order Suffix	Package Code *
Vertical	N	(EAD)
Horizontal	Α	(EAA)
SMD	С	(EAC)
Horizontal, 2-pin Tab	M	(EAM)
SMD, 2-Pin Tab	L	(EAL)

* Previously known as package styles 100/110. (Reference the applicable package code drawing for the dimensions and PC board layout)

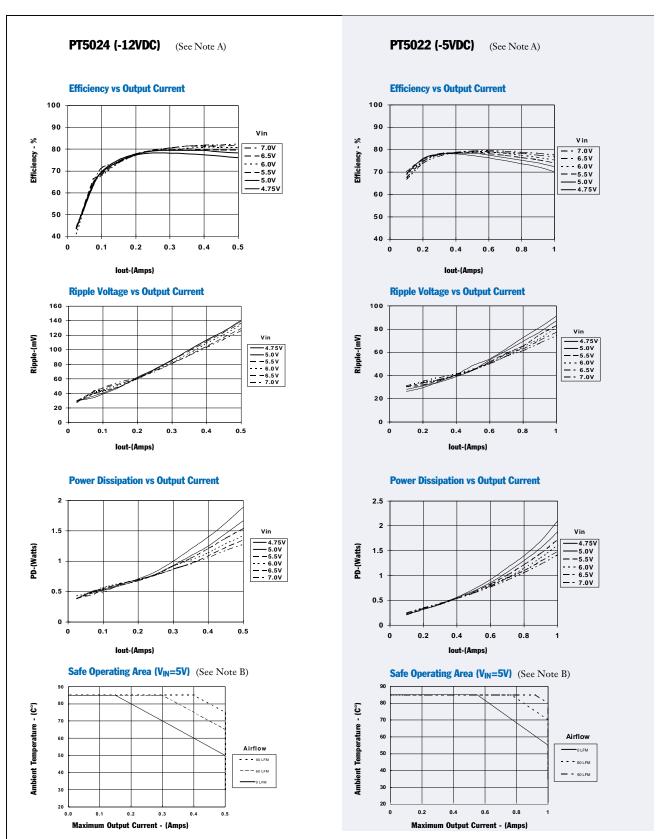
NOTE: PT5020 ISRs are not Short-Circuit Protected.

Specifications (Unless otherwise stated, $T_a = 25$ °C, $V_{in} = 5V$, $I_o = I_{o}max$, $C_2 = 100 \mu F$)

			P			
Characteristics	Symbol	Conditions	Min	Тур	Max	Units
Output Current	$I_{\rm o}$	$\begin{array}{c} Over \ V_{in} \ range \\ V_o = -1.7V \ to \ -6.5V \\ V_o = \ -9V \\ V_o = \ -12V \\ V_o = \ -15V \end{array}$	0.25 (1) 0.10 (1) 0.10 (1) 0.10 (1)	_ _ _	1.0 0.60 0.50 0.30	A
Input Voltage Range	$ m V_{in}$	Over Io range	4.75	_	7 (2)	V
Output Voltage Tolerance	$\Delta V_{\rm o}$	Over V _{in} Range T _a = -20°C to SOA limit (3)	_	±1.5	±3	$%V_{\circ}$
Line Regulation	Reg _{line}	Over V _{in} range	_	±0.5	±1	$% { m V_o}$
Load Regulation	Regload	$I_{o}min \le I_{o} \le I_{o}max$	_	±0.5	±1	$%V_{o}$
Efficiency	η	$I_o = 0.5 I_o max$	_	75	_	%
Vo Ripple (pk-pk)	$V_{\rm r}$	20MHz bandwidth	_	±2	±5	$% V_{o}$
Transient Response	t _{tr}	25% load change V _o over/undershoot	_	500 3.0	5.0	μSec %V _o
Current Limit	I_{lim}		_	150	_	%I _o max
Inrush Current	$I_{ m ir}$ $t_{ m ir}$	On start up	_	1.0 ⁽³⁾ 1.0	_	A mSec
Switching Frequency	$f_{ m s}$	Over I_o range $\begin{vmatrix} V_o \end{vmatrix} = 1.7 \text{ to } 8V$ $\begin{vmatrix} V_o \end{vmatrix} \ge 8 \text{ V}$	0.8 500	1 650	1.2 800	MHz kHz
Operating Temperature Range	T_a	_	-20	_	+85 (4)	°C
Thermal Resistance	θ_{ja}	Free Air Convection (40-60LFM)	_	50	_	°C/W
Storage Temperature	T_s		-40	_	+125	°C
Mechanical Shock		Per Mil-STD-883D, Method 2002.3 1 msec, Half Sine, mounted to a fixture	_	500	_	G's
Mechanical Vibration Per Mil-STD-883D, 20-2000 Hz		Suffixes N, A, & C Suffixes L & M	=	5 20	_	G's
Weight		Suffixes N, A, & C Suffixes L & M		4.5 6.5 (5)		grams

Notes: (1) The ISR will operate at no load with reduced specifications.

- (2) For applications with input voltages greater than 7 VDC, use the PT78NR100 Series.
 (3) The inrush current stated is above the normal input current for the associated output load.
- (4) See Safe Operating Area curves or consult the factory for the appropriate derating
 (5) The tab pins on the 5-pin mount package types (suffixes L & M) must be soldered. For more information see the applicable package outline drawing.



Note A: Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter. Note B: Thermal derating graphs are developed in free-air convection cooling, which corresponds to approximately 40–60LFM of airflow.



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
PT5021A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5021C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5021J	ACTIVE	SIP MOD ULE	EAJ	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5021L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5021N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022CT	ACTIVE	SIP MOD ULE	EAC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT5022H	ACTIVE	SIP MOD ULE	EAH	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5022J	ACTIVE	SIP MOD ULE	EAJ	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5022L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022M	ACTIVE	SIP MOD ULE	EAM	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5022S	ACTIVE	SIP MOD ULE	EAF	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5023A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5023C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5023N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024CT	ACTIVE	SIP MOD ULE	EAC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT5024J	ACTIVE	SIP MOD ULE	EAJ	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5024L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024M	ACTIVE	SIP MOD ULE	EAM	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5024S	ACTIVE	SIP MOD ULE	EAF	3	16	TBD	Call TI	Level-1-215C-UNLIM





com 13-Oct-2005

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)
PT5025A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5025C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5025H	ACTIVE	SIP MOD ULE	EAH	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5025L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5025N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5026A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5026C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5026CT	ACTIVE	SIP MOD ULE	EAC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT5026H	ACTIVE	SIP MOD ULE	EAH	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5026J	ACTIVE	SIP MOD ULE	EAJ	3	16	TBD	Call TI	Level-1-215C-UNLIM
PT5026L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5026LT	ACTIVE	SIP MOD ULE	EAL	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT5026M	ACTIVE	SIP MOD ULE	EAM	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5026N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5027A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5027C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5027N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5028A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5028N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5029A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5029C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5029M	ACTIVE	SIP MOD ULE	EAM	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5030A	ACTIVE	SIP MOD ULE	EAA	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5030C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5030L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5030N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM



PACKAGE OPTION ADDENDUM

13-Oct-2005

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
PT5031C	ACTIVE	SIP MOD ULE	EAC	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5031L	ACTIVE	SIP MOD ULE	EAL	3	35	TBD	Call TI	Level-1-215C-UNLIM
PT5031N	ACTIVE	SIP MOD ULE	EAD	3	35	TBD	Call TI	Level-1-215C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in

a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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