## ASSP

### 1.0 GHz band Low Power I/Q Modulator For Direct Conversion

## MB54608L/MB54608B

## ■ DESCRIPTION

The MB54608L/B is an I/Q Modulator for direct conversion method, and is used for up to 1.0 GHz band digital cellular phones such as GSM, PDC and so on.
MB54608L consists of a frequency doubler, a Flip-flop type quadrature phase shifter, I/Q modulator and a mixer for frequency offset with a separate power supply.
Fujitsu's advanced Bipolar process has realized very low current operation(lcc $=16.5 \mathrm{~mA} @ 3 \mathrm{~V}$ ).
SSOP-20 and BCC-16 package are available.(MB54608L: SSOP-20, MB54608B: BCC-16)

## ■ FEATURES

- Supporting GSM frequency band

Output frequency: 1.0 GHz (max.) Output power: -4 dBm (Typical, $\mathrm{V}_{\mathrm{BB}}=1.0 \mathrm{Vp}-\mathrm{p}$ input)

- Low voltage operation: $\mathrm{Vcc}=2.6 \mathrm{~V}$ to 3.8 V
(Continued)

PACKAGES
20-pin plastic SSOP

## MB54608L/MB54608B

(Continued)

- Low current: Please refer to below table.
- Offset mixer on-chip: The separate power supply control is possible. (only MB54608L)
- Output power level switch (Mode) enables high power mode

|  | Offset Mixer not used |  | Offset Mixer used |  |  |
| :--- | :--- | :---: | :---: | :---: | :---: |
|  | Mode = Open | Mode = GND | Mode = Open | Mode = GND |  |
| Normal <br> operation | Icc | 16.5 mA | 19.0 mA | 22.5 mA | 25.0 mA |
|  | Pout(VBB=1.0 Vp-p) | -4 dBm | -2 dBm | -4 dBm | -2 dBm |
| Power down | Ips | 0.22 mA |  | 0.44 mA |  |

Note:Typical values
Further increase of the output power is possible by attaching a resistor at Pcnt pin.

- Operating temperature range: $\mathrm{Ta}=-20$ to $+75^{\circ} \mathrm{C}$


## PIN ASSIGNMENTS

- MB54608L (SSOP-20)

(FPT-20P-M03)
- MB54608B (BBC-16)

(LCC-16P-M04)

Note: MB54608B doesn't have the off-set Mixer and Pcnt pin.

## MB54608L/MB54608B

PIN DESCRIPTIONS

- MB54608L (SSOP-20)

| Pin no. | Symbol | I/O | Descriptions |
| :---: | :---: | :---: | :--- |
| 1 | RFin | I | RF input for the offset mixer. When the offset mixer is not used, this pin <br> should be opened. |
| 2 | GND | - | Ground. |
| 3 | IFin | I | IF input for the offset mixer. When the offset mixer is not used, this pin <br> should be opened. |
| 4 | VccM | - | Power supply for the offset mixer. Power-on/off is possible indepently to the <br> modulator. When the offset mixer is not used, this pin should be switched <br> OFF. |
| 5 | Lo | I/O | Lo input for the I/Q modulator (Output for the offset mixer.) |$|$| 6 | Lox | I/O | Lo complementary input for the I/Q modulator (Complementary output for <br> the offset mixer.) |
| :---: | :---: | :--- | :--- |
| 7 | Vcc | - | Power supply for the I/Q modulator. |
| 8 | GND | - | Ground. |
| 10 | GND | - | Ground. |
| 11 | GND | - | Ground. |
| 13 | XQ | I | Q signal input for the I/Q modulator. |
| 14 | XI | I | Q signal complementary input for the I/Q modulator. |
| 15 | Mode | - | I |
| I signal input for the I/Q modulator. |  |  |  |
| 16 | Vcc | - | Output mode switch. Mode $=$ Open: Low power mode. <br> This pin should be connected to gr: High power mode. <br> Ther left open. |
| 17 | PSM | I | Power supply for the I/Q modulator. <br> Power saving control for the offset mixer. When PSM = L(GND), power <br> down mode is selected. When the offset mixer is not used, this pin should <br> be connected to Vcc voltage level or ground. |
| 18 | Out | O | Output for the I/Q modulator. (Open collector) <br> Open is prohibited when power is supplied to Vcc pin. |
| 19 | PSQ | I | Power saving control for the I/Q modulator. When PSQ = L(GND), power <br> down mode is selected. |
| 20 | Pcnt | - | Further, increasing the output power level is possible by attaching a resistor <br> between Pcnt pin and ground externally. |

- MB54608B (BCC-16)

| Pin no. | Pin nane | I/O | Descriptions |
| :---: | :---: | :---: | :--- |
| 1 | XQ | I | Q signal complementary input for the I/Q modulator. |
| 2 | XI | I | I signal complementary input for the I/Q modulator. |
| 3 | I | I | I signal input for the I/Q modulator. |
| 4 | Mode | - | Output mode switch. Mode=Open: Low power mode. <br> Mode=GND: High power mode. <br> This pin should be connected to ground or left open. |
| 5 | Vcc | - | Power supply for the I/Q modulator. |
| 6 | Out | O | Output for the I/Q modulator. (Open collector) <br> Open is prohibited when power is supplied to Vcc pin. |
| 7 | PSQ | I | Power saving control for the I/Q modulator. When PSQ = L(GND), power <br> down mode is selected. |
| 8 | N.C. | - | No connection. |
| 9 | GND | - | Ground. |
| 10 | N.C. | - | No connection. |
| 11 | Lo | I/O | Lo input for the I/Q modulator (Output for the offset mixer.) |
| 12 | Lox | I/O | Lo complementary input for the I/Q modulator (Complementary output for <br> the offset mixer.) |
| 13 | Vcc | - | Power supply for the I/Q modulator. |
| 14 | GND | - | Ground. |
| 15 | GND | - | Ground. |
| 16 | Q | I | Q signal input for the I/Q modulator. |

## MB54608L/MB54608B

## BLOCK DIAGRAM



Note: MB54608B doesn't have VccM, PSM, RFin, IFin and Pcnt pins.

## ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Rating |  | Unit | Remarks |
| :--- | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Max. |  |  |
| Power supply voltage | $\mathrm{V}_{\mathrm{cc}}$ | -0.5 | +5.0 | V |  |
| Output voltage | $\mathrm{V}_{\mathrm{o}}$ | -0.5 | $\mathrm{~V}_{\mathrm{cc}}+0.5(<5.0)$ | V |  |
| Input voltage | $\mathrm{V}_{\mathrm{l}}$ | -0.5 | $\mathrm{~V}_{\mathrm{cc}}+0.5(<5.0)$ | V |  |
| Allowed voltage on the open <br> collector pin | Voc | $\mathrm{V} \mathrm{Vc}-0.3$ <br> $(-0.5)$ | $\mathrm{V}_{\mathrm{cc}}+0.3$ <br> $(5.0)$ | V | Out pin, <br> Open is prohibited. |
| Output current | lo | 0 | +10 | mA |  |
| Storage temperature | Tstg | -55 | +125 | ${ }^{\circ} \mathrm{C}$ |  |

WARNING: Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

## ■ RECOMMENDED OPERATING CONDITIONS

| Parameter | Symbol | Value |  |  | Unit | Remarks |
| :--- | :---: | :---: | :---: | :---: | :---: | :--- |
|  |  | Min. | Typ. | Max. |  |  |
| Power supply voltage | $\mathrm{V}_{\mathrm{cc}}$ | 2.6 | 3.0 | 3.8 | V |  |
| Input voltage | $\mathrm{V}_{\mathrm{c}}$ | GND | - | $\mathrm{V}_{\mathrm{cc}}$ | V |  |
| Allowed voltage on the open <br> collector pin | Voc | $\mathrm{V}_{\mathrm{cc}}-0.2$ | - | $\mathrm{V}_{\mathrm{cc}}+0.2$ | V | Out pin. <br> Open is prohibited. |
| Operating temperature | Ta | -20 | - | +75 | V | Ambient temperature. |

WARNING: The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.
Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.
No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their FUJITSU representatives beforehand.

## MB54608L/MB54608B

## ELECTRICAL CHARACTERISTICS

1. DC CHARACTERISTICS (MB54608L, MB54608B)

$$
\left(\mathrm{Ta}=+25^{\circ} \mathrm{C}, \mathrm{~V} \mathrm{Cc}=3.0 \mathrm{~V}\right)
$$

| Parameter | Symbol | Value |  |  | Unit | Remarks |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. |  |  |  |  |
| Power supply current | Icc |  |  |  |  | Offset mix. | Mode pin | DC. <br> No AC signal input. |
|  |  | 12.0 | 16.5 | 24.5 | mA | Not used | Open |  |
|  |  | 13.5 | 19.0 | 28.0 | mA | Not used | GND |  |
|  |  | 16.0 | 22.5 | 33.5 | mA | Used | Open |  |
|  |  | 18.0 | 25.0 | 37.0 | mA | Used | GND |  |
| Power down current | Ips | - | 220 | 310 | $\mu \mathrm{A}$ | Not used | Don't care |  |
|  |  | - | 440 | 620 | $\mu \mathrm{A}$ | Used |  |  |
| Power down pin input voltage | VIHps | $\mathrm{V} \mathrm{cc} \times 0.7$ | - | - | V |  |  |  |
|  | VILps | - | - | $\mathrm{Vcc} \times 0.3$ | V |  |  |  |
| Power down pin input current | IIHps | - | - | 5.0 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{H}}=\mathrm{V}_{c c}$ |  | PSM, PSQ Value |
|  | IILps | -50 | - | - | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{IL}}=\mathrm{GND}$ |  |  |
| Pcnt pin load resistance | Rcnt | 50 | - | - | $\Omega$ |  |  |  |

Note: MB54608B doesn't have Offset mix., so please refer to "offset mix. = Not used" column in regard to power supply current.

## 2. AC CHARACTERISTICS

- A case of the offset mixer is used. (only MB54608L)
$\left(\mathrm{Ta}=+25^{\circ} \mathrm{C}, \mathrm{Vcc}=3.0 \mathrm{~V}\right)$

| Parameter |  | Symbol | Value |  |  | Unit | Remarks |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. |  |  |  |
| Baseband input | Operating band |  | $\mathrm{f}_{\text {B }}$ | DC | - | 10 | MHz |  |  |
|  | Input amplitude | $\mathrm{V}_{\text {в }}$ | 0.3 | 1.0 | 1.3 | Vp-p | Single ended input |  |
|  | Offset voltage | Vos | 1.4 | 1.5 | 1.6 | V | External offset voltage |  |
|  | Offset current | los | - | 3.0 | 4.0 | $\mu \mathrm{A}$ |  |  |
| Offset Mixer | Operating band | frF | - | 1078.5 | 1100 | MHz |  |  |
|  |  | $\mathrm{fiF}_{\text {F }}$ | - | 130.5 | 500 | MHz |  |  |
|  |  | flo | 800 | 948 | 1000 | MHz | Output |  |
|  | Input power level | Prf | -15 | -10 | 0 | dBm |  |  |
|  |  | PIF | -15 | -10 | 0 | dBm |  |  |
| RF output | Operating band | fout | 800 | 948 | 1000 | MHz |  |  |
|  | SSB output power level | Pout | -8 | -4 | - | dBm | Mode pin is opened | $\mathrm{V}_{\mathrm{BB}}=1.0 \mathrm{Vp}-\mathrm{p}$ (single ended input) <br> $\mathrm{f}_{\text {RF }}=1078.5 \mathrm{MHz}$ <br> $P_{\text {RF }}=-10 \mathrm{dBm}$ <br> $\mathrm{f}_{\mathrm{F}}=130.5 \mathrm{MHz}$ <br> $P_{\mathrm{IF}}=-10 \mathrm{dBm}$ <br> fout $=948 \mathrm{MHz}$ |
|  |  |  | -6 | -2 | - | dBm | Mode pin is grounded |  |
| Modulation accuracy | Amplitude error | Aerr | - | 2.0 | 3.0 | \% | RMS value |  |
|  | Phase error | Perr | - | 1.5 | 2.0 | deg. | RMS value |  |
|  | Vector error | Verr | - | 3.0 | 4.0 | \% | RMS value |  |
| Carrier suppression |  | CS | - | -35 | -27 | dBc | External offset. No offset adjustment. |  |
| Image rejection |  | IR | - | -40 | -28 | dBc |  |  |
| Adjucent channel power |  | ACP | - | -65 | -60 | dB | $\Delta \mathrm{f}=50 \mathrm{kHz}$ |  |
| IF $\times 7$ spurious |  | IF $\times 7$ | - | -65 | -60 | dBc |  |  |

## MB54608L/MB54608B

- A case of the offset mixer is not used. (MB54608L, MB54608B)
$\left(\mathrm{Ta}=+25^{\circ} \mathrm{C}, \mathrm{V} \mathrm{cc}=3.0 \mathrm{~V}\right)$


Notes: • Spec. of MB54608B is identical with MB54608L.

- When the offset mixer is not used, using a differential balun is recommended for input. (When the balun is not used, the changing carrier suppression may happen depending on the timing of powering up.)


## APPLICATION EXAMPLE (MB54608L)

1) A case of the offset mixer is used.


## 2) A case of the offset mixer is not used.



Note: LDB25C201A0950: Murata Mfg. Co., Ltd.

## MB54608L/MB54608B

(Continued)
(MB54608L)


- ORDERING INFORMATION

| Part number | Package | Remarks |
| :--- | :---: | :---: |
| MB54608L PFV | 20-pin, Plastic SSOP <br> (FPT-20P-M03) | MB54608L |
| MB54608L PV1 | 16-pad, Plastic BCC <br> (LCC-16P-M04) | MB54608B |

## MB54608L/MB54608B

## PACKAGE DIMENSION


(Continued)

## MB54608L/MB54608B

(Continued)
16-pad Plastic BCC
(LCC-16P-M04)


## FUJITSU LIMITED

## For further information please contact:

## Japan

FUJITSU LIMITED
Corporate Global Business Support Division
Electronic Devices
KAWASAKI PLANT, 4-1-1, Kamikodanaka, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588, Japan
Tel: +81-44-754-3763
Fax: +81-44-754-3329
http://www.fujitsu.co.jp/

## North and South America

FUJITSU MICROELECTRONICS, INC. 3545 North First Street, San Jose, CA 95134-1804, USA
Tel: +1-408-922-9000
Fax: +1-408-922-9179
Customer Response Center
Mon. - Fri.: 7 am - 5 pm (PST)
Tel: +1-800-866-8608
Fax: +1-408-922-9179
http://www.fujitsumicro.com/

## Europe

FUJITSU MICROELECTRONICS EUROPE GmbH
Am Siebenstein 6-10, D-63303 Dreieich-Buchschlag, Germany
Tel: +49-6103-690-0
Fax: +49-6103-690-122
http://www.fujitsu-fme.com/
Asia Pacific
FUJITSU MICROELECTRONICS ASIA PTE LTD \#05-08, 151 Lorong Chuan, New Tech Park, Singapore 556741
Tel: +65-281-0770
Fax: +65-281-0220
http://www.fmap.com.sg/

All Rights Reserved.
The contents of this document are subject to change without notice.
Customers are advised to consult with FUJITSU sales representatives before ordering.

The information and circuit diagrams in this document are presented as examples of semiconductor device applications, and are not intended to be incorporated in devices for actual use. Also, FUJITSU is unable to assume responsibility for infringement of any patent rights or other rights of third parties arising from the use of this information or circuit diagrams.

The contents of this document may not be reproduced or copied without the permission of FUJITSU LIMITED.

FUJITSU semiconductor devices are intended for use in standard applications (computers, office automation and other office equipments, industrial, communications, and measurement equipments, personal or household devices, etc.). CAUTION:
Customers considering the use of our products in special applications where failure or abnormal operation may directly affect human lives or cause physical injury or property damage, or where extremely high levels of reliability are demanded (such as aerospace systems, atomic energy controls, sea floor repeaters, vehicle operating controls, medical devices for life support, etc.) are requested to consult with FUJITSU sales representatives before such use. The company will not be responsible for damages arising from such use without prior approval.

Any semiconductor devices have inherently a certain rate of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.

If any products described in this document represent goods or technologies subject to certain restrictions on export under the Foreign Exchange and Foreign Trade Control Law of Japan, the prior authorization by Japanese government should be required for export of those products from Japan.

F0001
© FUJITSU LIMITED Printed in Japan

