

NO.2846A

LB1731

High-Voltage, Current-Sink Output Driver

Overview

The LB1731 is a 4-channel high-voltage current sink output driver. Inputs are active-low CMOS/TTL logic-level, and outputs are high-voltage open-collector NPN Darlington pairs.

Each driver in the LB1731 sinks up to 1.5A and withstands collector voltages of up to 85V.

The LB1731 is available in a 16-pin DIP package.

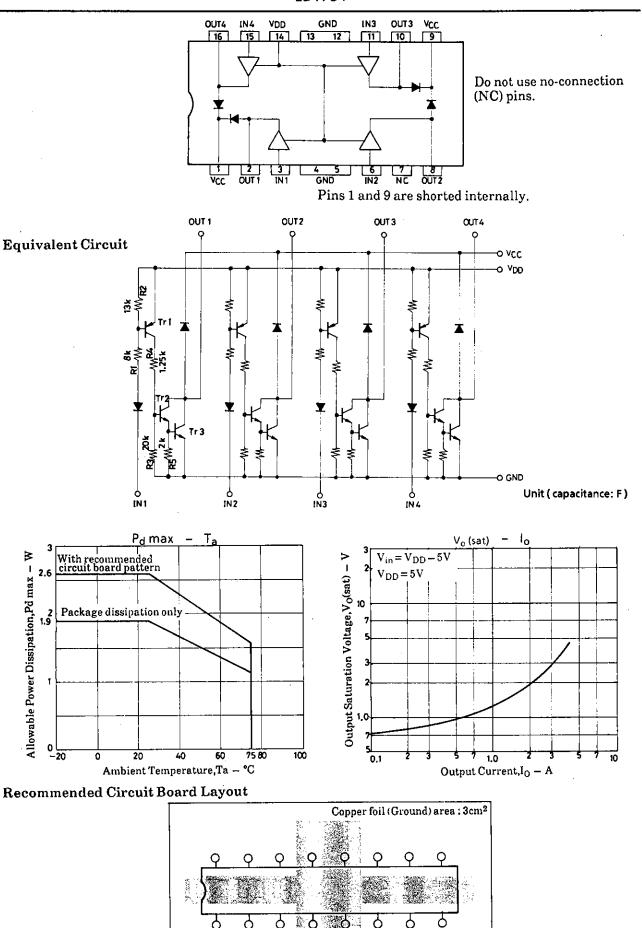
Features

- · Four independent high-voltage high-current drivers
- Output clamp diodes
- · Input protection diodes
- 5V CMOS- and TTL-compatible logic-level inputs

Absolute Maximum Rating at T				unit	
Maximum Supply Voltage	$V_{ m DD}$ max $V_{ m CC}$ max		7.0 82		
Applied Output Voltage	V _O max		85		
Applied Input Voltage	V _{IN} max	$V_{IN} \ge GND$	$V_{\rm DD} = 7.0 \text{ to } V_{\rm DD} + 10.0$		
Output Current	I _O max	441	1.5		
Clamp Diode Forward Current	-~		1.5		
Allowable Power Dissipation	Pd max	Package only	1.9	W	
		With recommende			
One noting Towns notions	M	circuit board patte		0.00	
Operating Temperature Storage Temperature	Topr Tstg		-20 to +75		
Diorage Temperature	1 Stg		-55 to +150	°C	
Allowable Operating Conditions at Ta=25°C unit					
Power Supply Voltage Range	$V_{ m DD}$		3.5 to 7.0		
Input ON-level Voltage	$ m V_{IN}$ on	$V_{IN} \ge GND, I_0 = 1.0A$	$V_{DD} - 7.0 \text{ to } V_{DD} - 2.6$		
Input OFF-level Voltage	$ m V_{IN}$ off	I _O ≦30μA	$V_{DD} = 0.3 \text{ to } V_{DD} + 10.0$	V	
Electrical Characteristics at $Ta = 25$ °C, $V_{DD} = 5.0$ V min typ					unit
Output Saturation Voltage		$V_{IN} = V_{DD} - 5.0V_{IO}$		p max	V
	-	$V_{IN} = V_{DD} - 5.0V, I_{O}$		1.5	v
	$ m V_{O}sat3$	$V_{IN} = V_{DD} - 5.0 V_{IO}$	=1.5A	2.0	V
Output Sustain Voltage	V_{O} sus	$I_0 = 100 \text{mA}$	85		v
Input Current	I_{IN}	$V_{DD} = 7.0 \text{V}, V_{IN} = V_D$	$_{\mathrm{D}}-7.0\mathrm{V}$	0.5	mΑ
Clamp Diode Forward Voltage	V_{FS}	$I_{FS} = 1.5A$		3.0	V
Clamp Diode Reverse Current	I_{RS}	$V_{\rm CC} = 82 \text{V}, V_{\rm O} = 0 \text{V}$	•	30	μA
Package Dimensions 3054A-D16FNIC 16					
(unit:mm)					
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Equivalent Circuit

Allowable Power Dissipation, Pd max - W



Circuit board (80×60mm)

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